

Features

- BVCEO > 80V
- Small Form Factor Thermally Efficient Package. Enables Higher Density End Products
- Ic = 1A Continuous Collector Current
- ICM = 2A Peak Pulse Current
- Low Saturation Voltage V_{CE(sat)} < 500mV @ 0.5A
- Complementary PNP Types: DXTP06080BFG
- Wettable Flank For Improved Optical Inspection
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please <u>contact us</u> or your local Diodes representative. <u>https://www.diodes.com/quality/product-definitions/</u>

Mechanical Data

- Case: PowerDI[®]3333-8
- Case Material: Molded Plastic. "Green" Molding Compound; UL Flammability Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Plated Leads Solderable per MIL-STD-202, Method 208 (3)
- Weight: 0.03 grams (Approximate)

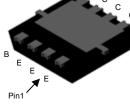
Applications

- Medium Power Switching
- Power Amplification
- AF Driver and Output Stages

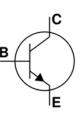
PowerDI3333-8 (SWP) (Type UX)



Top View



Bottom View



Device Symbol

Ordering Information (Note 4)

Part Number	Compliance	Marking	Reel Size (inches)	Tape Width (mm)	Quantity Per Reel
DXTN06080BFG-7	Standard	2L3	7	12	2,000

Notes:

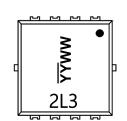
No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information

PowerDI3333-8 (SWP) (Type UX)



2L3= Product Type Marking Code YYWW = Date Code Marking YY = Last Two Digits of Year (ex: 21 = 2021) WW = Week Code (01 to 53)



Absolute Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Collector-Base Voltage	Vсво	100	V
Collector-Emitter Voltage	V _{CEO}	80	V
Emitter-Base Voltage	VEBO	7	V
Continuous Collector Current	lc	1	A
Peak Pulse Collector Current	Ісм	2	A
Continuous Base Current	IB	100	mA
Peak Pulse Base Current	I _{BM}	200	mA

Thermal Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	
Power Dissipation	(Note 5)	Da	1	W
	(Note 6)	PD	2.1	W
Thermal Resistance, Junction to Ambient	(Note 5)	Devi	128	°C/W
mermar Resistance, Junction to Ambient	(Note 6)	R _{0JA}	59	°C/W
Thermal Resistance, Junction to Leads (Note 7)		Rejl	10.7	°C/W
Operating and Storage Temperature Range		TJ, TSTG	-55 to +150	°C

ESD Ratings (Note 8)

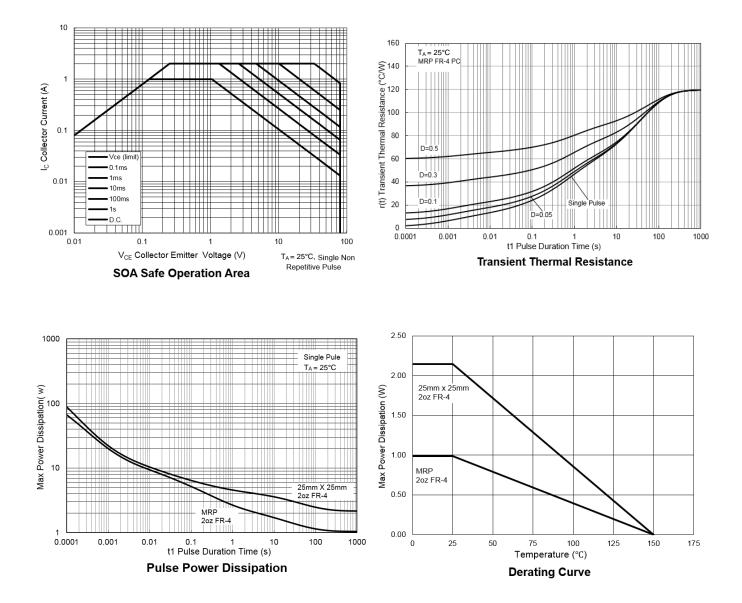
Notes:

Characteristic	Symbol	Value	Unit	JEDEC Class
Electrostatic Discharge - Human Body Model	ESD HBM	4,000	V	ЗA
Electrostatic Discharge - Machine Model	ESD MM	400	V	С

5. For a device mounted with the collector tab on MRP FR4-PCB; device is measured under still air conditions whilst operating in a steady-state. Same as Note 5, except the device is mounted on 25mm x 25mm 2oz copper.
Thermal resistance from junction to solder-point (at the collector tab).
Refer to JEDEC specification JESD22-A114 and JESD22-A115.



Thermal Characteristics and Derating Information





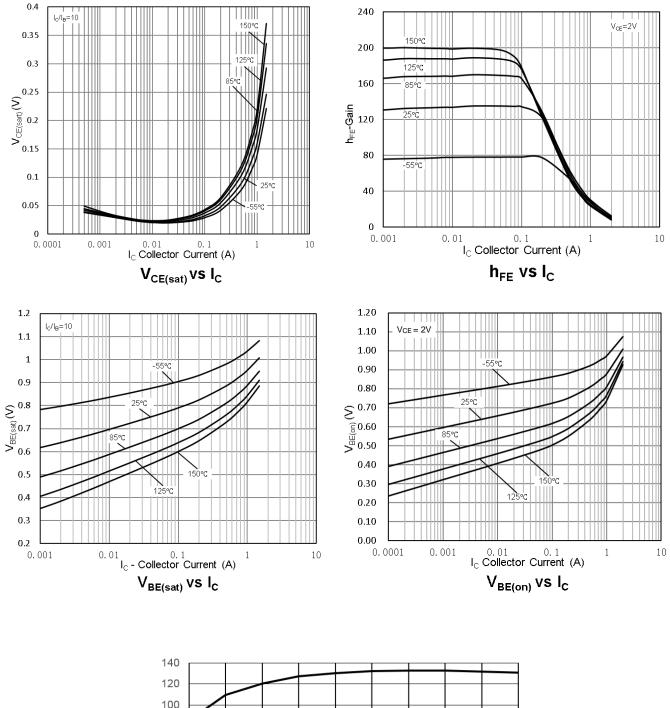
Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

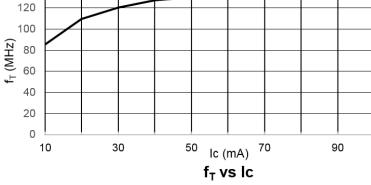
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
Collector-Base Breakdown Voltage	BV _{CBO}	100	300	_	V	I _C = 100μA
Collector-Emitter Breakdown Voltage (Note 9)	BV _{CEO}	80	139	_	v	I _c = 10mA
Emitter-Base Breakdown Voltage	BV _{EBO}	7	8.3	_	V	I _E = 100μA
Collector-Base Cut-Off Current	I _{CBO}	-	8 0.1	50 20	nA μA	V _{CB} = 100V V _{CB} = 80V, T _A = +150°C
Collector-Emitter Cut-Off Current	I _{CES}	_	6	20	nA	$V_{CE} = 80V$
Emitter Cut-Off Current	I _{EBO}	_	0.1	20	nA	$V_{EB} = 6V$
Static Forward Current Transfer Ratio (Note 9)	h _{FE}	25 100 40 25 —	129 127 60 36 28	 250 	_	$ \begin{split} &I_{C} = 5mA, V_{CE} = 2V \\ &I_{C} = 150mA, V_{CE} = 2V \\ &I_{C} = 500mA, V_{CE} = 2V \\ &I_{C} = 800mA, V_{CE} = 2V \\ &I_{C} = 1A, V_{CE} = 2V \end{split} $
Collector-Emitter Saturation Voltage (Note 9)	V _{CE(sat)}	—	91 135	250 500	mV	$I_{C} = 500$ mA, $I_{B} = 50$ mA $I_{C} = 800$ mA, $I_{B} = 50$ mA
Base-Emitter Turn-On Voltage (Note 9)	V _{BE(on)}	_	0.821	1	V	$I_{C} = 500 \text{mA}, V_{CE} = 2 \text{V}$
Input Capacitance	Cibo		160	_	pF	$V_{EB} = 0.5V. f = 1MHz$
Output Capacitance	C _{obo}		11		pF	$V_{CB} = 10V. f = 1MHz$
Transition Frequency	f _T	_	130	_	MHz	$I_{C} = 50 \text{mA}, V_{CE} = 10 \text{V}$ f = 100MHz
	t _{delay}	_	8.6		ns	
Switching Characteristics	t _{rise}		3.8	_	ns	$V_{CC} = -10V, I_{C} = 500mA$
	t _{storage}		491	_	ns	$I_{B1} = -I_{B2} = 50 \text{mA}$
	t _{fall}	_	171	_	ns	

Note: 9. Measured under pulsed conditions. Pulse width \leq 300µs. Duty cycle \leq 2%.



Typical Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

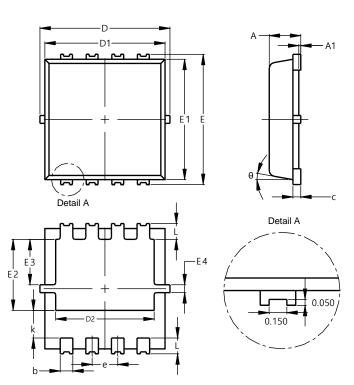






Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.



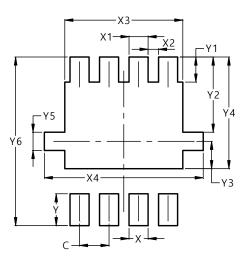
PowerDI3333-8 (SWP) (Type UX)

PowerDI3333-8 (SWP)						
(Type UX)						
Dim	Min	Max	Тур			
Α	0.75	0.85	0.80			
A1	0.00	0.05				
b	0.25	0.40	0.32			
C	0.10	0.25	0.15			
D	3.20	3.40	3.30			
D1	2.95	3.15	3.05			
D2	2.30	2.70	2.50			
Е	3.20	3.40	3.30			
E1	2.95	3.15	3.05			
E2	1.60	2.00	1.80			
E3	0.95	1.35	1.15			
E4	0.10	0.30	0.20			
е	_	_	0.65			
k	0.50	0.90	0.70			
L	0.30	0.50	0.40			
θ	0°	12°	10°			
All I	All Dimensions in mm					

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

PowerDI3333-8 (SWP) (Type UX)



Dimensions	Value (in mm)
С	0.650
Х	0.420
X1	0.420
X2	0.230
X3	2.600
X4	3.500
Y	0.700
Y1	0.550
Y2	1.650
Y3	0.600
Y4	2.450
Y5	0.400
Y6	3.700

Note: For high voltage applications, the appropriate industry sector guidelines should be considered with regards to creepage and clearance distances between device terminals and PCB tracking.



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