



DMWSH120H90SM4Q

1200V N-CHANNEL SILICON CARBIDE POWER MOSFET

Product Summary

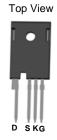
BV _{DSS}	RDS(ON) Max	I _D T _C = +25°C
1200V	97.5mΩ @V _{GS} = 15V	40A

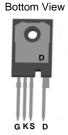
Description and Applications

This SiC MOSFET is designed to minimize the on-state resistance yet maintain superior switching performance, making it ideal for high-efficiency power-management applications.

- EV high-power DC-DC converters
- EV charging systems
- AC-DC traction inverters
- Automotive motor drivers

TO247-4 Standard





Pin Configuration

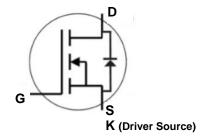
Features and Benefits

- Low On-Resistance
- High BV_{DSS} Rating for Power Application
- Low Input Capacitance
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- The DMWSH120H90SM4Q is suitable for automotive applications requiring specific change control; this part is AEC-Q101 qualified, PPAP capable, and manufactured in IATF 16949 certified facilities.

https://www.diodes.com/quality/product-definitions/

Mechanical Data

- Package: TO247-4
- Package Material: Molded Plastic, "Green" Molding Compound.
 UL Flammability Classification Rating 94V-0
- Terminal Connections: See Diagram
- Terminals: Finish Matte Tin Annealed over Copper Leadframe.
 Solderable per MIL-STD-202, Method 208 (23)
- Weight: 6.6 grams (Approximate)



Internal Schematic

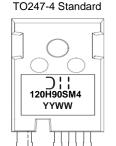
Ordering Information (Note 4)

Part Number	Backago	Packing		
Part Number	Package	Carrier		
DMWSH120H90SM4Q	TO247-4 Standard	30 Pieces	Tube	

Notes:

- 1. EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. All applicable RoHS exemptions applied.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information



DII = Manufacturer's Marking
120H90SM4 = Product Type Marking Code
YYWW or YYWW = Date Code Marking
YY or YY = Last Two Digits of Year (ex: 24 = 2024)
WW or WW = Week Code (01 to 53)



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Drain-Source Voltage		V _{DSS}	1200	V
Gate-Source Voltage (Dynamic)		Vgss	+19/-8	V
Gate-Source Voltage (Static)		Vgss	+15/-4	V
Continuous Drain Current (Notes 5, 9)	$T_C = +25$ °C $T_C = +100$ °C	I _D	40.0 28.3	А
Continuous Diode Forward Current (Note 5)	Is	43	А	
Pulsed Source Current (Pulse Width tp Limited by T _{J Max}) (Note 5)		Ism	88	А
Pulsed Drain Current (Pulse Width t _P Limited by T _{J Max}) (Note 5)		I _{DM}	88	А

Thermal Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit		
Total Power Dissipation (Note 5)	Tc = +25°C	Do	235	W	
Total Power Dissipation (Note 5)	Tc = +100°C	PD	117	VV	
Thermal Resistance, Junction to Ambient (Note 6)	Reja	30	°C/W		
Thermal Resistance, Junction to Case (Note 5)	Rejc	0.64	*C/VV		
Operating and Storage Temperature Range		TJ, TSTG	-55 to +175	°C	

Electrical Characteristics (@TA = +25°C, unless otherwise specified.)

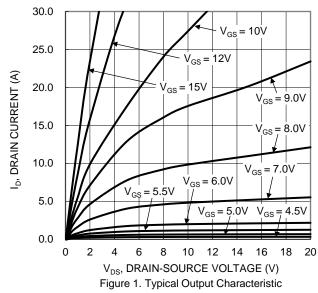
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 8)							
Drain-Source Breakdown Voltage	BV _{DSS}	1200	_	_	V	$V_{GS} = 0$, $I_{D} = 100 \mu A$	
Zero Gate Voltage Drain Current	I _{DSS}	_	_	100	μA	V _{DS} = 1200V, V _{GS} = 0	
Gate-Source Leakage	Igss	_	_	±200	nA	$V_{GS} = +15/-4V, V_{DS} = 0$	
ON CHARACTERISTICS (Note 8)							
Gate Threshold Voltage	Vgs(TH)	1.7	2.5	3.5	V	$V_{DS} = V_{GS}$, $I_D = 5mA$	
Static Drain-Source On-Resistance	R _{DS(ON)}	_	75	97.5	mΩ	$V_{GS} = 15V, I_D = 20A$	
Diode Forward Voltage	VsD	_	4.3		V	VGS = -4V, IS = 10A	
Transconductance	gfs	_	4.7	_	S	VDS = 20V, ID = 20A	
DYNAMIC CHARACTERISTICS (Note 7)							
Input Capacitance	Ciss	_	1112	_			
Output Capacitance	Coss	_	64	_	pF	$V_{GS} = 0$, $V_{DS} = 1000V$,	
Reverse Transfer Capacitance	Crss	_	4.42	_		V _{AC} = 25mV, f = 1MHz	
Coss Stored Energy	Eoss	_	39.5	_	μJ		
Turn-On Switching Energy (Body Diode Forward)	Eon	_	172	_	μJ	$V_{GS} = -4V/+15V$, $V_{DS} = 800V$, $Rg = 5\Omega$, $I_D = 20A$, $L = 156\mu H$	
Turn-Off Switching Energy (Body Diode Forward)	Eoff	_	71	_	μυ		
Gate Resistance	Rg	_	2.5	_	Ω	V _{AC} = 100mV, f = 1MHz	
Total Gate Charge	Qg	_	51.1	_		V _{GS} = -4V/+15V, V _{DS} = 800V,	
Gate-Source Charge	Qgs	_	15.9	_	nC		
Gate-Drain Charge	Qgd	_	18.8	_		ID = 20A	
Turn-On Delay Time	td(ON)	_	9.1	_		$V_{GS} = -4V/+15V$, $V_{DD} = 800V$, $Rg = 5\Omega$, $I_{D} = 20A$, Inductive Load	
Turn-On Rise Time	t _R	_	21.3	_			
Turn-Off Delay Time	tD(OFF)	_	17.4	_	ns		
Turn-Off Fall Time	tF	_	6.4	_			
Body Diode Reverse-Recovery Time	t _{RR}	_	12.1	_	ns	V 4V V 200V	
Body Diode Reverse-Recovery Charge	Q _{RR}	_	145	_	nC	$V_{GS} = -4V$, $V_{DS} = 800V$,	
Body Diode Reverse-Recovery Current	IRRM		20.7	_	Α	IF = 20A, di/dt = 3600A/µs	

Notes:

- 5. Device mounted on an infinite heatsink.
- 6. Device mounted on FR-4 substrate PC board, 2oz. copper, with minimum recommended pad layout.
- Guaranteed by design. Not subject to production testing.
 Short duration pulse test used to minimize self-heating effect.
- 9. Drain current limited by maximum junction temperature.

DMWSH120H90SM4Q





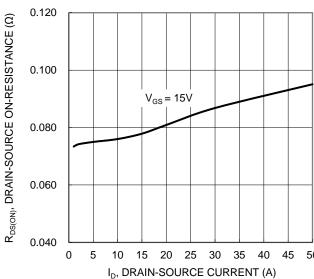


Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage

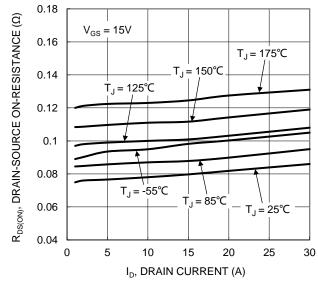


Figure 5. Typical On-Resistance vs. Drain Current and Temperature

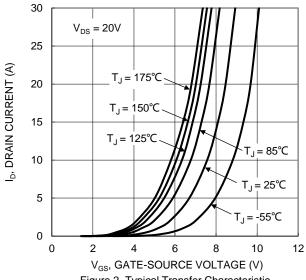


Figure 2. Typical Transfer Characteristic

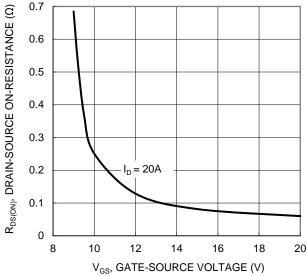


Figure 4. Typical Transfer Characteristic

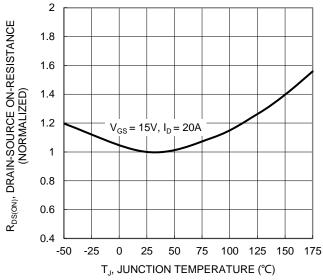


Figure 6. On-Resistance Variation with Temperature





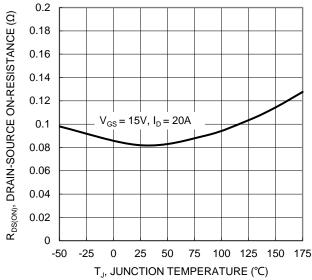


Figure 7. On-Resistance Variation with Temperature

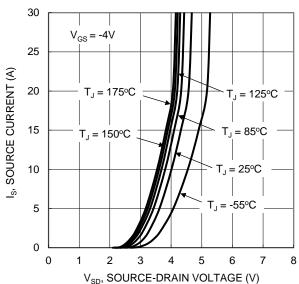


Figure 9. Diode Forward Voltage vs. Current

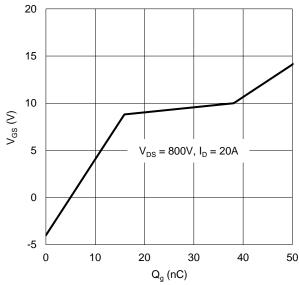


Figure 11. Gate Charge

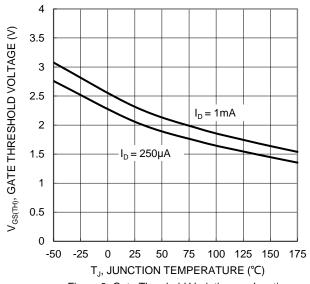


Figure 8. Gate Threshold Variation vs. Junction Temperature

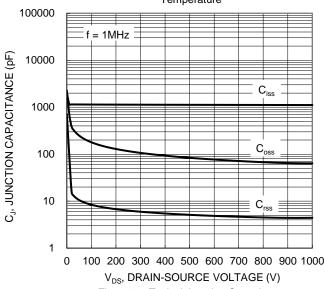


Figure 10. Typical Junction Capacitance

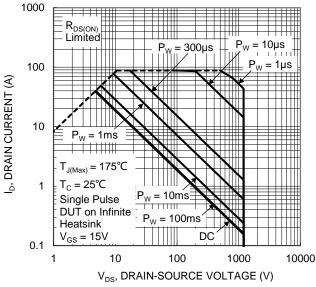


Figure 12. SOA, Safe Operation Area



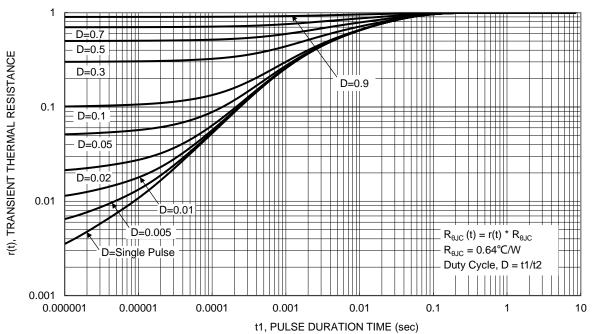


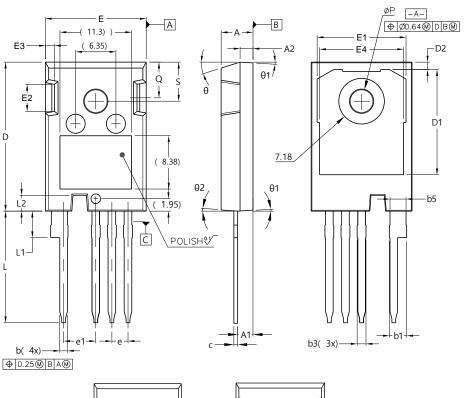
Figure 13. Transient Thermal Resistance



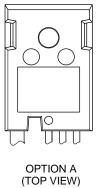
Package Outline Dimensions

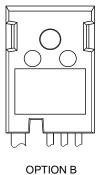
Please see http://www.diodes.com/package-outlines.html for the latest version.

TO247-4 Standard



TO247-4 Standard					
Dim	Min	Max			
Α	4.83	5.21			
A1	2.29	2.54			
A2	1.91	2.16			
b	1.07	1.33			
b1	2.39	2.94			
b3	1.07	1.60			
b5	2.39	2.69			
С	0.55	0.68			
D	23.30	23.60			
D1	16.25	17.65			
D2	0.95	1.25			
Е	15.75	16.30			
E1	13.10	14.15			
E2	3.68	5.10			
E3	1.00	1.90			
E4	12.38	13.43			
е	2.54 BSC				
e1		BSC			
L	17.31	17.82			
L1	3.97	4.37			
L2	2.35	2.65			
ØP	3.51	3.65			
Q	5.49	6.00			
S	6.04	6.30			
θ	17.5°- 20° REF				
θ1	3.5°- 5° REF				
θ2	4°- 5° REF				
All Dir	All Dimensions in mm				





(TOP VIEW)



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