



40V +175°C N-CHANNEL ENHANCEMENT MODE MOSFET PowerDI5060-8

Product Summary

BV _{DSS}	R _{DS(ON)} Max	I _D Max Tc = +25°C
40V	7.3mΩ @ V _{GS} = 10V	73.0A
407	$12m\Omega$ @ $V_{GS} = 4.5V$	58.0A

Features

- Rated to +175°C Ideal for High Ambient Temperature Environments
- 100% Unclamped Inductive Switching (UIS) Test in Production Ensures More Reliable and Robust End Application
- High Conversion Efficiency
- Low R_{DS(ON)} Minimizes Power Losses
- Wettable Flank for Improved Optical Inspection
- Fast Switching Speed
- Low Input Capacitance
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/104/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please contact us or your local Diodes representative. https://www.diodes.com/quality/product-definitions/
- An Automotive-Compliant Part is Available Under Separate Datasheet (DMTH47M2LPSWQ)

Description and Applications

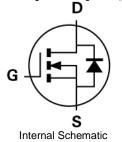
This MOSFET is designed to minimize the on-state resistance (RDS(ON)) yet maintain superior switching performance, making it ideal for high efficiency power management applications.

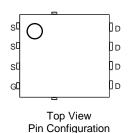
- High frequency switching
- Synchronous rectifications
- DC-DC converters

Mechanical Data

- Package: PowerDI[®]5060-8
- Package Material: Molded Plastic, "Green" Molding Compound.
 UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Finish Matte Tin Annealed over Copper Leadframe.
 Solderable per MIL-STD-202, Method 208 3
- Weight: 0.097 grams (Approximate)







Ordering Information (Note 4)

Part Number	Package	Packing		
Fait Number	Package	Qty.	Carrier	
DMTH47M2LPSW-13	PowerDI5060-8 (SWP) (Type UX)	2500	Tape & Reel	

Notes:

- 1. EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. All applicable RoHS exemptions applied.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information



⊃¦¦ = Manufacturer's Marking
 TH47M2LS = Product Type Marking Code
 YY
 YY
 WW = Date Code Marking
 YY
 = Year (ex: 22 = 2022)
 WW = Week (01 to 53)

PowerDI is a registered trademark of Diodes Incorporated.



Maximum Ratings (@ $T_A = +25^{\circ}C$, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	
Drain-Source Voltage		VDSS	40	V
Gate-Source Voltage		V_{GSS}	±20	V
Continuous Drain Current (Note 6) $ T_C = +25^{\circ}C $ $T_C = +100^{\circ}C $		ΙD	73.0 51.0	А
Maximum Continuous Body Diode Forward Current (Note 6)	Is	73.0	А	
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)		lом	292	Α
Pulsed Body Diode Forward Current (10µs Pulse, Duty Cycle = 1%)		Ism	292	А
Avalanche Current, L = 0.1mH		las	22.1	Α
Avalanche Energy, L = 0.1mH		Eas	24.4	mJ

Thermal Characteristics

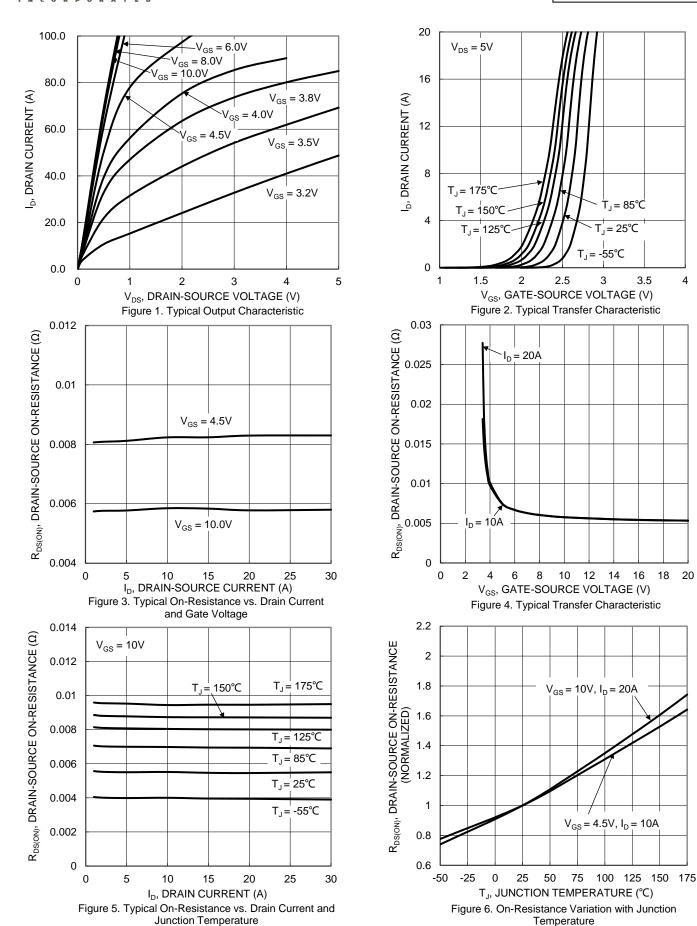
Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 5)	T _A = +25°C	PD	3.8	W
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	Reja	39.4	°C/W
Total Power Dissipation (Note 6)	Tc = +25°C	PD	68	W
Thermal Resistance, Junction to Case (Note 6)	Rejc	2.2	°C/W	
Operating and Storage Temperature Range	TJ, TSTG	-55 to +175	°C	

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

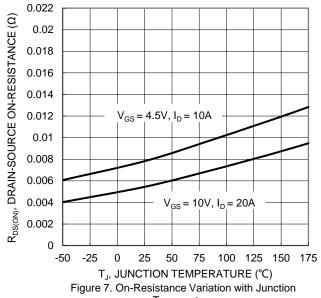
Characteristic	Symbol	Min	Tyn	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)	Syllibol	IVIIII	Тур	IVIAX	Onit	rest Condition	
Drain-Source Breakdown Voltage	BVpss	40	_	_	V	V _G S = 0V, I _D = 250µA	
Zero Gate Voltage Drain Current	Ipss	_	_	1	μA	Vps = 32V, Vgs = 0V	
Gate-Source Leakage	IGSS	_	_	±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 7)	1633			2100	10.	VGS - ±20V, VDS - 0V	
Gate Threshold Voltage	Vgs(TH)	1.2	_	2.3	V	$V_{DS} = V_{GS}$, $I_D = 250\mu A$	
	100(111)	_	5.7	7.3		Vgs = 10V, ID = 20A	
Static Drain-Source On-Resistance	RDS(ON)	_	8.1	12	mΩ	V _G S = 4.5V, I _D = 10A	
Diode Forward Voltage	V _{SD}	_	0.8	1.2	V	V _{GS} = 0V, I _S = 20A	
DYNAMIC CHARACTERISTICS (Note 8)							
Input Capacitance	Ciss	_	891	_		V _{DS} = 20V, V _{GS} = 0V, f = 1MHz	
Output Capacitance	Coss	_	490	_	pF		
Reverse Transfer Capacitance	Crss	_	14.8	_			
Gate Resistance	Rg	_	1.87	_	Ω	$V_{DS} = 0V$, $V_{GS} = 0V$, $f = 1MHz$	
Total Gate Charge (V _{GS} = 10V)	Qg	_	12.6	_		V _{DD} = 20V, I _D = 20A	
Total Gate Charge (V _{GS} = 4.5V)	Qg	_	5.9	_	nC		
Gate-Source Charge	Qgs	_	2.4	_	nc nc		
Gate-Drain Charge	Qgd	_	1.7	_			
Turn-On Delay Time	t _{D(ON)}	_	3.9	_		$V_{DD} = 20V, V_{GS} = 10V,$ $R_g = 3\Omega, I_D = 20A$	
Turn-On Rise Time	t _R	_	5.4	_			
Turn-Off Delay Time	t _{D(OFF)}	_	15.4	_	ns		
Turn-Off Fall Time	t _F	_	8.5	_			
Body Diode Reverse Recovery Time	trr	_	56.6	_	ns	1 200 4:/44 4000///	
Body Diode Reverse Recovery Charge	Q _{RR}	_	40.0	_	nC	$I_F = 20A$, di/dt = 100A/ μ s	

 Device mounted on FR-4 substrate PC board, 2oz copper, with thermal bias to bottom layer 1inch square copper plate.
 Thermal resistance from junction to soldering point (on the exposed drain pad).
 Short duration pulse test used to minimize self-heating effect.
 Guaranteed by design. Not subject to product testing. Notes:









Temperature

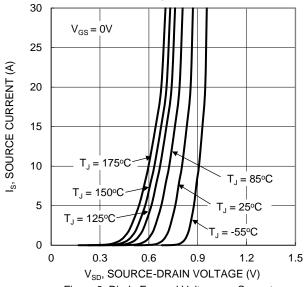


Figure 9. Diode Forward Voltage vs. Current

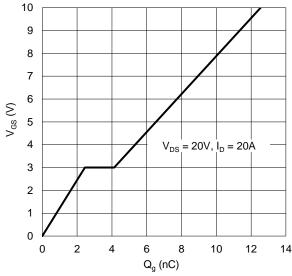


Figure 11. Gate Charge

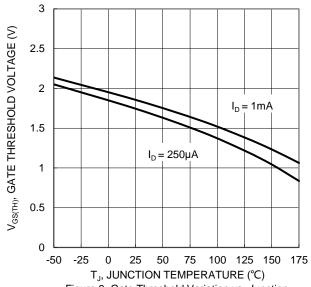
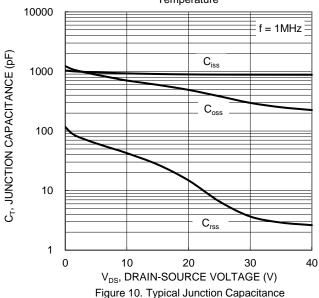


Figure 8. Gate Threshold Variation vs. Junction Temperature



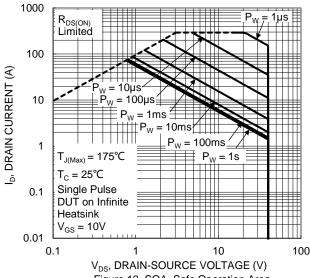


Figure 12. SOA, Safe Operation Area



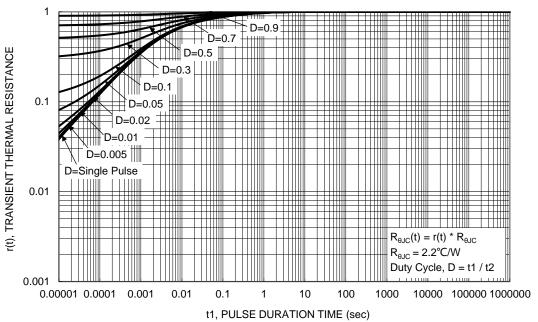


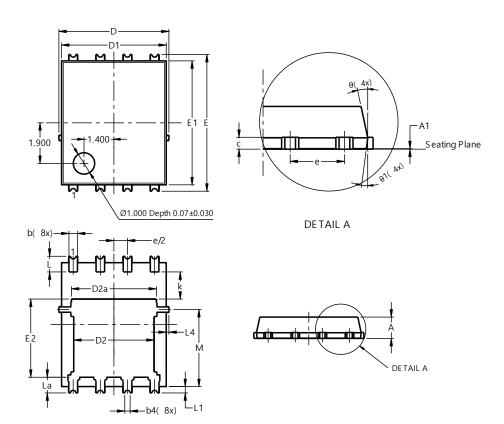
Figure 13. Transient Thermal Resistance



Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

PowerDI5060-8 (SWP) (Type UX)

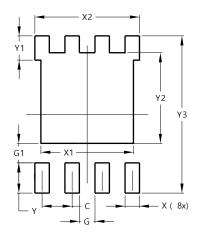


PowerDI5060-8 (SWP)				
(Type UX) ´				
Dim	Min	Max	Тур	
Α	0.90	1.10	1.00	
A1	0	0.05		
b	0.30	0.50	0.41	
b2	0.20	0.35	0.25	
b4	C).25REF		
С	0.230	0.330	0.277	
D		.15 BS0	2	
D1	4.70	5.10	4.90	
D2	3.56	3.96	3.76	
D2a	3.78	4.18	3.98	
E	6	.40 BS0		
E1	5.60	6.00	5.80	
E2	3.46	3.86	3.66	
E2a	4.195	4.595	4.395	
е	1	.27BSC		
k	1.05			
L	0.635	0.835	0.735	
La	0.635	0.835	0.735	
L1	0.200	0.400	0.300	
L1a	0.050REF			
L4	0.025	0.225	0.125	
М	3.205	4.005	3.605	
θ	10°	12°	11°	
θ1	6°	8°	7°	
All Dimensions in mm				

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

PowerDI5060-8 (SWP) (Type UX)



Dimensions	Value		
Dillielisions	(in mm)		
С	1.270		
G	0.660		
G1	0.820		
X	0.610		
X1	4.100		
X2	4.420		
Υ	1.270		
Y1	1.020		
Y2	3.810		
Y3	6.610		



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