



40V +175°C N-CHANNEL ENHANCEMENT MODE MOSFET PowerDI5060-8

Product Summary

BV _{DSS}	R _{DS(ON)} max	I _D T _C = +25°C
40V	1.8mΩ @ V _{GS} = 10V	210A

Description and Applications

This MOSFET is designed to meet the stringent requirements of automotive applications. It is qualified to AEC-Q101, supported by a PPAP and is ideal for use in:

- Engine Management Systems
- Body Control Electronics
- DC-DC Converters

Features

- Rated to +175°C Ideal for High Ambient Temperature Environments
- 100% Unclamped Inductive Switching (UIS) Test in Production Ensures More Reliable and Robust End Application
- Thermally Efficient Package-Cooler Running Applications
- High Conversion Efficiency
- Low R_{DS(ON)} Minimizes On State Losses
- <1.1mm Package Profile Ideal for Thin Applications
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- The DMTH41M8SPSQ is suitable for automotive applications requiring specific change control; this part is AEC-Q101 qualified, PPAP capable, and manufactured in IATF 16949 certified facilities.

https://www.diodes.com/quality/product-definitions/

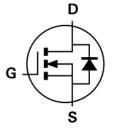
Mechanical Data

- Case: PowerDI® 5060-8
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Finish Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 (©3)
- Weight: 0.097 grams (Approximate)

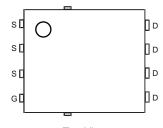


Top View

Bottom View



Internal Schematic



Top View Pin Configuration

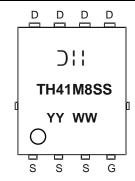
Ordering Information (Note 4)

Part Number	Case	Packaging
DMTH41M8SPSQ-13	PowerDI5060-8 (Type K)	2,500 / Tape & Reel

Notes:

- 1. EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. All applicable RoHS exemptions applied.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/

Marking Information



☐ H = Manufacturer's Marking
TH41M8SS = Product Type Marking Code
YYWW = Date Code Marking
YY = Last Two Digits of Year (ex: 20 = 2020)
WW = Week Code (01 to 53)



Maximum Ratings (@ $T_A = +25^{\circ}C$, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	
Drain-Source Voltage	VDSS	40	V	
Gate-Source Voltage	Vgss	±20	V	
Continuous Drain Current, V _{GS} = 10V (Note 6)	$T_{C} = +25^{\circ}C$ $T_{C} = +100^{\circ}C$	lo	210 150	Α
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)	I _{DM}	840	Α	
Continuous Body Diode Forward Current (Note 6) T _C = +25°C		Is	210	Α
Pulsed Body Diode Forward Current (10µs Pulse, Duty Cycle = 1%)	Ism	840	А	
Avalanche Current, L = 0.1mH	las	72.8	А	
Avalanche Energy, L = 0.1mH	E _{AS}	265	mJ	

Thermal Characteristics

Characteristic	Symbol	Value	Unit	
Total Power Dissipation (Note 5)	T _A = +25°C	PD	3.06	W
Thermal Resistance, Junction to Ambient (Note 5) Steady State		RθJA	49	°C/W
Total Power Dissipation (Note 6) $T_C = +25^{\circ}C$		PD	150	W
Thermal Resistance, Junction to Case (Note 6)	R _θ JC	1.0	°C/W	
Operating and Storage Temperature Range		TJ, TSTG	-55 to +175	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)							
Drain-Source Breakdown Voltage	BVDSS	40		_	V	$V_{GS} = 0V, I_{D} = 250\mu A$	
Zero Gate Voltage Drain Current	IDSS	_	_	1	μΑ	V _{DS} = 32V, V _{GS} = 0V	
Gate-Source Leakage	Igss		_	±100	nA	$V_{GS} = \pm 20V$, $V_{DS} = 0V$	
ON CHARACTERISTICS (Note 7)							
Gate Threshold Voltage	Vgs(TH)	2	_	4	V	$V_{DS} = V_{GS}$, $I_D = 250\mu A$	
Static Drain-Source On-Resistance	RDS(ON)	_	1.4	1.8	mΩ	$V_{GS} = 10V, I_{D} = 30A$	
Diode Forward Voltage	V_{SD}	_	0.8	1.2	V	$V_{GS} = 0V, I_{S} = 20A$	
DYNAMIC CHARACTERISTICS (Note 8)					•		
Input Capacitance	Ciss		6968	_		V _{DS} = 20V, V _{GS} = 0V, f = 1MHz	
Output Capacitance	Coss	_	1812	_	pF		
Reverse Transfer Capacitance	Crss	_	59	_			
Gate Resistance	Rg	_	1.21	_	Ω	$V_{DS} = 0V$, $V_{GS} = 0V$, $f = 1MHz$	
Total Gate Charge	Qg	_	79.5	_		V _{DD} = 20V, I _D = 90A, V _{GS} = 10V	
Gate-Source Charge	Qgs	_	20.6		nC		
Gate-Drain Charge	Qgd	_	16.5	_		VGS = 10V	
Turn-On Delay Time	t _D (ON)	_	13.3	_		$V_{DD} = 20V, V_{GS} = 10V,$ $I_{D} = 90A, R_{G} = 3.5\Omega$	
Turn-On Rise Time	t _R	_	41.3	_			
Turn-Off Delay Time	tD(OFF)	_	35.1	_	ns		
Turn-Off Fall Time	tF	_	13.7	_			
Reverse Recovery Time	trr	_	62	_	ns	I 504 di/dt - 1004/ug	
Reverse Recovery Charge	Q _{RR}	_	103	_	nC	I _F = 50A, di/dt = 100A/μs	

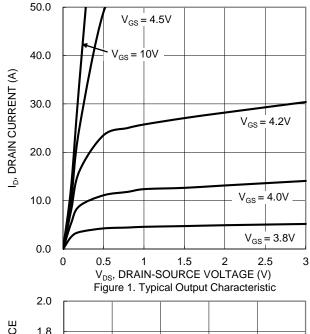
Notes: 5. Device mounted on FR-4 substrate PC board, 2oz copper, with thermal bias to bottom layer 1inch square copper plate.

^{6.} Thermal resistance from junction to soldering point (on the exposed drain pad).

^{7.} Short duration pulse test used to minimize self-heating effect.

^{8.} Guaranteed by design. Not subject to product testing.





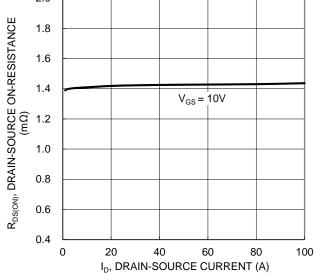


Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage

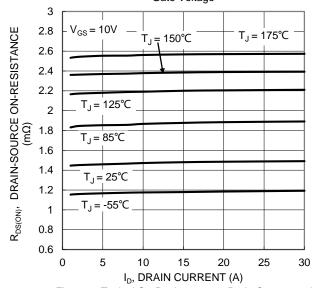


Figure 5. Typical On-Resistance vs. Drain Current and Temperature

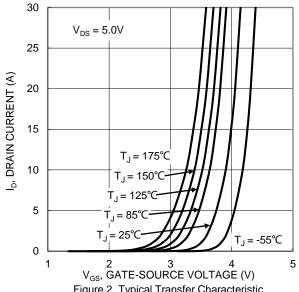


Figure 2. Typical Transfer Characteristic

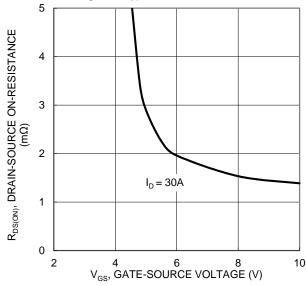


Figure 4. Typical Transfer Characteristic

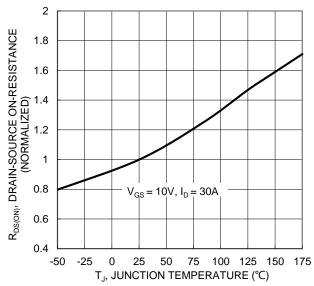


Figure 6. On-Resistance Variation with Temperature





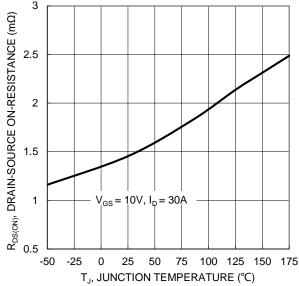


Figure 7. On-Resistance Variation with Temperature

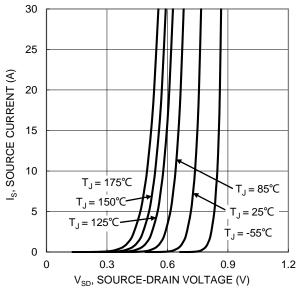
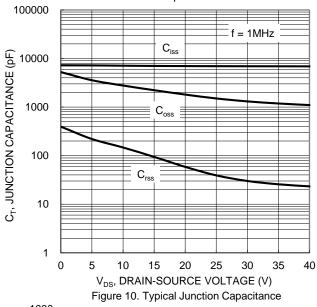


Figure 9. Diode Forward Voltage vs. Current 10 8 6 $V_{GS}(V)$ $V_{DS} = 20V, I_{D} = 90A$ 4 2 0 0 20 40 60 80 Q_q (nC) Figure 11. Gate Charge

4 $V_{GS(TH)}$, GATE THRESHOLD VOLTAGE (V) 3.5 3 $I_D = 1 mA$ 2.5 2 $I_{D} = 250 \mu A$ 1.5 1 0.5 0 -50 -25 25 50 75 100 125 150 175 T_J, JUNCTION TEMPERATURE (°C)

Figure 8. Gate Threshold Variation vs. Junction Temperature



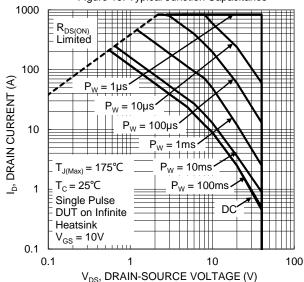
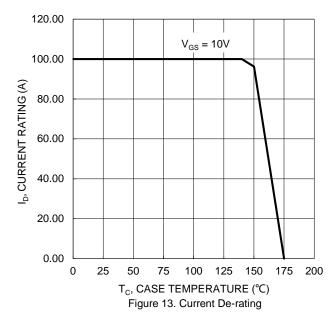


Figure 12. SOA, Safe Operation Area





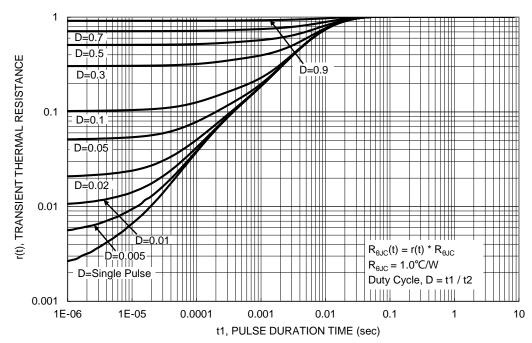


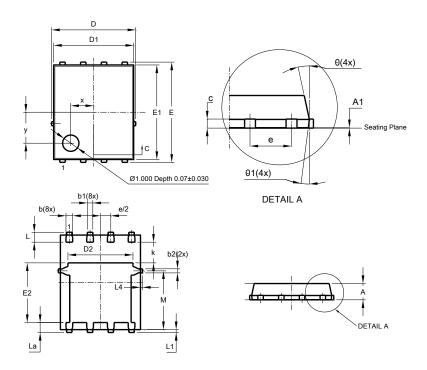
Figure 14. Transient Thermal Resistance



Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

PowerDI5060-8 (Type K)

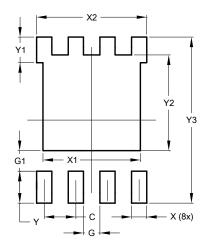


PowerDI5060-8 (Type K)				
Dim	Min	Max	Тур	
Α	0.90	1.10	1.00	
A1	0	0.05	0.02	
b	0.33	0.51	0.41	
b1	0.300	0.366	0.333	
b2	0.20	0.35	0.25	
C D	0.23	0.33	0.277	
	5	.15 BS0	2	
D1	4.85	4.95	4.90	
D2	-	-	3.98	
Е	6	.15 BS0	_	
E1	5.75	5.85	5.80	
E2	3.56	3.725	3.66	
е	1	.27BS0)	
k	-	-	1.27	
L	0.51	0.71	0.61	
La	0.51	0.675	0.61	
L1	0.05	0.20	0.175	
L4	-	-	0.125	
М	3.50	3.71	3.605	
Х	-	-	1.400	
у	-	-	1.900	
θ	10°	12°	11°	
θ1	6°	8°	7°	
All Dimensions in mm				

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

PowerDI5060-8 (Type K)



Dimensions	Value		
	(in mm)		
С	1.270		
G	0.660		
G1	0.820		
X	0.610		
X1	3.910		
X2	4.420		
Y	1.270		
Y1	1.020		
Y2	3.810		
Y3	6.610		



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