



40V 175°C N-CHANNEL ENHANCEMENT MODE MOSFET TO263AB

Product Summary

BV _{DSS}	R _{DS(ON)} Max	I _D Max T _C = +25°C
40V	3mΩ @ V _{GS} = 10V	192A

Description and Applications

This MOSFET is designed to minimize the on-state resistance (R_{DS(ON)}) yet maintain superior switching performance, making it ideal for high efficiency power management applications.

- Engine Management Systems
- Body Control Electronics
- DC-DC Converters

Features

- Rated to +175°C Ideal for High Ambient Temperature Environments
- 100% Unclamped Inductive Switching (UIS) Test in Production Ensures More Reliable and Robust End Application
- Low R_{DS(ON)} Minimizes Power Losses
- Low Q_g Minimizes Switching Losses
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e.: parts qualified to AEC-Q100/101/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please refer to the related automotive grade (Q-suffix) part. A listing can be found at

https://www.diodes.com/products/automotive/automotive-products/.

 This part is qualified to JEDEC standards (as references in AEC-Q) for High Reliability.

https://www.diodes.com/quality/product-definitions/

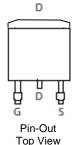
Mechanical Data

- Case: TO263AB
- Case Material: Molded Plastic, "Green" Molding Compound; UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Finish Matte Tin Annealed over Copper Leadframe.
 Solderable per MIL-STD-202, Method 208 (£3)
- Weight: 1.7 grams (Approximate)

TO263AB (D2PAK)



Top View



G T

Internal Schematic

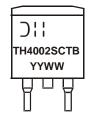
Ordering Information (Note 4)

Part Number	Case	Packaging
DMTH4002SCTB-13	TO263AB (D2PAK)	800 / Tape & Reel

Notes:

- 1. EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. All applicable RoHS exemptions applied.
- See http://www.diodes.com/quality/lead_free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information



⊃¦¦ = Manufacturer's Marking TH4002SCTB = Product Type Marking Code YYWW = Date Code Marking YY = Last Two Digits of Year (ex: 20 = 2020) WW = Week (01 to 53)



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	
Drain-Source Voltage	VDSS	40	V	
Gate-Source Voltage	Vgss	±20	V	
Continuous Drain Current (Note 6)	Tc = +25°C	ID	192	А
Continuous Diam Curient (Note 6)	T _C = +100°C		136	
Maximum Continuous Body Diode Forward Current (Note 6) $T_C = +25^{\circ}C$		Is	100	A
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)	l _{DM}	760	Α	
Pulsed Body Diode Forward Current (10µs Pulse, Duty Cycle = 1%	I _{SM}	760	A	
Avalanche Current, L = 3mH	las	19.2	Α	
Avalanche Energy, L = 3mH	Eas	551.8	mJ	

Thermal Characteristics

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 5)	T _A = +25°C	P_{D}	6	W
Thermal Resistance, Junction to Ambient (Note 5)		$R_{\theta JA}$	25	°C/W
Total Power Dissipation (Note 6)	T _C = +25°C	P _D	166.7	W
Thermal Resistance, Junction to Case (Note 6)		Rejc	0.9	°C/W
Operating and Storage Temperature Range		T _J , T _{STG}	-55 to +175	°C

Electrical Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)							
Drain-Source Breakdown Voltage	BV _{DSS}	40		1	V	VGS = 0V, ID = 1mA	
Zero Gate Voltage Drain Current	IDSS	_	_	1	μA V _{DS} = 32V, V _{GS} = 0V		
Gate-Source Leakage	I _{GSS}	_	_	±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 7)							
Gate Threshold Voltage	V _{GS(TH)}	2	1	4	V	$V_{DS} = V_{GS}$, $I_D = 250\mu A$	
Static Drain-Source On-Resistance	RDS(ON)		2.22	3	mΩ	$V_{GS} = 10V, I_{D} = 90A$	
Diode Forward Voltage	V_{SD}	_	0.8	1.2	V	$V_{GS} = 0V, I_{S} = 20A$	
DYNAMIC CHARACTERISTICS (Note 8)							
Input Capacitance	C _{iss}		7180	-		V _{DS} = 20V, V _{GS} = 0V, f = 1MHz	
Output Capacitance	Coss		1698	1	pF		
Reverse Transfer Capacitance	Crss	_	17	1			
Gate Resistance	R_g	_	1.04		Ω	$V_{DS} = 0V$, $V_{GS} = 0V$, $f = 1MHz$	
Total Gate Charge	Qg	_	77.5	1		V _{DD} = 20V, I _D = 90A, V _{GS} = 10V	
Gate-Source Charge	Qgs	_	23.6	1	nC		
Gate-Drain Charge	Q_{gd}	_	13.6	1			
Turn-On Delay Time	td(on)	_	16.8	_		$V_{DD} = 20V, V_{GS} = 10V,$ $I_{D} = 90A, R_{g} = 3.5\Omega$	
Turn-On Rise Time	t _R	_	8.0	1			
Turn-Off Delay Time	tD(OFF)	_	35.8	_	ns		
Turn-Off Fall Time	tF	_	11.6	_			
Reverse Recovery Time	t _{RR}	_	46.36	-	ns	I= 150 di/dt 1000/up	
Reverse Recovery Charge	Qrr	_	56.11	_	$\frac{1}{1}$ IF = 15A, di/dt = 100A/µs		

5. Device mounted on FR-4 substrate PC board, 2oz copper, with thermal bias to bottom layer 1inch square copper plate.

- 6. Thermal resistance from junction to soldering point (on the exposed drain pad).
- 7. Short duration pulse test used to minimize self-heating effect.
- 8. Guaranteed by design. Not subject to product testing.



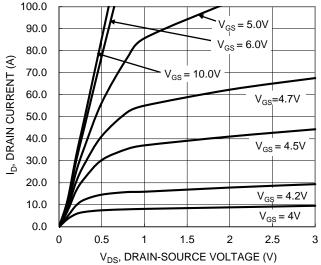


Figure 1. Typical Output Characteristic

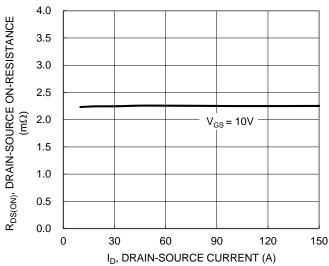


Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage

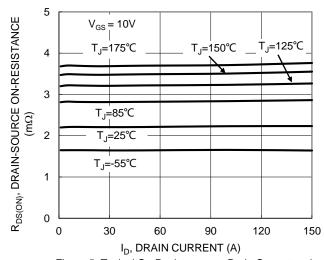


Figure 5. Typical On-Resistance vs. Drain Current and Temperature

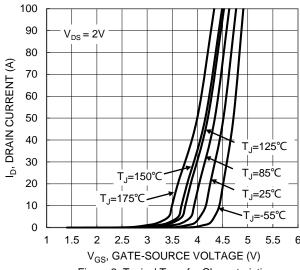


Figure 2. Typical Transfer Characteristic

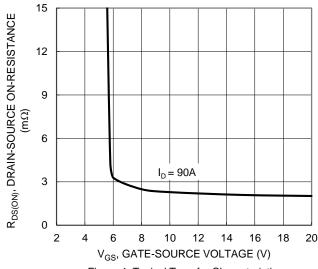


Figure 4. Typical Transfer Characteristic

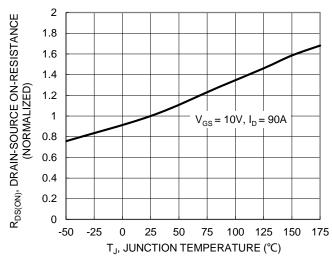


Figure 6. On-Resistance Variation with Temperature



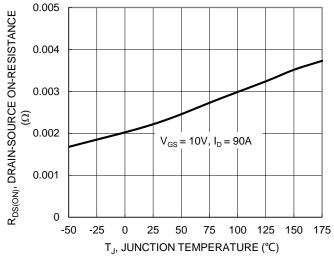


Figure 7. On-Resistance Variation with Temperature

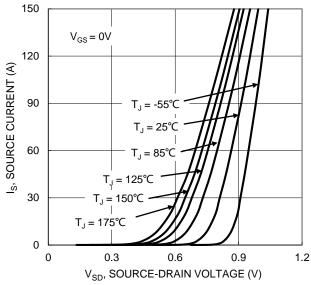
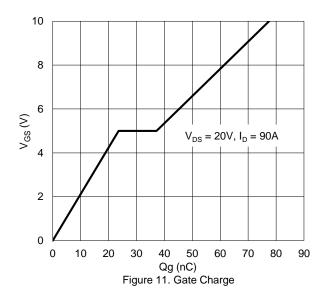


Figure 9. Diode Forward Voltage vs. Current



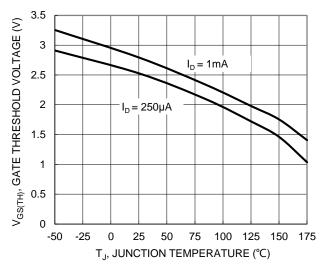
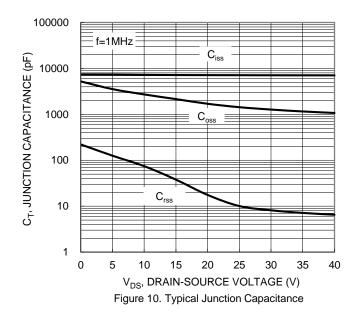
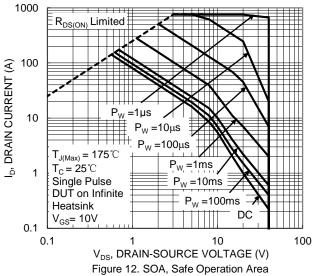


Figure 8. Gate Threshold Variation vs. Junction Temperature







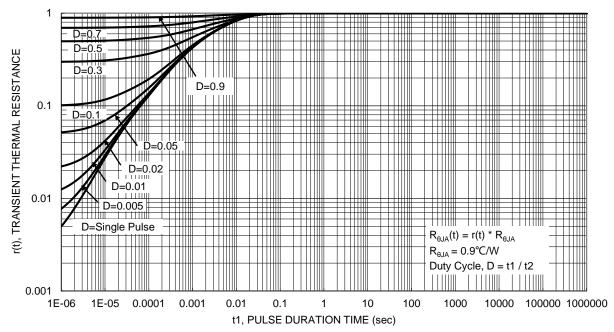


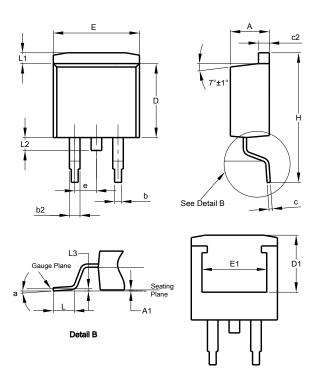
Figure 13. Transient Thermal Resistance



Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

TO263AB (D2PAK)

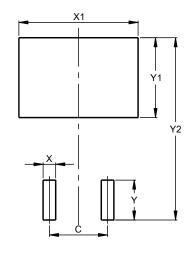


TO263AB (D2PAK)					
Dim	Min	Max	Тур		
Α	4.07	4.82	-		
A1	0.00	0.25	-		
b	0.51	0.99	-		
b2	1.15	1.77	-		
С	0.356	0.73	-		
c2	1.143	1.65	-		
D	8.39	9.65	-		
D1	6.55	6.95	-		
е	:	2.54 TYP			
Е	9.66	10.66	-		
E1	6.23	8.23	-		
Н	14.61	15.87	-		
L	1.78	2.79	-		
L1	-	1.67	-		
L2	-	1.77	-		
L3	-	-	0.254		
а	0°	8°	-		
All Dimensions in mm					

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

TO263AB (D2PAK)



Dimensions	Value (in mm)			
С	5.08			
Х	1.10			
X1	10.41			
Y	3.50			
Y1	7.01			
Y2	15.99			



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