





100V 175°C N-CHANNEL ENHANCEMENT MODE MOSFET PowerDI5060-8

Product Summary

BV _{DSS}	R _{DS(ON)} Max	I _D T _C = +25°C (Note 9)
100V	8.8mΩ @ V _{GS} = 10V	100A
1000	11.5mΩ @ V _{GS} = 6V	100A

Description

This new generation N-Channel Enhancement Mode MOSFET is designed to minimize R_{DS(ON)}, yet maintain superior switching performance. This device is ideal for use in Notebook battery power management and load switches.

Applications

- Motor controls
- DC-DC converters
- Power management

Features

- Rated to +175°C Ideal for High Ambient Temperature Environments
- 100% Unclamped Inductive Switching Ensures more reliable and robust end application
- Low R_{DS(ON)} Minimizes On-State Losses
- Fast Switching Speed
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen- and Antimony-Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e.: parts qualified to AEC-Q100/101/104/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please refer to the related automotive grade (Q-suffix) part. A listing can be found at

https://www.diodes.com/products/automotive/automotiveproducts/.

This part is qualified to JEDEC standards (as references in AEC-Q) for High Reliability.

https://www.diodes.com/quality/product-definitions/

Mechanical Data

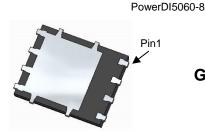
- Package: PowerDI[®]5060-8
- Package Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Diagram Below
- Terminal Finish Matte Tin Annealed over Copper Lead-Frame. Solderable per MIL-STD-202, Method 208 @3
- Weight: 0.097 grams (Approximate)



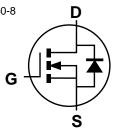


Top View

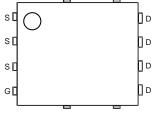
Site 2:



Bottom View



Internal Schematic



Top View Pin Configuration

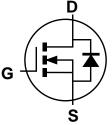
PowerDI5060-8 (SWP) (Type UX)



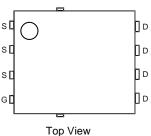
Top View



Bottom View



Internal Schematic



Pin Configuration

Notes:

- 1. EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. All applicable RoHS exemptions applied.
- 2. See http://www.diodes.com/quality/lead_free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

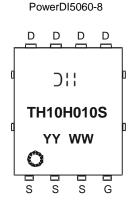


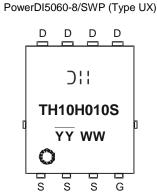
Ordering Information (Note 4)

Orderable Part Number	Package	Packing		
Orderable Fait Number	r ackage	Quantity	Carrier	
DMTH10H010SPS-13	PowerDI5060-8	2,500	Tape & Reel	
DMTH10H010SPS-13	PowerDI5060-8(SWP) (Type UX)	2,500	Tape & Reel	

4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information





YYWW or $\overline{YY}WW = Date Code Marking$ YY or \overline{YY} = Last Two Digits of Year (ex: 23 = 2023) WW = Week Code (01 to 53)

Maximum Ratings (@ T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value 100	Unit V	
Drain-Source Voltage	V_{DSS}			
Gate-Source Voltage		V _{GSS}	±20	V
Continuous Drain Current, V _{GS} = 10V (Note 5)	$T_A = +25^{\circ}C$ $T_A = +100^{\circ}C$	I _D	15 11	А
Continuous Drain Current, V _{GS} = 10V (Note 6)	$T_{C} = +25^{\circ}C$ (Note 9) $T_{C} = +100^{\circ}C$	I _D	100 87	А
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)	I _{DM}	250	Α	
Maximum Continuous Body Diode Forward Current	Is	100	Α	
Avalanche Current, L = 0.3mH	I _{AS}	25	Α	
Avalanche Energy, L = 0.3mH	E _{AS}	93.7	mJ	
Avalanche Current (Note 7), L = 3mH		I _{AS}	14.3	Α
Avalanche Energy (Note 7), L = 3mH		E _{AS}	307	mJ

Thermal Characteristics

Characteristic	-	Symbol	Value	Unit
Total Power Dissipation (Note 5)	$T_A = +25^{\circ}C$	P_{D}	3	W
Thermal Resistance, Junction to Ambient (Note 5)		R _{0JA}	49	°C/W
Total Power Dissipation (Note 6)	$T_C = +25^{\circ}C$	P _D	166	W
Thermal Resistance, Junction to Case (Note 6)		R _{θJC}	0.9	°C/W
Operating and Storage Temperature Range		T _{J,} T _{STG}	-55 to +175	°C

Notes:

- 5. Device mounted on FR-4 substrate PC board, 2oz copper, with thermal bias to bottom layer 1inch square copper plate.
- 6. Thermal resistance from junction to soldering point (on the exposed drain pad).
 7. Guaranteed by design. Not subject to product testing.
 8. Short duration pulse test used to minimize self-heating effect.
 9. Package limited.



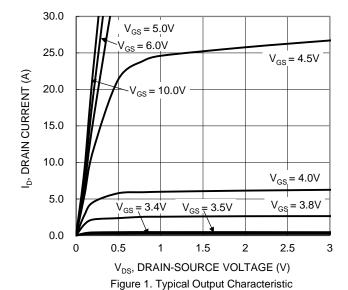
Electrical Characteristics (@ T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)							
Drain-Source Breakdown Voltage	BV _{DSS}	100			٧	$V_{GS} = 0V$, $I_D = 1mA$	
Zero Gate Voltage Drain Current	I _{DSS}		_	1	μΑ	$V_{DS} = 80V, V_{GS} = 0V$	
Gate-Source Leakage	Igss	_	_	±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 7)							
Gate Threshold Voltage	V _{GS(TH)}	2		4	V	$V_{DS} = V_{GS}$, $I_D = 250\mu A$	
Static Drain-Source On-Resistance			6.6	8.8	mΩ	$V_{GS} = 10V, I_D = 13A$	
Static Diain-Source On-Resistance	R _{DS(ON)}		8.5	11.5	11122	$V_{GS} = 6V, I_D = 13A$	
Diode Forward Voltage	V_{SD}	_	0.8	1.3	V	$V_{GS} = 0V, I_{S} = 13A$	
DYNAMIC CHARACTERISTICS (Note 8)							
Input Capacitance	C _{ISS}	_	4,468	_		V _{DS} = 50V, V _{GS} = 0V f = 1MHz	
Output Capacitance	Coss	_	746	_	pF		
Reverse Transfer Capacitance	C _{RSS}	_	32	_			
Gate Resistance	R_{G}	_	0.91	_	Ω	$V_{DS} = 0V$, $V_{GS} = 0V$, $f = 1MHz$	
Total Gate Charge	Q_G		56.4	_		V 50V L 40A	
Gate-Source Charge	Q _{GS}	_	15.4	_	nC	$V_{DD} = 50V, I_D = 13A,$ $V_{GS} = 10V$	
Gate-Drain Charge	Q_{GD}		14	_			
Turn-On Delay Time	t _{D(ON)}		18.6	_			
Turn-On Rise Time	t _R		22.5	_	ns	$V_{DD} = 50V, V_{GS} = 10V,$ $I_{D} = 13A, R_{g} = 6\Omega$	
Turn-Off Delay Time	t _{D(OFF)}	_	44.8	_			
Turn-Off Fall Time	t _F		29.5	_			
Reverse Recovery Time	t _{RR}		54.5	_	ns	1 424 4:/4+ 4004/	
Reverse Recovery Charge	Q _{RR}	_	106.4		nC	$I_F = 13A$, di/dt = 100A/µs	

7. Short duration pulse test used to minimize self-heating effect. 8. Guaranteed by design. Not subject to product testing. Notes:







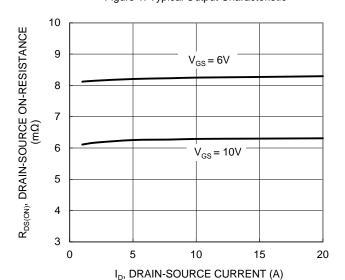


Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage

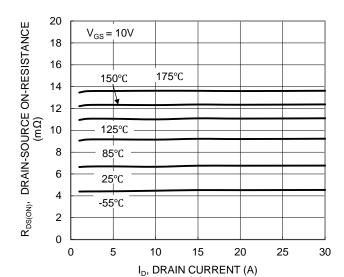


Figure 5. Typical On-Resistance vs. Drain Current and Junction Temperature

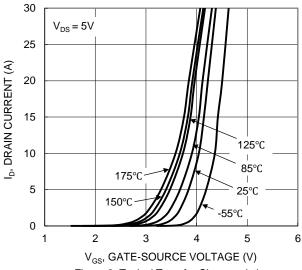
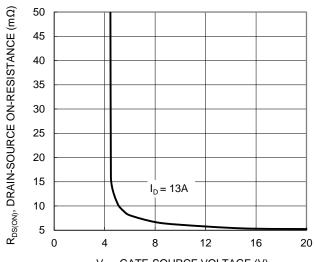


Figure 2. Typical Transfer Characteristic



 V_{GS} , GATE-SOURCE VOLTAGE (V) Figure 4. Typical Transfer Characteristic

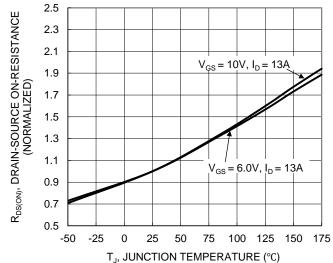


Figure 6. On-Resistance Variation with Junction Temperature





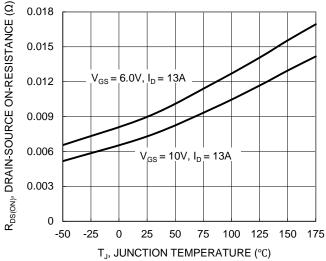
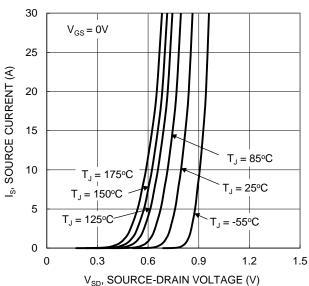


Figure 7. On-Resistance Variation with Junction Temperature



V_{SD}, SOURCE-DRAIN VOLTAGE (V) Figure 9. Diode Forward Voltage vs. Current

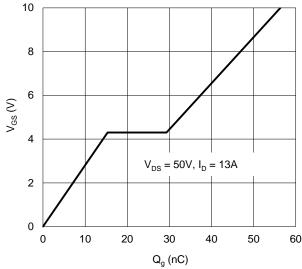


Figure 11. Gate Charge

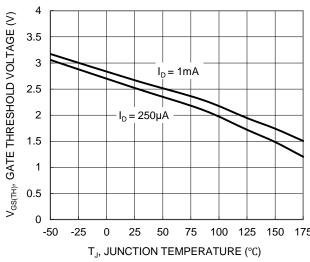


Figure 8. Gate Threshold Variation vs. Junction Temperature

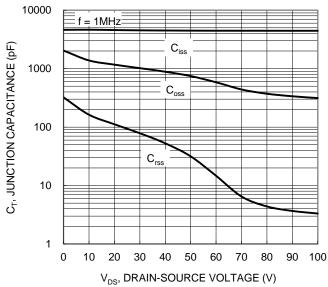


Figure 10. Typical Junction Capacitance

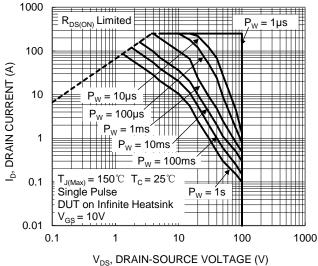


Figure 12. SOA, Safe Operation Area



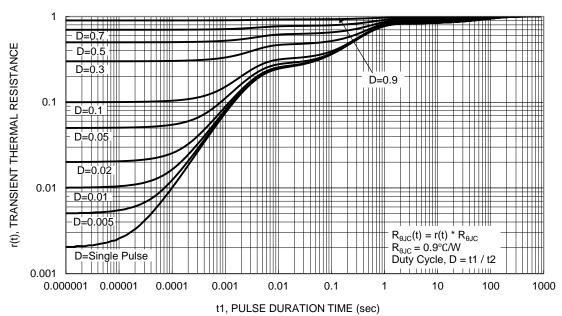


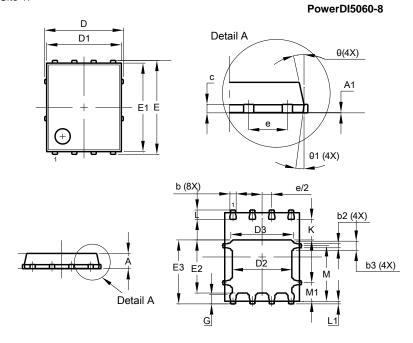
Figure 13. Transient Thermal Resistance



Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

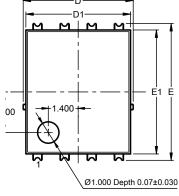
Site 1:

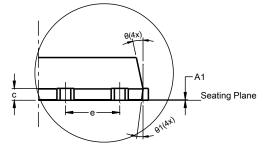


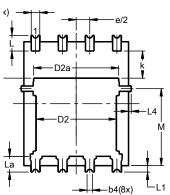
PowerDI5060-8				
Dim	Min	Max	Тур	
Α	0.90	1.10	1.00	
A1	0.00	0.05	-	
b	0.33	0.51	0.41	
b2	0.200	0.350	0.273	
b3	0.40	0.80	0.60	
С	0.230	0.330	0.277	
D		5.15 BSC	;	
D1	4.70	5.10	4.90	
D2	3.70	4.10	3.90	
D3	3.90	4.30	4.10	
Е	•	6.15 BSC	,	
E1	5.60	6.00	5.80	
E2	3.28	3.68	3.48	
E3	3.99	4.39	4.19	
е		1.27 BSC		
G	0.51	0.71	0.61	
K	0.51	-	-	
L	0.51	0.71	0.61	
L1	0.100	0.200	0.175	
M	3.235	4.035	3.635	
M1	1.00	1.40	1.21	
Θ	10°	12°	11°	
Θ1	6°	8°	7°	
All Dimensions in mm				

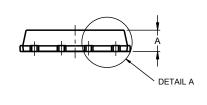
Site 2:











DETAIL A

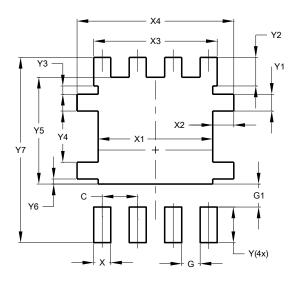
Dov	vorDIE0	20 0 (2)	MD)	
PowerDI5060-8 (SWP) (Type UX)				
Dim	Min	Max	Тур	
Α	0.90	1.10	1.00	
A1	0	0.05		
b	0.30	0.50	0.41	
b2	0.20	0.35	0.25	
b4	().25REF		
С	0.230	0.330	0.277	
D	5.15 BSC			
D1	4.70	5.10	4.90	
D2	3.56	3.96	3.76	
D2a	3.78	4.18	3.98	
E	6.40 BSC			
E1	5.60	6.00	5.80	
E2	3.46	3.86	3.66	
E2a	4.195	4.595	4.395	
е	1.27BSC			
k	1.05			
L	0.635	0.835	0.735	
La	0.635	0.835	0.735	
L1	0.200	0.400	0.300	
L1a	0.050REF			
L4	0.025	0.225	0.125	
М	3.205	4.005	3.605	
θ	10°	12°	11°	
θ1	6°	8°	7°	
All Dimensions in mm				



Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

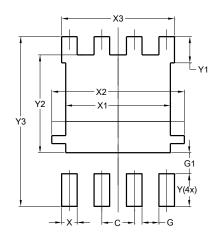
Site 1: PowerDI5060-8



Dimensions	Value (in mm)
С	1.270
G	0.660
G1	0.820
Х	0.610
X1	4.100
X2	0.755
Х3	4.420
X4	5.610
Υ	1.270
Y1	0.600
Y2	1.020
Y3	0.295
Y4	1.825
Y5	3.810
Y6	0.180
Y7	6.610

Site 2:

PowerDI5060-8 (SWP) (Type UX)



Dimensions	Value		
	(in mm)		
С	1.270		
G	0.660		
G1	0.820		
Х	0.610		
X1	4.100		
X2	5.190		
Х3	4.420		
Υ	1.270		
Y1	1.020		
Y2	3.810		
Y3	6.610		



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