



# 60V N-CHANNEL ENHANCEMENT MODE MOSFET PowerDI5060-8

### **Product Summary**

		I <sub>D</sub> MAX
BVDSS	Rds(on) MAX	$T_C = +25^{\circ}C$
		(Note 9)
60V	$3.1 \text{m}\Omega$ @ V <sub>GS</sub> = 10V	100A

## **Description and Applications**

This MOSFET is designed to minimize the on-state resistance (RDS(ON)) yet maintain superior switching performance, making it ideal for high-efficiency power-management applications.

- Switching
- · Synchronous rectifications
- DC-DC converters

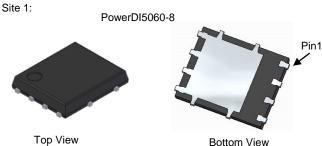
### **Features**

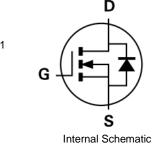
- 100% Unclamped Inductive Switching (UIS) Test in Production Ensures More Reliable And Robust End Application
- Low R<sub>DS(ON)</sub> Minimizes Power Losses
- Low Q<sub>g</sub> Minimizes Switching Losses
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/104/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please contact us or your local Diodes representative.

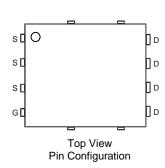
https://www.diodes.com/quality/product-definitions/

### **Mechanical Data**

- Package: PowerDI<sup>®</sup>5060-8
- Package Material: Molded Plastic, "Green" Molding Compound;
   UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Finish Matte Tin Annealed over Copper Leadframe;
   Solderable per MIL-STD-202, Method 208<sup>®</sup>
- Weight: 0.097 grams (Approximate)







Site 2:

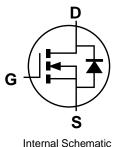
PowerDI5060-8/SWP (Type UX)



Top View



**Bottom View** 



S [ O ] D
S [ ] D
Top View
Pin Configuration

Ordering Information (Note 4)

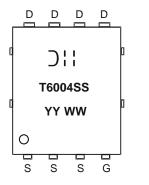
Part Number	Dookers	Pa	Packing		
Part Number	Package	Qty.	Carrier		
DMT6004SPS-13	PowerDI5060-8	2,500	Tape & Reel		
DMT6004SPS-13	PowerDI5060-8/SWP (Type UX)	2,500	Tape & Reel		

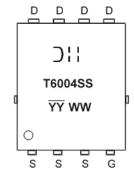
Notes:

- 1. EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. All applicable RoHS exemptions applied.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.



## **Marking Information**





);; = Manufacturer's Marking
T6004SS = Product Type Marking Code
YYWW = Date Code Marking YY or  $\overline{YY}$  = Year (ex: 23 = 2023) WW = Week (01 to 53)

### **Maximum Ratings** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic			Symbol	Value	Unit
Drain-Source Voltage			VDSS	60	V
Gate-Source Voltage			Vgss	±20	V
Continuous Drain Current (Note 5) Steady $T_A = +25^{\circ}C$ State $T_A = +70^{\circ}C$		lo	23 18	А	
Continuous Drain Current (Notes 6 & 9) $T_{C} = +25^{\circ}C$ $T_{C} = +70^{\circ}C$			lo	100 100	А
Maximum Continuous Body Diode Forward Current (Notes 6 & 9)			Is	100	А
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)			Ідм	400	А
Pulsed Body Diode Forward Current (10µs Pulse, Duty Cycle = 1%)			Іѕм	400	Α
Avalanche Current, L = 0.2mH			I <sub>AS</sub>	45	Α
Avalanche Energy, L = 0.2mH			Eas	200	mJ

## **Thermal Characteristics**

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 5)	T <sub>A</sub> = +25°C	PD	2.6	W
Thermal Resistance, Junction to Ambient	Steady State	Reja	47	°C/W
Total Power Dissipation (Note 6)	T <sub>C</sub> = +25°C	PD	139	W
Thermal Resistance, Junction to Case	<u>.</u>	Rejc	0.9	°C/W
Operating and Storage Temperature Range		TJ, TSTG	-55 to +150	°C

Notes:

- 5. Device mounted on FR-4 substrate PC board, 2oz copper, with thermal bias to bottom layer 1-inch square copper plate.
- 6. Thermal resistance from junction to soldering point (on the exposed drain pad).
- 7. Short duration pulse test used to minimize self-heating effect. 8. Guaranteed by design. Not subject to production testing.
- 9. Package limited.



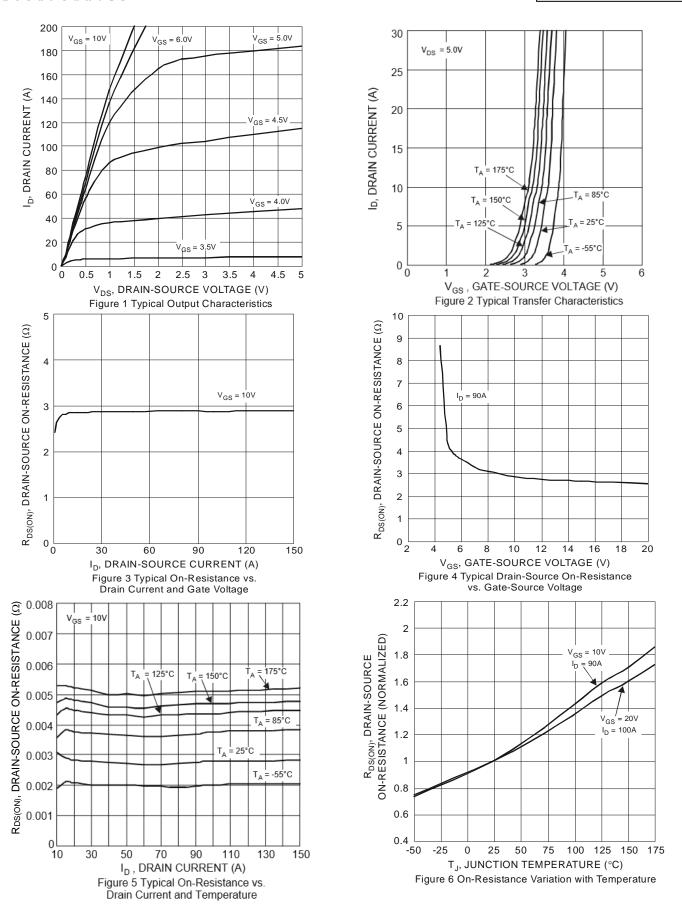
## Electrical Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)							
Drain-Source Breakdown Voltage	BVDSS	60	_	_	V	$V_{GS} = 0V$ , $I_D = 1mA$	
Zero Gate Voltage Drain Current	IDSS	-	_	1	μΑ	V <sub>DS</sub> = 48V, V <sub>GS</sub> = 0V	
Gate-Source Leakage	Igss	_	_	±100	nA	$V_{GS} = \pm 20V$ , $V_{DS} = 0V$	
ON CHARACTERISTICS (Note 7)							
Gate Threshold Voltage	V <sub>GS(TH)</sub>	2	2.5	4	V	$V_{DS} = V_{GS}$ , $I_D = 250\mu A$	
Static Drain-Source On-Resistance	R <sub>DS(ON)</sub>		_	3.1	mΩ	$V_{GS} = 10V, I_D = 50A$	
Diode Forward Voltage	$V_{SD}$	_	0.9	1.2	V	V <sub>GS</sub> = 0V, I <sub>S</sub> = 20A	
DYNAMIC CHARACTERISTICS (Note 8)							
Input Capacitance	Ciss		4,556	_	pF	V <sub>DS</sub> = 30V, V <sub>GS</sub> = 0V, f = 1MHz	
Output Capacitance	Coss		1,383	_			
Reverse Transfer Capacitance	Crss		105.2	_			
Gate Resistance	Rg	1	0.7	_	Ω	$V_{DS} = 0V$ , $V_{GS} = 0V$ , $f = 1MHz$	
Total Gate Charge	Qg		95.4	_		V 20V I 00A	
Gate-Source Charge	Qgs	_	21.6	_	nC	$V_{DD} = 30V, I_D = 90A,$ $V_{GS} = 10V$	
Gate-Drain Charge	$Q_{gd}$	_	20.4	_			
Turn-On Delay Time	tD(ON)	_	13.2	_			
Turn-On Rise Time	t <sub>R</sub>	_	11.7	_		$V_{DD} = 30V, V_{GS} = 10V,$ $I_{D} = 90A, R_{g} = 3.5\Omega$	
Turn-Off Delay Time	t <sub>D(OFF)</sub>	_	31	_	ns		
Turn-Off Fall Time	t <sub>F</sub>	-	12	_			
Body Diode Reverse Recovery Time	trr	_	50.5	_	ns	I <sub>F</sub> = 50A, di/dt = 100A/μs	
Body Diode Reverse Recovery Charge	Q <sub>RR</sub>	_	80.8	_	nC		

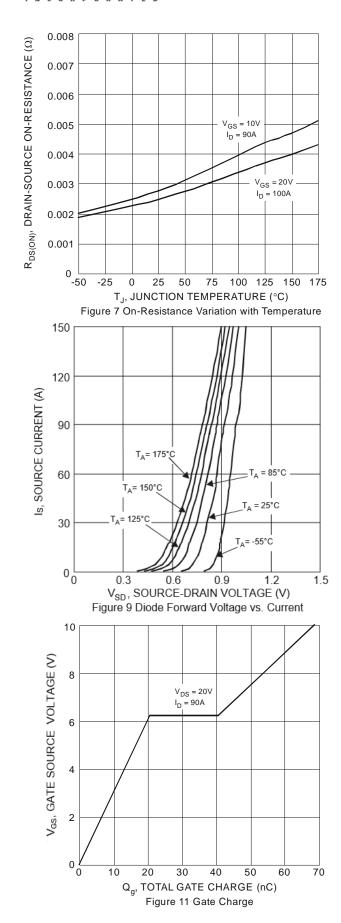
Notes:

<sup>7.</sup> Short duration pulse test used to minimize self-heating effect. 8. Guaranteed by design. Not subject to production testing.









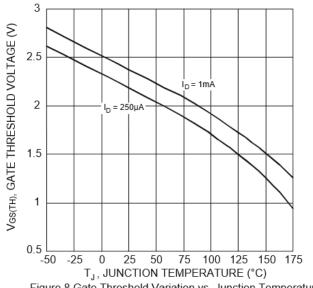
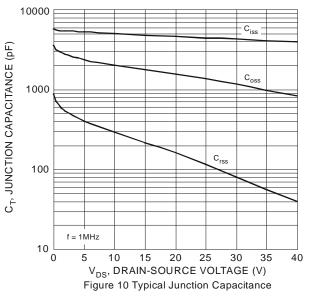
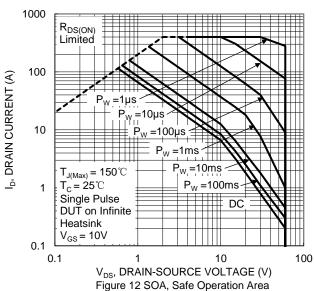


Figure 8 Gate Threshold Variation vs. Junction Temperature







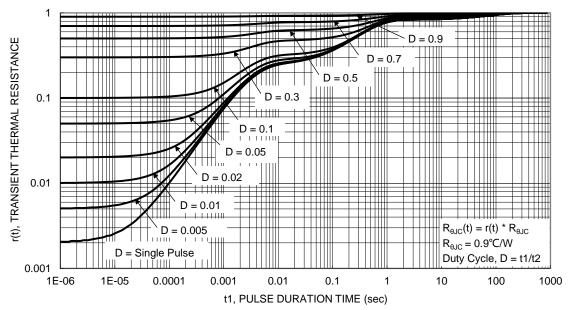


Figure 13. Transient Thermal Resistance

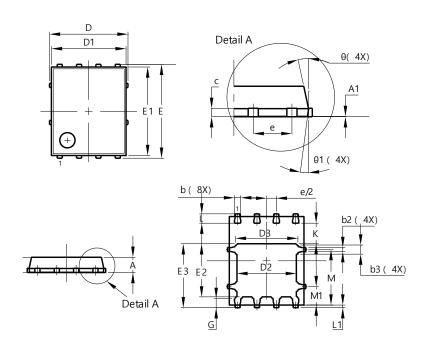


## **Package Outline Dimensions**

Please see http://www.diodes.com/package-outlines.html for the latest version.

Site 1:

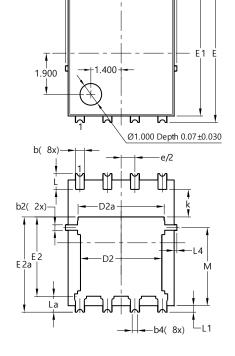
### PowerDI5060-8



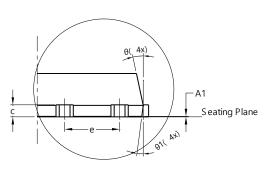
PowerDI5060-8				
Dim	Min	Max	Тур	
Α	0.90	1.10	1.00	
<b>A</b> 1	0.00	0.05	_	
b	0.33	0.51	0.41	
b2	0.200	0.350	0.273	
b3	0.40	0.80	0.60	
С	0.230	0.330	0.277	
D		5.15 BSC		
D1	4.70	5.10	4.90	
D2	3.70	4.10	3.90	
D3	3.90	4.30	4.10	
Е	(	6.15 BSC	;	
E1	5.60	6.00	5.80	
E2	3.28	3.68	3.48	
E3	3.99	4.39	4.19	
е		1.27 BSC	;	
G	0.51	0.71	0.61	
K	0.51	1	-	
L	0.51	0.71	0.61	
L1	0.100	0.200	0.175	
M	3.235	4.035	3.635	
M1	1.00	1.40	1.21	
Θ	10°	12º	11º	
Θ1	6°	80	7º	
All Dimensions in mm				

Site 2:

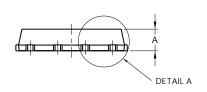
### PowerDI5060-8/SWP (Type UX)



-D1



DETAIL A



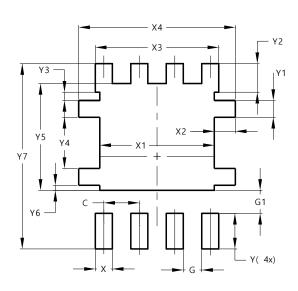
PowerDI5060-8/SWP (Type UX)				
Dim	Min	Max	Тур	
Α	0.90	1.10	1.00	
A1	0	0.05		
b	0.30	0.50	0.41	
b2	0.20	0.35	0.25	
b4	(	).25REF		
С	0.230	0.330	0.277	
D	5.15 BSC			
D1	4.70	5.10	4.90	
D2	3.56	3.96	3.76	
D2a	3.78	4.18	3.98	
Е	6.40 BSC			
E1	5.60	6.00	5.80	
E2	3.46	3.86	3.66	
E2a	4.195	4.595	4.395	
е	1.27BSC			
k	1.05			
L	0.635	0.835	0.735	
La	0.635	0.835	0.735	
L1	0.200	0.400	0.300	
L1a	0.050REF			
L4	0.025	0.225	0.125	
M	3.205	4.005	3.605	
θ	10°	12°	11°	
θ1	6°	8°	7°	
All Dimensions in mm				



## **Suggested Pad Layout**

Site 1:

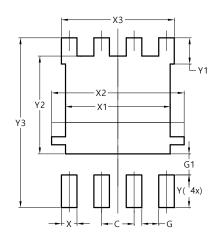
### PowerDI5060-8



Dimensions	Value (in mm)
С	1.270
G	0.660
G1	0.820
Χ	0.610
X1	4.100
X2	0.755
Х3	4.420
X4	5.610
Υ	1.270
Y1	0.600
Y2	1.020
Y3	0.295
Y4	1.825
Y5	3.810
Y6	0.180
Y7	6.610

Site 2:

### PowerDI5060-8/SWP (Type UX)



Dimensions	Value		
	(in mm)		
С	1.270		
G	0.660		
G1	0.820		
Х	0.610		
X1	4.100		
X2	5.190		
Х3	4.420		
Υ	1.270		
Y1	1.020		
Y2	3.810		
Y3	6.610		



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