



#### 100V N-CHANNEL ENHANCEMENT MODE MOSFET

## **Product Summary**

BV <sub>DSS</sub>	RDS(ON) Max	I <sub>D Max</sub> T <sub>C</sub> = +25°C	
100V	$9m\Omega$ @ $V_{GS} = 10V$	84A	
	$13m\Omega @ V_{GS} = 4.5V$	70A	

## **Features and Benefits**

- 100% Unclamped Inductive Switching (UIS) Test in Production Ensures More Reliable and Robust End Application
- Low On-Resistance
- Low Input Capacitance
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

## **Description and Applications**

This MOSFET is designed to minimize the on-state resistance (R<sub>DS(ON)</sub>) and yet maintain superior switching performance, making it ideal for high-efficiency power management applications.

- Motor Control
- Backlighting

#### **Mechanical Data**

- Case: TO251
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Terminal Connections: See Diagram
- Terminals: Finish Matte Tin Annealed over Copper Leadframe.
   Solderable per MIL-STD-202, Method 208 (2)
- Weight: 0.33 grams (Approximate)

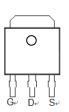




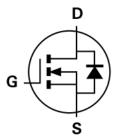




**Bottom View** 



Top View Pin Configuration



Internal Schematic

### Ordering Information (Note 4)

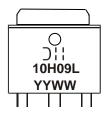
Part Number	Case	Packaging
DMT10H009LH3	TO251 (Type TH3)	75 Pieces / Tube

Notes:

- 1. EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. All applicable RoHS exemptions applied.
- 2. See http://www.diodes.com/quality/lead\_free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/

## **Marking Information**

TO251 (Type TH3)



);; = Manufacturer's Marking
10H09L = Product Type Marking Code
YYWW = Date Code Marking
YY or <u>YY</u> = Last Two Digits of Year (ex: 19 = 2019)
WW or WW = Week (01 to 53)



# **Maximum Ratings** ( $@T_A = +25^{\circ}C$ , unless otherwise specified.)

Characteristic	Symbol	Value	Unit	
Drain-Source Voltage	$V_{DSS}$	100	V	
Gate-Source Voltage	V <sub>GSS</sub>	±20	V	
Continuous Drain Current (Note 5) V <sub>GS</sub> = 10V	$T_C = +25$ °C $T_C = +70$ °C	I <sub>D</sub>	84 67	А
Maximum Body Diode Forward Current (Note 6)	Is	84	А	
Pulsed Drain Current (10µs Pulse, T <sub>C</sub> =+25°C, Package Limited )	I <sub>DM</sub>	336	A	
Pulsed Body Diode Current (10µs Pulse, T <sub>C</sub> =+25°C, Package Limited )	I <sub>SM</sub>	336	Α	
Avalanche Current, L = 0.3mH (Note 9)	I <sub>AS</sub>	21	Α	
Avalanche Energy, L = 0.3mH (Note 9)	E <sub>AS</sub>	66	mJ	

## Thermal Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit	
Total Power Dissipation (Note 5)	T <sub>C</sub> = +25°C	D-	96	w	
Total Power Dissipation (Note 5)	T <sub>C</sub> = +70°C	$P_{D}$	61	VV	
Thermal Resistance, Junction to Ambient (Note 6)	R <sub>0JA</sub>	44	°C/W		
Thermal Resistance, Junction to Case (Note 5)	$R_{\theta JC}$	1.3	*C/VV		
Operating and Storage Temperature Range		T <sub>J,</sub> T <sub>STG</sub>	-55 to +150	°C	

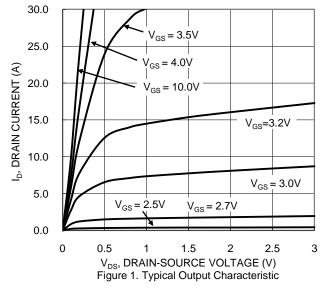
# **Electrical Characteristics** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

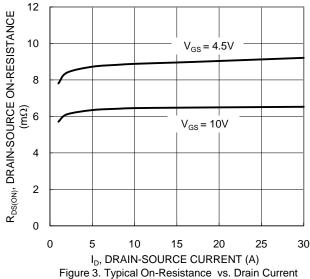
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 8)							
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	100	_	_	V	$V_{GS} = 0V$ , $I_D = 1mA$	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	_	_	1	μA	$V_{DS} = 80V, V_{GS} = 0V$	
Gate-Source Leakage	I <sub>GSS</sub>	_	_	100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 8)		•			•		
Gate Threshold Voltage	V <sub>GS(TH)</sub>	1.3	_	2.5	V	$V_{DS} = V_{GS}, I_D = 250 \mu A$	
Static Drain-Source On-Resistance	D	_	7	9	mΩ	V <sub>GS</sub> = 10V, I <sub>D</sub> = 20A	
Static Dialif-Source Off-Resistance	R <sub>DS(ON)</sub>	_	9	13	mΩ	$V_{GS} = 4.5V, I_D = 5A$	
Diode Forward Voltage	$V_{SD}$	_	8.0	1.2	V	$V_{GS} = 0V, I_{S} = 20A$	
DYNAMIC CHARACTERISTICS (Note 7)							
Input Capacitance	Ciss	_	2309	_		V <sub>DS</sub> = 50V, V <sub>GS</sub> = 0V f = 1MHz	
Output Capacitance	Coss	_	536	_	pF		
Reverse Transfer Capacitance	C <sub>rss</sub>	_	13.7	_		I = IIVII IZ	
Gate Resistance	$R_g$	_	1.9	_	Ω	$V_{DS} = 0V$ , $V_{GS} = 0V$ , $f = 1MHz$	
Total Gate Charge	Qg	_	20.2	_		V 50V L 00A	
Gate-Source Charge	$Q_{gs}$	_	7.0	_	nC	$V_{DD} = 50V, I_D = 20A,$ $V_{GS} = 4.5V$	
Gate-Drain Charge	$Q_{gd}$	_	8.5	_		VGS = 4.5V	
Turn-On Delay Time	t <sub>D(ON)</sub>	_	5.4	_			
Turn-On Rise Time	t <sub>R</sub>	_	10.6	_	ns	$V_{DD} = 50V, V_{GS} = 10V,$ $I_{D} = 20A, R_{g} = 3\Omega$	
Turn-Off Delay Time	t <sub>D(OFF)</sub>	_	28.3	_	115		
Turn-Off Fall Time	t <sub>F</sub>	_	14.9				
Reverse Recovery Time	t <sub>RR</sub>	_	44.3		ns	I 201 di/dt _ 1001/up	
Reverse Recovery Charge	Q <sub>RR</sub>	_	65.5		nC	I <sub>F</sub> = 20A, di/dt = 100A/μs	

Notes:

- 5. Device mounted on infinite heatsink.6. Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.
- Guaranteed by design. Not subject to production testing.
   Short duration pulse test used to minimize self-heating effect.
- 9.  $I_{AS}$  and  $E_{AS}$  ratings are based on low frequency and duty cycles to keep  $T_J$  = +25°C.







and Gate Voltage

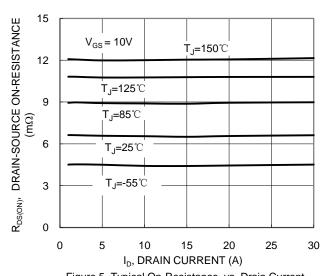


Figure 5. Typical On-Resistance vs. Drain Current and Junction Temperature

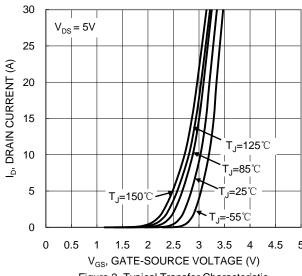


Figure 2. Typical Transfer Characteristic

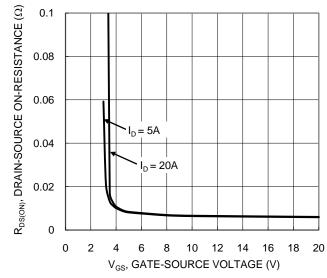


Figure 4. Typical Transfer Characteristic

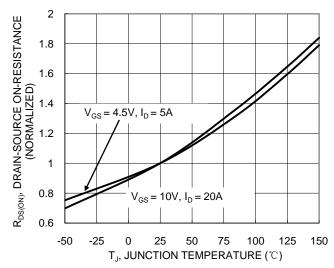
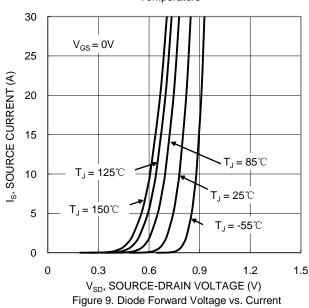


Figure 6. On-Resistance Variation with Junction Temperature



#### 0.02 R<sub>DS(ON)</sub>, DRAIN-SOURCE ON-RESISTANCE 0.016 $V_{GS} = 4.5V, I_{D} = 5A$ 0.012 0.008 $V_{GS} = 10V, I_{D} = 20A$ 0.004 0 -25 0 -50 25 50 75 100 125 150 T<sub>J</sub>, JUNCTION TEMPERATURE (°C)

Figure 7. On-Resistance Variation with Junction
Temperature



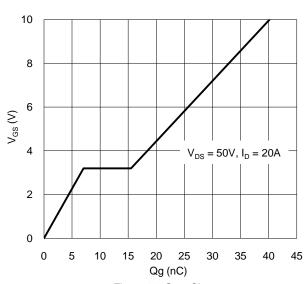


Figure 11. Gate Charge

### **DMT10H009LH3**

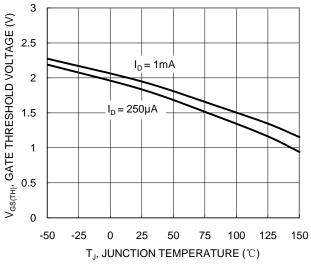
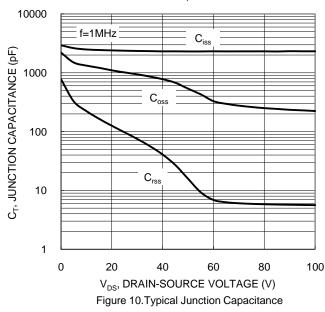
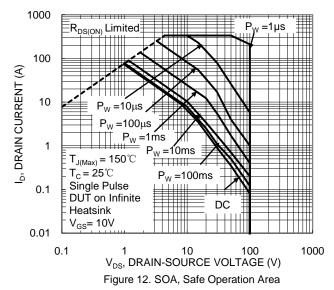


Figure 8. Gate Threshold Variation vs. Junction Temperature







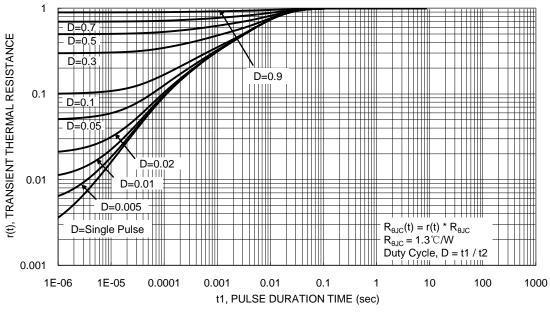
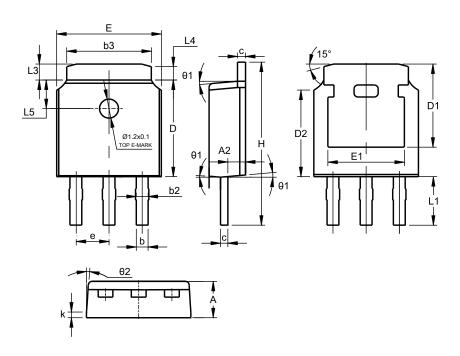


Figure 13. Transient Thermal Resistance

## **Package Outline Dimensions**

Please see http://www.diodes.com/package-outlines.html for the latest version.

#### TO251 (Type TH3)



TO251						
(Type TH3)						
Dim	Min	Тур				
Α	2.20	2.40	2.30			
A2	0.97	1.17	1.07			
b	0.68	0.90	0.78			
b2	0.76	0.95	0.84			
b3	5.20	5.50	5.33			
C	0.43	0.63	0.53			
D	5.98	6.22	6.10			
D1	5	5.30 REF				
D2	5.26	5.66	5.46			
е	2.	286 BS	C			
Е	6.40	6.80	6.60			
E1	4.63	5.03	4.83			
Н	9.40	9.85	9.62			
k	0.40REF					
L1	2.30	2.70	2.50			
L3	0.88 1.28		1.02			
L4	0.75 REF					
L5	1.65	1.95	1.80			
θ1	5°	9°	7°			
θ2	5°	9°	7°			
All Dimensions in mm						



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