

P-CHANNEL ENHANCEMENT MODE MOSFET WITH INTEGRATED SCHOTTKY DIODE

Product Summary

MOSFET							
BV _{DSS}	R _{DS(ON) max}	l _D					
	95mΩ @ $V_{GS} = -4.5V$	-3.4A					
-20V	120mΩ @ V _{GS} = -2.5V	-3.0A					
	150mΩ @ V _{GS} = -1.8V	-2.7A					
	SCHOTTKY DIODE						
V R	V _{F max}	lo					
20V	400mV @ I _F = 0.5A	1.0A					
200	470mV @ I _F = 1.0A	1.0A					

Features and Benefits

- MOSFET with Low RDS(ON) Minimize Conduction Losses
- Low Gate Threshold Voltage, -1.3V Max
- Schottky Diode with Low Forward Voltage Drop
- Low Profile, 0.5mm Max Height
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
 - For automotive applications requiring specific change control (i.e.: parts qualified to AEC-Q100/101/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please refer to the related automotive grade (Q-suffix) part. A listing can be found at

https://www.diodes.com/products/automotive/automotive-products/.

 This part is qualified to JEDEC standards (as references in AEC-Q) for High Reliability.

https://www.diodes.com/quality/product-definitions/

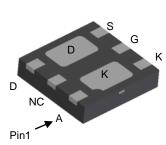
Description and Applications

This new generation MOSFET has been designed to minimize the onstate resistance (R_{DS(ON)}) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

Power Management Functions

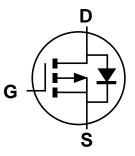
Mechanical Data

- Case: U-DFN2020-6
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Diagram
- Terminals: Finish NiPdAu Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 (4)
- Weight: 0.0065 grams (Approximate)

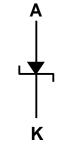


U-DFN2020-6 (Type B)

Bottom View



Q1 P-MOSFET



D1 SCHOTTKY DIODE

Ordering Information (Note 4)

Part Number	Case	Packaging		
DMS2095LFDB-7	U-DFN2020-6 (Type B)	3,000/Tape & Reel		
DMS2095LFDB-13	U-DFN2020-6 (Type B)	10,000/Tape & Reel		

Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.

- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.



Marking Information

Site 1



MS2 = Product Type Marking Code YM = Date Code Marking Y = Year (ex: H = 2020) M = Month (ex: 9 = September)

Date Code Key

Year	2012		2020	2021	2022	2023	2024	2025	2026	2027	2028	2029
Code	Z		Н	ı	J	K	L	М	N	0	Р	R
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D

Site 2



MS2 = Product Type Marking Code YWX = Date Code Marking Y = Year (ex: 0 = 2020) W = Week (ex: a = week 27; z represents week 52 and 53) X = Internal Code (ex: U = Monday)

Date Code Kev

Year	2012		2020	2021	2022	2023	2024	2025	2026	2027	2028	2029
Code	2		0	1	2	3	4	5	6	7	8	9
Week	1-26			27-52			53					
Code	A-Z				a-z			Z				
Internal Code	Sur	n	Mon		Tue	W	ed	Thu		Fri		Sat
Code	Т		U		V	V	V	Х		Y		Z



Maximum Ratings - P-CHANNEL MOSFET - Q1 (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit		
Drain-Source Voltage			V _{DSS}	-20	V
Gate-Source Voltage (Note 5)	V _{GSS}	±12	V		
Continuous Drain Current (Note 7) V 45V	Steady State	$T_A = +25$ °C $T_A = +70$ °C	ΙD	-3.4 -2.7	А
Continuous Drain Current (Note 7) V _{GS} = -4.5V	t<10s	T _A = +25°C T _A = +70°C	lo	-3.9 -3.1	А
Maximum Body Diode Forward Current (Note 7)	Is	-1	Α		
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%	I _{DM}	-10	А		

Maximum Ratings – SCHOTTKY – D1 (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	Vrrm Vrwm Vr	20	V
Average Rectified Output Current (Note 7, t<10s)	lo	1	Α
Peak Repetitive Forward Current (Note 7, t<10s)	I _{FRM}	2	Α
Non-Repetitive Peak Forward Surge Current (Note 7, t<10s) Single Half Sine-Wave Superimposed on Rated Load	IFSM	20	А

Thermal Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Total Dawer Dissipation (Note C)	T _A = +25°C	1	0.81	10/
Total Power Dissipation (Note 6)	T _A = +70°C	PD	0.52	W
Thermal Desistance Investigate Archiect (Nets C)	Steady State	1	154	9000
Thermal Resistance, Junction to Ambient (Note 6)	t<10s	$R_{\theta JA}$	114	°C/W
Total Dawar Dissipation (Note 7)	T _A = +25°C	0	1.64	W
Total Power Dissipation (Note 7)	T _A = +70°C	P_{D}	1.04	
Thormal Posistance Junction to Ambient (Note 7)	Steady State	р	77	°C/W
Thermal Resistance, Junction to Ambient (Note 7)	t<10s	$R_{\theta JA}$	57	7
Thermal Resistance, Junction to Case (Note 7)		R _θ JC	27.5	°C/W
Operating and Storage Temperature Range		T _{J,} T _{STG}	-55 to +150	°C

Notes: 5. AEC-Q101 V_{GS} maximum is $\pm 9.6 V$.

^{6.} Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.
7. Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.



Electrical Characteristics - P-CHANNEL MOSFET - Q1 (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 8)						
Drain-Source Breakdown Voltage	BVDSS	-20	_	_	V	V _G S = 0V, I _D = -250μA
Zero Gate Voltage Drain Current	I _{DSS}	_	_	-1	μA	$V_{DS} = -20V, V_{GS} = 0V$
Gate-Source Leakage	I _{GSS}	_	_	±800	nA	$V_{GS} = \pm 12V, V_{DS} = 0V$
ON CHARACTERISTICS (Note 8)						
Gate Threshold Voltage	VGS(TH)	-0.4		-1.3	٧	V _{DS} = V _{GS} , I _D = -250μA
		_	48	95		V _G S = -4.5V, I _D = -2.8A
Static Drain-Source On-Resistance	RDS(ON)	_	65	120	$m\Omega$	V _G S = -2.5V, I _D = -2.0A
		_	90	150		V _G S = -1.8V, I _D = -1.0A
Diode Forward Voltage	VsD	_	-0.42	-1.2	V	V _G S = 0V, I _S = -1.0A
DYNAMIC CHARACTERISTICS (Note 9)						
Input Capacitance	Ciss	_	561	—	pF	.,
Output Capacitance	Coss	_	78		pF	$V_{DS} = -10V, V_{GS} = 0V$ f = 1.0MHz
Reverse Transfer Capacitance	Crss	_	66	_	pF	1 - 1.51/11/2
Gate Resistance	Rg	_	59.5	_	Ω	$V_{DS} = 0V$, $V_{GS} = 0V$, $f = 1MHz$
Total Gate Charge	Qg	_	7.0	_	nC	
Gate-Source Charge	Qgs	_	0.9	_	nC	$V_{GS} = -4.5V, V_{DS} = -10V,$ $I_{D} = -2.5A$
Gate-Drain Charge	Q _{gd}	_	1.7	_	nC	1D = -2.5A
Turn-On Delay Time	td(ON)	_	5.3	_	ns	
Turn-On Rise Time	t _R	_	5.8	_	ns	V _{DD} = -10V, V _{GS} = -4.5V,
Turn-Off Delay Time	tD(OFF)	_	69	_	ns	$R_L = 4\Omega$, $R_G = 6\Omega$
Turn-Off Fall Time	tF	_	54	_	ns	
Reverse Recovery Time	trr	_	12.4	_	ns	0.54 1/1/2 4004/
Reverse Recovery Charge	QRR	_	3.7	_	nC	I _F = -2.5A, di/dt = 100A/μs

Electrical Characteristics – SCHOTTKY – D1 (@T_A = +25°C, unless otherwise specified.)

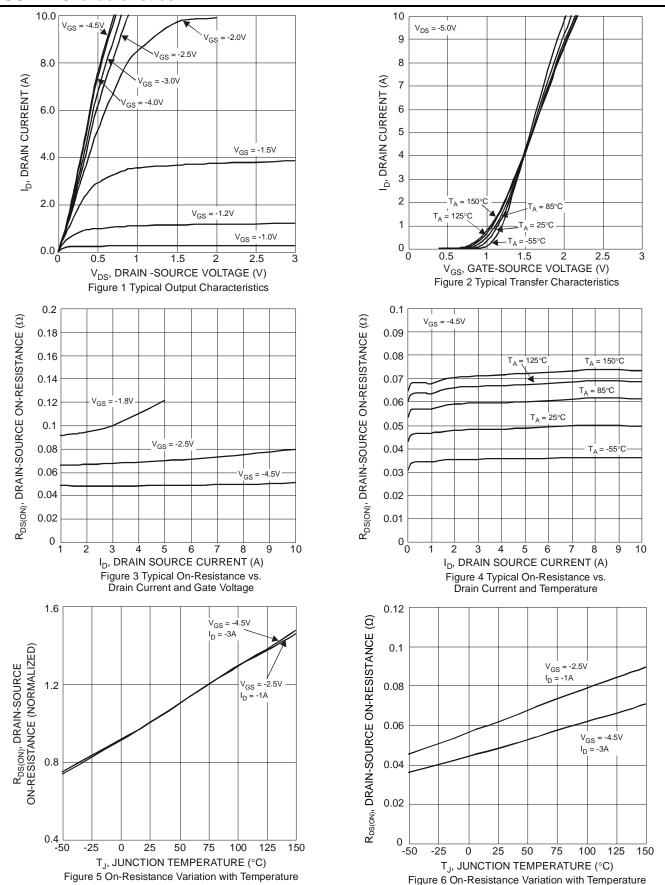
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
Reverse Breakdown Voltage (Note 8)	$V_{(BR)R}$	20	35		V	$I_R = 1mA$
Forward Voltage (Note 8)	VF			0.40 0.47	\/	I _F = 0.5A I _F = 1.0A
Reverse Current (Note 8)	IR		30	80	μΑ	V _R = 20V

Notes:

^{8.} Short duration pulse test used to minimize self-heating effect. 9. Guaranteed by design. Not subject to product testing.



MOSFET Characteristics





MOSFET Characteristics (continued)

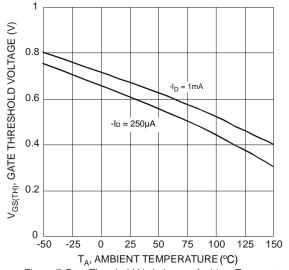


Figure 7 Gate Threshold Variation vs. Ambient Temperature

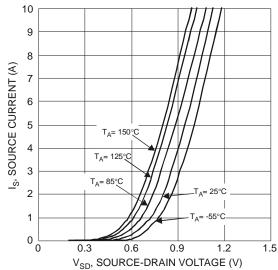


Figure 8 Diode Forward Voltage vs. Current

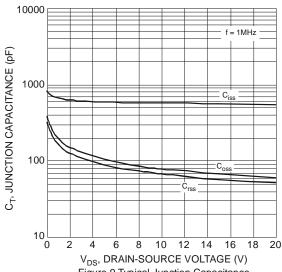
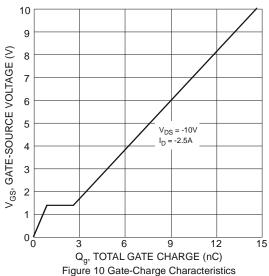


Figure 9 Typical Junction Capacitance

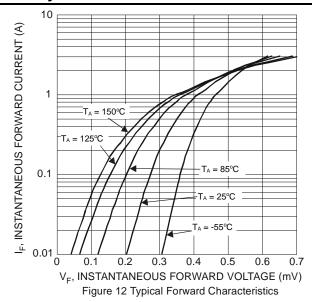


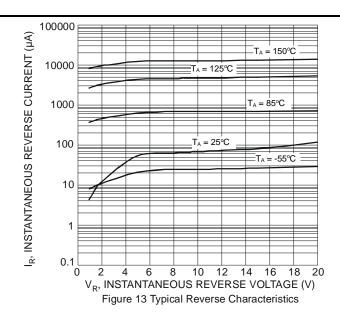
r(t), TRANSIENT THERMAL RESISTANCE 0.01 $R_{\theta JA}(t) = r(t) * R_{\theta JA}$ R_{θJA} = 155°C/W Duty Cycle, D = t1 / t2 0.001 0.00001 0.0001 0.001 0.1 100 1000

t1, PULSE DURATION TIME (sec) Figure 11 Transient Thermal Resistance



Schottky Characteristics



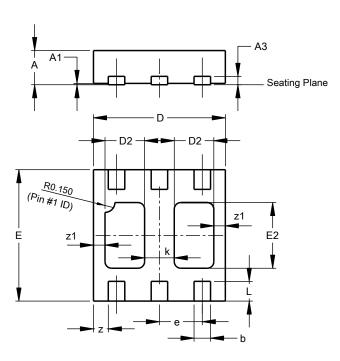




Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

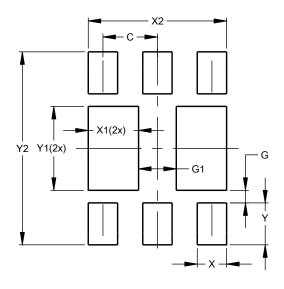
U-DFN2020-6 (Type B)



	U-DFN2020-6							
Type B								
Dim	Min	Max	Тур					
Α	0.545	0.605	0.575					
A1	0.00	0.05	0.02					
A3	-	-	0.13					
b	0.20	0.30	0.25					
D	1.95	2.075	2.00					
D2	0.50	0.70	0.60					
е	-	-	0.65					
Е	1.95	2.075	2.00					
E2	0.90	1.10	1.00					
k	-	-	0.45					
L	0.25	0.35	0.30					
Z	-	-	0.225					
z1	-	-	0.175					
All	All Dimensions in mm							

Suggested Pad Layout

U-DFN2020-6 (Type B)



Dimensions	Value (in mm)
С	0.650
G	0.150
G1	0.450
Х	0.350
X1	0.600
X2	1.650
Y	0.500
Y1	1.000
Y2	2.300



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