

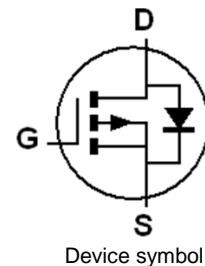
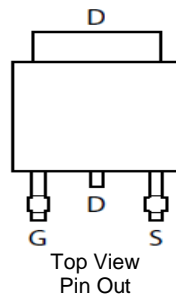
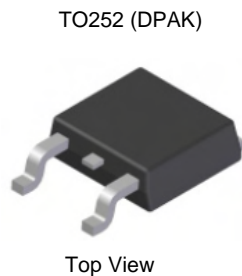
## Product Summary

BV <sub>DSS</sub>	R <sub>DS(ON)</sub> Max	I <sub>D</sub> Max T <sub>c</sub> = +25°C
-40V	25mΩ @ V <sub>GS</sub> = -10V	-40A
	45mΩ @ V <sub>GS</sub> = -4.5V	-33A

## Description and Applications

This MOSFET is designed to meet the stringent requirements of automotive applications. It is qualified to AEC-Q101, supported by a PPAP and is ideal for use in:

- Motor controls
- Backlighting
- DC-DC converters
- Printer equipment



## Features and Benefits

- Low On-Resistance
- Fast Switching Speed
- Low-Input/Output Leakage
- 100% Unclamped Inductive Switch (UIS) Test in Production
- **Lead-Free Finish; RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**
- **The DMP4026LK3Q is suitable for automotive applications requiring specific change control; this part is AEC-Q101 qualified, PPAP capable, and manufactured in IATF 16949 certified facilities.**

<https://www.diodes.com/quality/product-definitions/>

## Mechanical Data

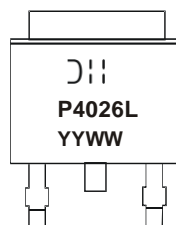
- Package: TO252
- Package Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 3 per J-STD-020
- Terminals Connections: See Diagram Below
- Terminals: Finish - Matte Tin Annealed over Copper Lead Frame. Solderable per MIL-STD-202, Method 208 Ⓒ3
- Weight: 0.315 grams (Approximate)

## Ordering Information (Note 4)

Part Number	Package	Packing	
		Qty.	Carrier
DMP4026LK3Q-13	TO252 (DPAK)	2500	Reel

- Notes:
1. EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. All applicable RoHS exemptions applied.
  2. See <https://www.diodes.com/quality/lead-free/> for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
  3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
  4. For packaging details, go to our website at <https://www.diodes.com/design/support/packaging/diodes-packaging/>.

## Marking Information



D = Manufacturer's Marking  
P4026L = Product Type Marking Code  
YYWW = Date Code Marking  
YY = Year (ex: 23 = 2023)  
WW = Week (01 to 53)

**Maximum Ratings** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic			Symbol	Value	Unit
Drain-Source Voltage			V <sub>DSS</sub>	-40	V
Gate-Source Voltage			V <sub>GSS</sub>	±20	V
Continuous Drain Current (Note 6) V <sub>GS</sub> = -10V	Steady State	T <sub>C</sub> = +25°C	I <sub>D</sub>	-40	A
		T <sub>C</sub> = +70°C		-32	
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)			I <sub>DM</sub>	-160	A
Maximum Body Diode Forward Current (Note 6)			I <sub>S</sub>	-40	A
Avalanche Current, L = 0.3mH			I <sub>AS</sub>	-22.9	A
Avalanche Energy, L = 0.3mH			E <sub>AS</sub>	78.7	mJ

**Thermal Characteristics** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 5)	T <sub>A</sub> = +25°C	P <sub>D</sub>	3.5	W
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	R <sub>θJA</sub>	35	°C/W
Total Power Dissipation (Note 6)	T <sub>C</sub> = +25°C	P <sub>D</sub>	70	W
Thermal Resistance, Junction to Case (Note 6)	Steady State	R <sub>θJC</sub>	1.77	°C/W
Operating and Storage Temperature Range		T <sub>J</sub> , T <sub>STG</sub>	-55 to +150	°C

**Electrical Characteristics** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
<b>OFF CHARACTERISTICS</b> (Note 7)						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	-40	—	—	V	I <sub>D</sub> = -250µA, V <sub>GS</sub> = 0V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	—	—	-1	µA	V <sub>DS</sub> = -40V, V <sub>GS</sub> = 0V
Gate-Source Leakage	I <sub>GSS</sub>	—	—	±100	nA	V <sub>GS</sub> = ±20V, V <sub>DS</sub> = 0V
<b>ON CHARACTERISTICS</b> (Note 7)						
Gate Threshold Voltage	V <sub>GS(TH)</sub>	-0.8	—	-1.8	V	I <sub>D</sub> = -250µA, V <sub>DS</sub> = V <sub>GS</sub>
Static Drain-Source On-Resistance	R <sub>DS(ON)</sub>	—	13	25	mΩ	V <sub>GS</sub> = -10V, I <sub>D</sub> = -3A
			17	45		V <sub>GS</sub> = -4.5V, I <sub>D</sub> = -3A
Diode Forward Voltage	V <sub>SD</sub>	—	-0.7	-1	V	I <sub>S</sub> = -1A, V <sub>GS</sub> = 0V
<b>DYNAMIC CHARACTERISTICS</b> (Note 8)						
Input Capacitance	C <sub>iss</sub>	—	2064	—	pF	V <sub>DS</sub> = -20V, V <sub>GS</sub> = 0V, f = 1MHz
Output Capacitance	C <sub>oss</sub>	—	212	—		
Reverse Transfer Capacitance	C <sub>rss</sub>	—	183	—		
Gate Resistance	R <sub>g</sub>	—	2.5	—	Ω	V <sub>DS</sub> = 0V, V <sub>GS</sub> = 0V, f = 1MHz
Total Gate Charge (V <sub>GS</sub> = -10V)	Q <sub>g</sub>	—	45.8	—	nC	V <sub>DS</sub> = -20V, I <sub>D</sub> = -3A
Total Gate Charge (V <sub>GS</sub> = -4.5V)	Q <sub>g</sub>	—	23.5	—		
Gate-Source Charge	Q <sub>gs</sub>	—	5	—		
Gate-Drain Charge	Q <sub>gd</sub>	—	6.7	—		
Turn-On Delay Time	t <sub>D(ON)</sub>	—	4.3	—	ns	V <sub>GS</sub> = -10V, V <sub>DD</sub> = -20V, I <sub>D</sub> = -3A, R <sub>G</sub> = 6Ω
Turn-On Rise Time	t <sub>R</sub>	—	4.7	—		
Turn-Off Delay Time	t <sub>D(OFF)</sub>	—	71.8	—		
Turn-Off Fall Time	t <sub>F</sub>	—	23.9	—		

- Notes:
- Device mounted on FR-4 substrate PC board, 2oz copper, with thermal bias to bottom layer 1inch square copper plate.
  - Thermal resistance from junction to soldering point (on the exposed drain pad).
  - Short duration pulse test used to minimize self-heating effect.
  - Guaranteed by design. Not subject to product testing.

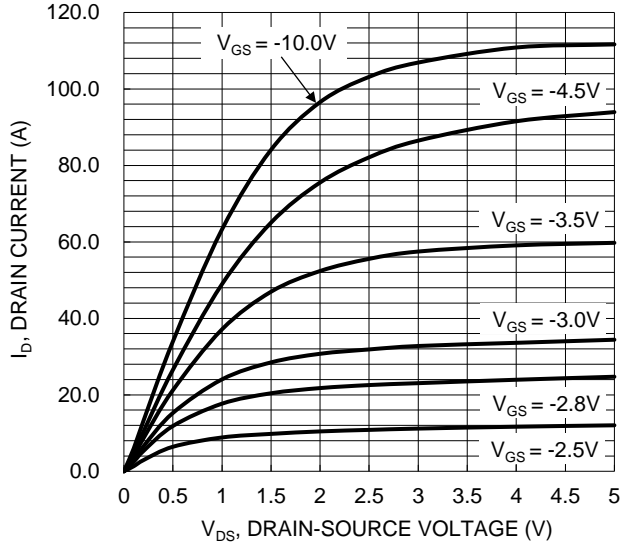


Figure 1. Typical Output Characteristic

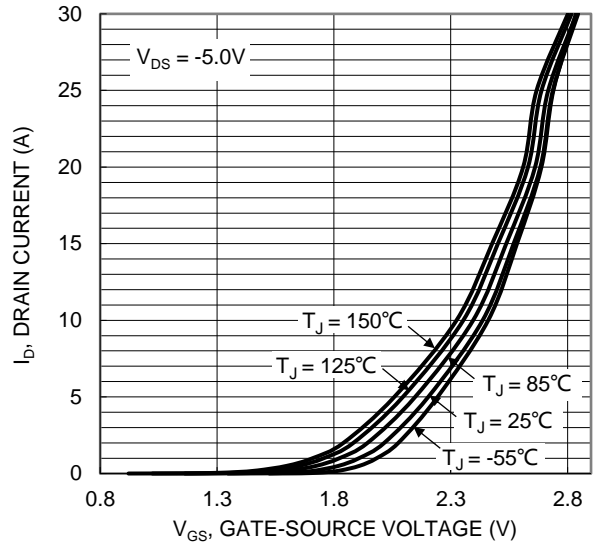


Figure 2. Typical Transfer Characteristic

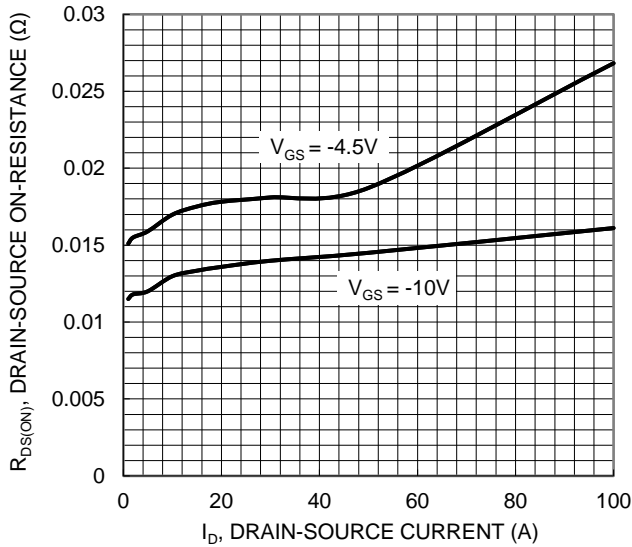


Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage

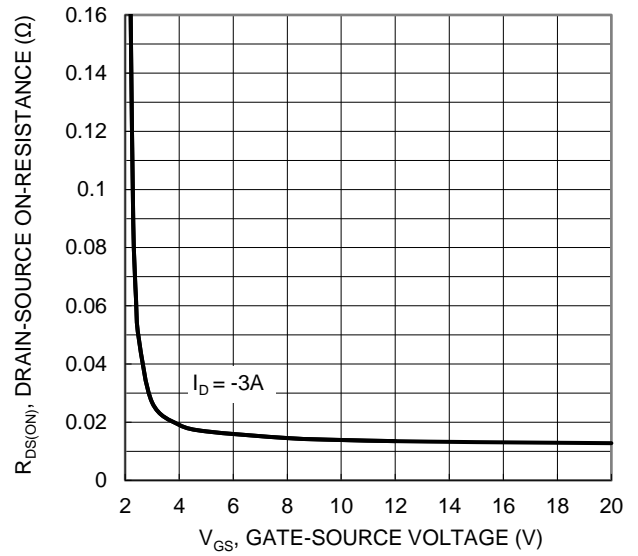


Figure 4. Typical Transfer Characteristic

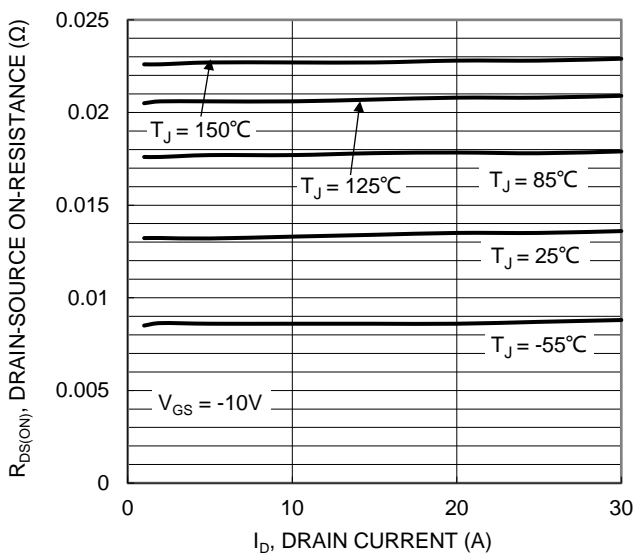


Figure 5. Typical On-Resistance vs. Drain Current and Junction Temperature

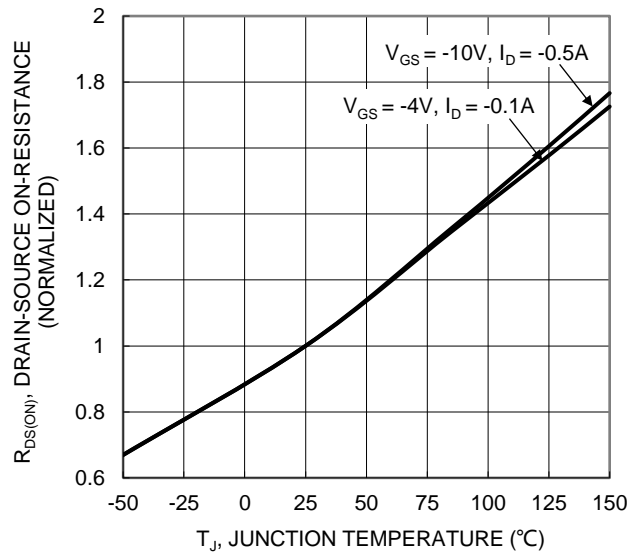


Figure 6. On-Resistance Variation with Junction Temperature

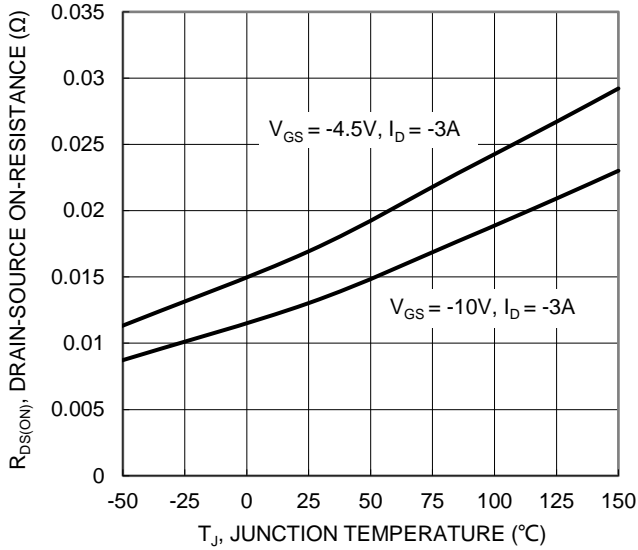


Figure 7. On-Resistance Variation with Junction Temperature

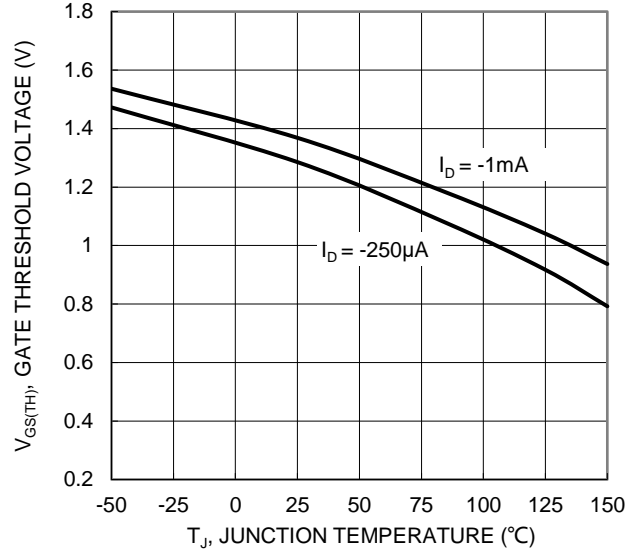


Figure 8. Gate Threshold Variation vs. Junction Temperature

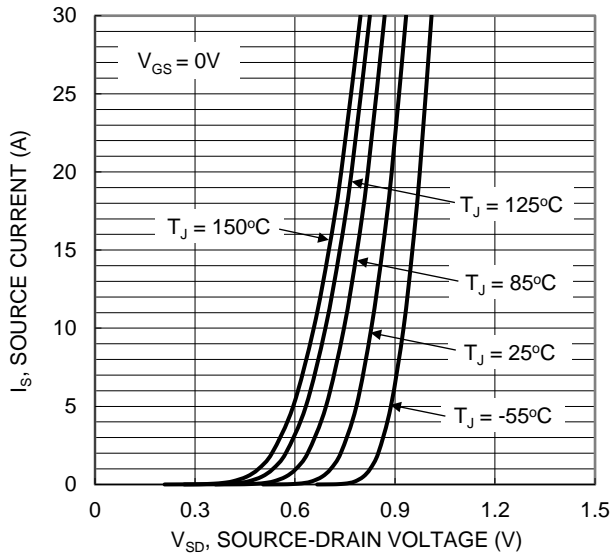


Figure 9. Diode Forward Voltage vs. Current

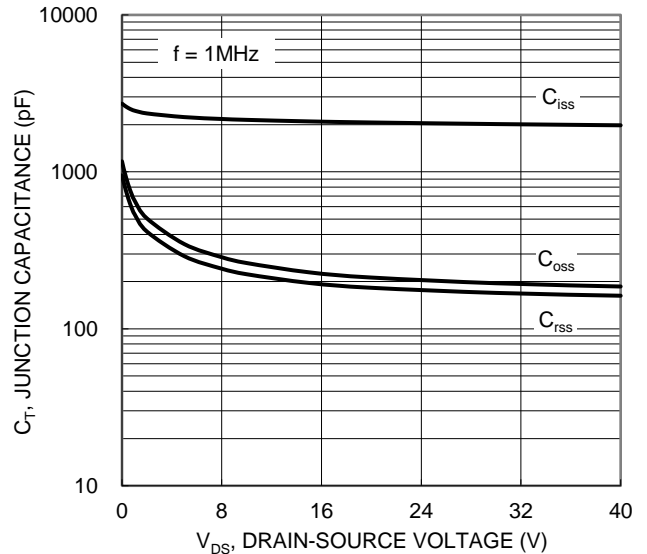


Figure 10. Typical Junction Capacitance

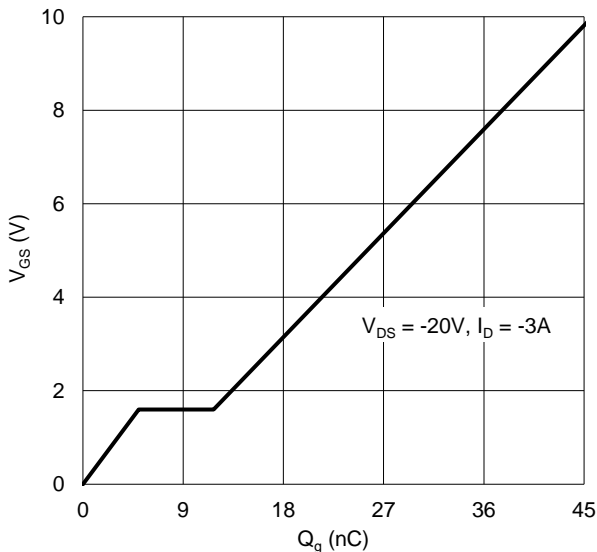


Figure 11. Gate Charge

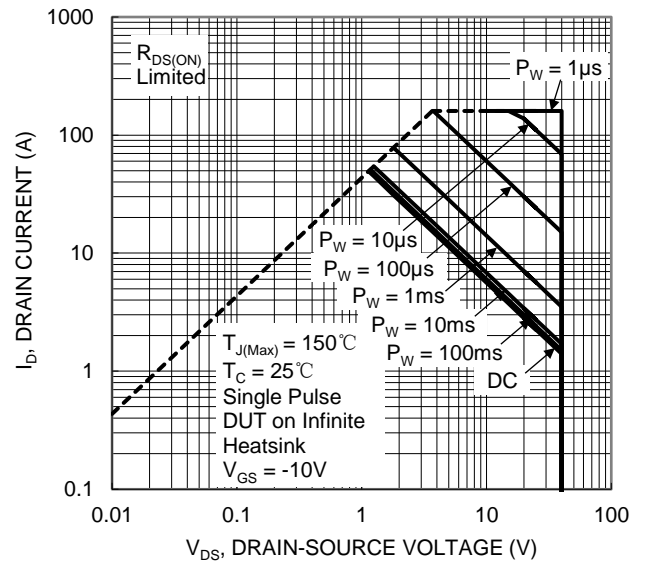


Figure 12. SOA, Safe Operation Area

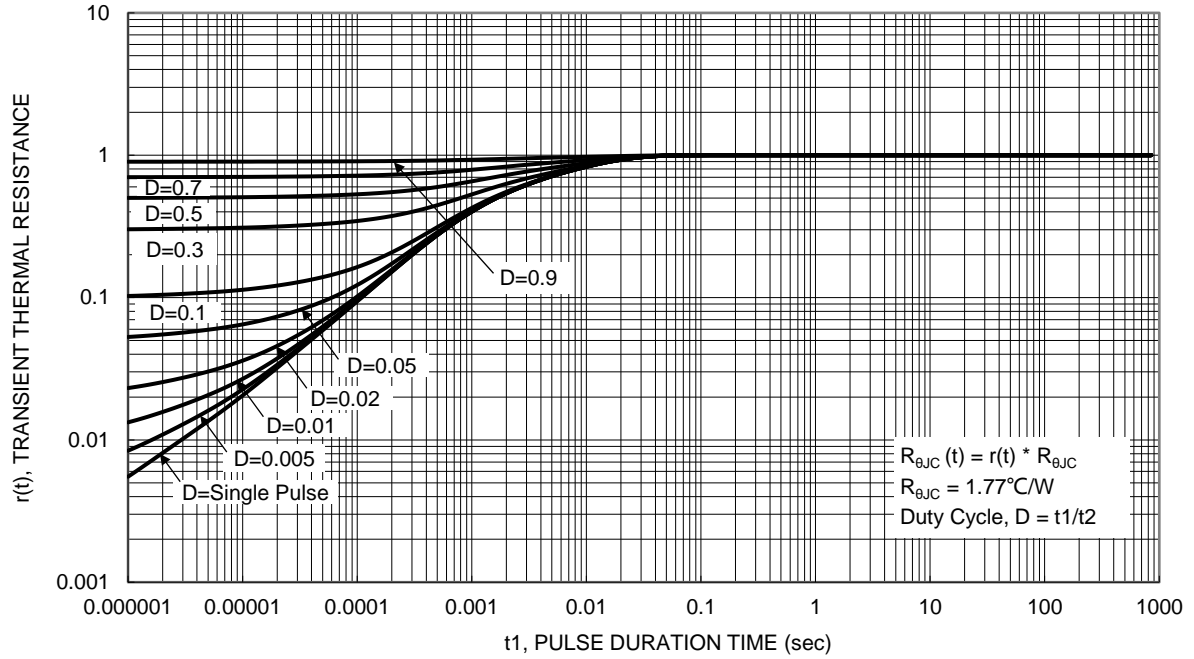
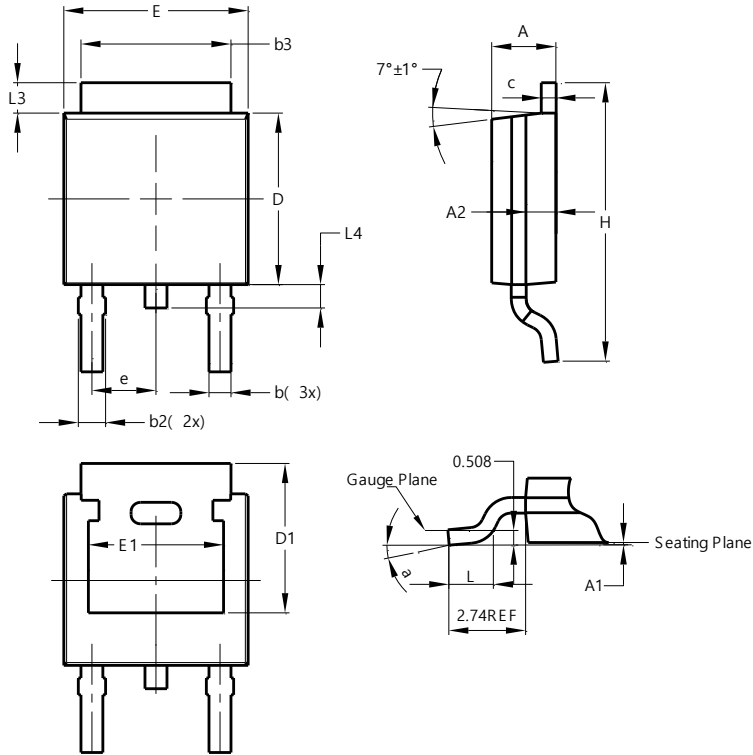


Figure 13. Transient Thermal Resistance

## Package Outline Dimensions

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

TO252 (DPAK)

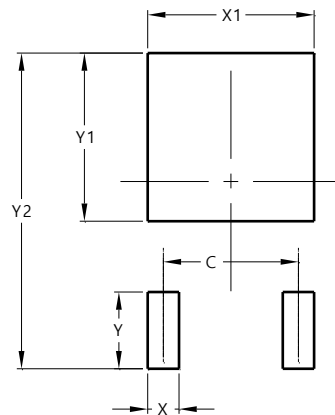


TO252 (DPAK)			
Dim	Min	Max	Typ
A	2.19	2.39	2.29
A1	0.00	0.13	0.08
A2	0.97	1.17	1.07
b	0.64	0.88	0.783
b2	0.76	1.14	0.95
b3	5.21	5.50	5.33
c	0.45	0.58	0.531
D	6.00	6.20	6.10
D1	5.21	--	--
e	2.286 BSC		
E	6.45	6.70	6.58
E1	4.32	--	--
H	9.40	10.41	9.91
L	1.40	1.78	1.59
L3	0.88	1.27	1.08
L4	0.64	1.02	0.83
a	0°	10°	--
<b>All Dimensions in mm</b>			

## Suggested Pad Layout

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

TO252 (DPAK)



Dimensions	Value (in mm)
C	4.572
X	1.060
X1	5.632
Y	2.600
Y1	5.700
Y2	10.700

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