



DUAL P-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

BV _{DSS}	R _{DS(ON)} Max	I _D Max T _A = +25°C
-30V	5Ω @ VGS = -4.5V	-0.22A
	6Ω @ VGS = -2.5V	-0.20A
	7Ω @ VGS = -1.8V	-0.18A
	10Ω @ Vgs = -1.5V	-0.15A

Description and Applications

This MOSFET is designed to minimize the on-state resistance (R_{DS(ON)}) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

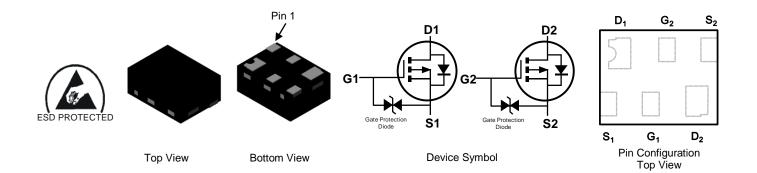
- General Purpose Interfacing Switch
- Power Management Functions
- Analog Switch

Features and Benefits

- Low On-Resistance
- Very Low Gate Threshold Voltage
- Low Input Capacitance
- Fast Switching Speed
- Ultra-Small Surface Mount Package 0.8mm x 0.6mm
- ESD Protected Gate
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

Mechanical Data

- Case: X2-DFN0806-6
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Annealed over Copper Leadframe.
 Solderable per MIL-STD-202, Method 208 (3)
- Weight: 0.001 grams (Approximate)



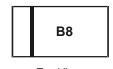
Ordering Information (Note 4)

Part Number	Case	Packaging	
DMP32D9UDA-7B	X2-DFN0806-6	10,000/Tape & Reel	

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information



B8 = Product Type Marking Code Bar denotes Pin 1

Top View



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic			Symbol	Value	Unit
Drain-Source Voltage			V _{DSS}	-30	V
Gate-Source Voltage			V _{GSS}	±12	V
Continuous Proin Current (Note 5) // 45/	Steady State	T _A = +25°C	- I _D	-0.22	Α
Continuous Drain Current (Note 5) V _{GS} = -4.5V		T _A = +70°C		-0.17	
Maximum Continuous Body Diode Forward Current (Note 5)			I _S	-0.8	А
Pulsed Drain Current (Note 5)			I _{DM}	-0.8	А

Thermal Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	
Total Power Dissipation (Note 5)		P_D	0.36	W
Thermal Resistance, Junction to Ambient (Note 5) Steady State		$R_{\theta JA}$	345	°C/W
Operating and Storage Temperature Range		$T_{J_i}T_{STG}$	-55 to +150	°C

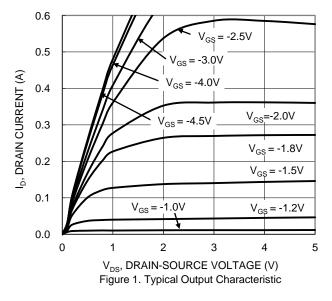
Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 6)							
Drain-Source Breakdown Voltage		-30	_	_	V	$V_{GS} = 0V, I_D = -250\mu A$	
Zero Gate Voltage Drain Current @T _C = +25°C		_	_	-100	nA	V _{DS} = -24V, V _{GS} = 0V	
Gate-Source Leakage		_	_	±10	μA	$V_{GS} = \pm 10V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 6)							
Gate Threshold Voltage	V _{GS(TH)}	-0.4	-0.7	-1.0	V	$V_{DS} = V_{GS}$, $I_D = -250\mu A$	
		_	1.8	5	Ω	$V_{GS} = -4.5V, I_D = -100mA$	
Static Drain-Source On-Resistance	Proven	_	2.3	6		$V_{GS} = -2.5V, I_D = -50mA$	
Static Diami-Source Off-Nesistance	R _{DS(ON)}	_	3	7		$V_{GS} = -1.8V, I_D = -20mA$	
		_	3.4	10		$V_{GS} = -1.5V, I_D = -10mA$	
Diode Forward Voltage		_	-0.6	-1.0	V	$V_{GS} = 0V, I_{S} = -10mA$	
DYNAMIC CHARACTERISTICS (Note 7)	<u> </u>						
Input Capacitance	C _{iss}	_	21.8	_	pF	151/1/	
Output Capacitance		_	2.82	_	pF	$V_{DS} = -15V, V_{GS} = 0V,$ - f = 1.0MHz	
Reverse Transfer Capacitance	C _{rss}	_	1.66	_	pF	1 - 1.00112	
Total Gate Charge	Qg	_	0.35	_	nC	451/1/ 451/	
Gate-Source Charge		_	0.05	_	nC	$V_{GS} = -4.5V, V_{DS} = -15V,$	
Gate-Drain Charge	Q _{gd}	_	0.10	_	nC	$I_D = -200 \text{mA}$	
Turn-On Delay Time	t _{D(ON)}	_	3.5	_	ns		
Turn-On Rise Time Turn-Off Delay Time Turn-Off Fall Time		_	5.2	_	ns	$V_{DD} = -15V, V_{GS} = -4.5V,$	
		_	18.8	_	ns	$R_g = 2\Omega, I_D = -200 \text{mA}$	
		_	8.7	_	ns		

Notes:

- 5. Device mounted on minimum recommended pad layout test board, 10µs pulse duty cycle = 1%.
 6. Short duration pulse test used to minimize self-heating effect.
 7. Guaranteed by design. Not subject to product testing





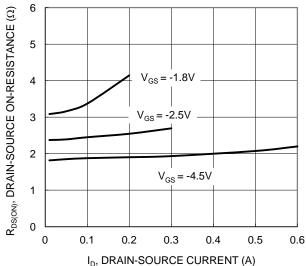


Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage

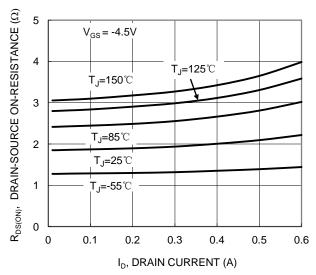
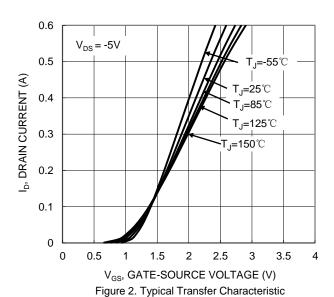
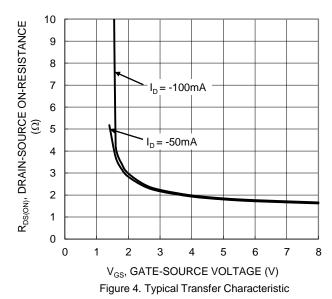


Figure 5. Typical On-Resistance vs. Drain Current and Junction Temperature





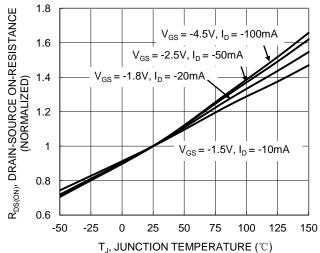


Figure 6. On-Resistance Variation with Junction Temperature



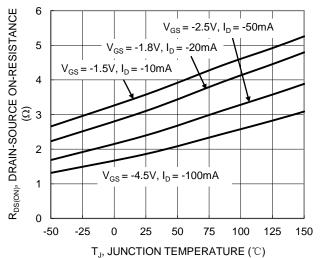
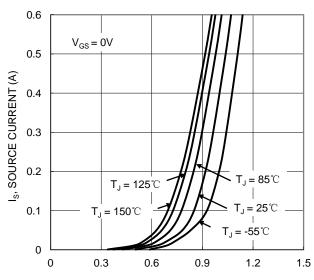
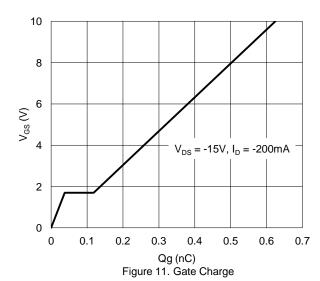


Figure 7. On-Resistance Variation with Junction Temperature



V_{SD}, SOURCE-DRAIN VOLTAGE (V) Figure 9. Diode Forward Voltage vs. Current



1 $V_{GS(TH)}$, GATE THRESHOLD VOLTAGE (V) 0.9 $I_D = -1mA$ 8.0 $I_{D} = -250 \mu A$ 0.7 0.6 0.5 0.4 -50 -25 100 125 150 25 50 75

 $\rm T_{J}, JUNCTION$ TEMPERATURE ($^{\circ}\!\!\rm C)$ Figure 8. Gate Threshold Variation vs. Junction Temperature

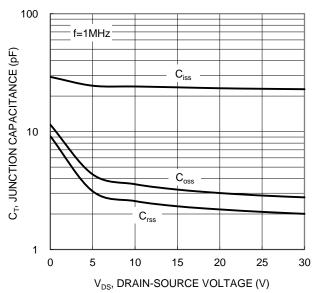
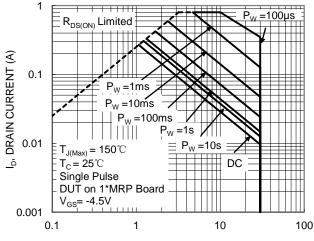


Figure 10. Typical Junction Capacitance



V_{DS}, DRAIN-SOURCE VOLTAGE (V) Figure 12. SOA, Safe Operation Area



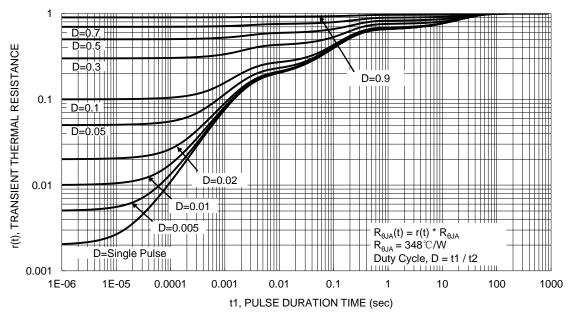


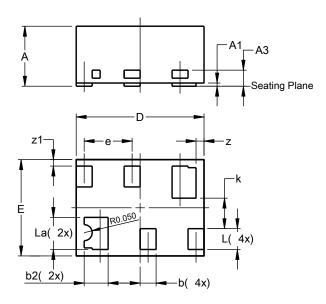
Figure 13. Transient Thermal Resistance



Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

X2-DFN0806-6

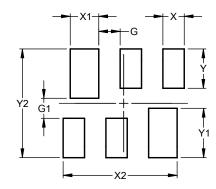


X2-DFN0806-6					
Dim	Min	Max	Тур		
Α		0.40	0.36		
A1	0.00	0.03	0.02		
A3			0.10		
b	0.07	0.15	0.10		
b2	0.10	0.20	0.15		
D	0.75	0.85	0.80		
Е	0.55	0.65	0.60		
е			0.30		
k			0.19		
L	0.10	0.18	0.13		
La	0.17	0.25	0.20		
Z			0.05		
z 1			0.04		
All	All Dimensions in mm				

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

X2-DFN0806-6



Dimensions	Value (in mm)
G	0.150
G1	0.140
Х	0.150
X1	0.200
X2	0.800
Υ	0.275
Y1	0.345
Y2	0.760



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