



DMP32D8UFZ

P-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

BV _{DSS}	R _{DS(ON)} Max	I _D Max T _A = +25°C
-30V	5Ω @ V _{GS} = -4.5V	-0.2A
	6Ω @ V _{GS} = -2.5V	-0.17A
	7Ω @ V _{GS} = -1.8V	-0.16A
	10Ω @ V _{GS} = -1.5V	-0.13A

Description and Applications

This MOSFET has been designed to minimize the on-state resistance (RDS(ON)) yet maintain superior switching performance, making it ideal for high efficiency power management applications.

- General purpose interfacing switches
- Power management functions
- Analog switches

Features and Benefits

- Low Package Profile, 0.42mm Maximum Package Height
- 0.62mm x 0.62mm Package Footprint
- Low On-Resistance
- Very Low Gate Threshold Voltage, -1.0V Max
- ESD Protected Gate
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/104/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please contact us or your local Diodes representative.

https://www.diodes.com/quality/product-definitions/

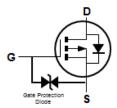
Mechanical Data

- Package: X2-DFN0606-3
- Package Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish NiPdAu over Copper Leadframe. Solderable per MIL-STD-202, Method 208@4
- Weight: 0.001 grams (Approximate)

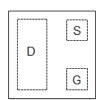








Equivalent Circuit



Top View Package Pin Configuration

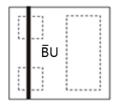
Ordering Information (Note 4)

Part Number	Package	Packing		
Fait Number	Package	Qty.	Carrier	
DMP32D8UFZ-7B	X2-DFN0606-3	10K	Tape & Reel	

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/

Marking Information



Top View

BU = Product Type Marking Code
Bar Denotes Gate and Source Side

DMP32D8UFZ
Document number: DS44182 Rev. 2 - 2

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Maximum Ratings (@ $T_A = +25$ °C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Drain-Source Voltage	VDSS	-30	V
Gate-Source Voltage	Vgss	±10	V
Continuous Drain Current (Note 5) V _{GS} = -4.5V	lo	-200 -150	mA
Pulsed Drain Current (Note 6)	I _{DM}	-900	mA

Thermal Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	
Total Power Dissipation (Note 5)	Steady State	P _D	290	mW
Thermal Resistance, Junction to Ambient (Note 5)	$R_{\theta JA}$	438	°C/W	
Operating and Storage Temperature Range	TJ, TSTG	-55 to +150	°C	

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

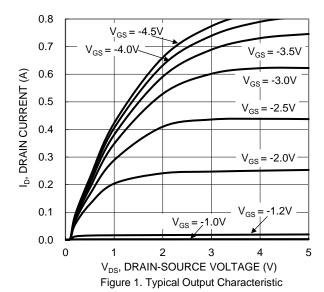
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)							
Drain-Source Breakdown Voltage	BVDSS	-30	_	_	V	$V_{GS} = 0V, I_{D} = -250\mu A$	
Zero Gate Voltage Drain Current @T _C = +25°C	I _{DSS}	_		-100	nA	V _{DS} = -24V, V _{GS} = 0V	
Gate-Source Leakage	I _{GSS}	_	_	±10	μΑ	$V_{GS} = \pm 10V$, $V_{DS} = 0V$	
ON CHARACTERISTICS (Note 7)							
Gate Threshold Voltage	V _{GS(TH)}	-0.4	_	-1.0	V	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$	
		_		5		$V_{GS} = -4.5V, I_{D} = -100mA$	
		_	_	6		$V_{GS} = -2.5V, I_{D} = -50mA$	
Static Drain-Source On-Resistance	R _{DS(ON)}	_	_	7	Ω	$V_{GS} = -1.8V, I_D = -20mA$	
		_	_	10		V _G S = -1.5V, I _D = -10mA	
		_	6	_		$V_{GS} = -1.2V, I_{D} = -1mA$	
Diode Forward Voltage	VsD	_	-0.75	-1.0	V	V _G S = 0V, I _S = -10mA	
DYNAMIC CHARACTERISTICS (Note 8)						•	
Input Capacitance	Ciss	_	17	_	pF		
Output Capacitance	Coss	_	8.9		pF	V _{DS} = -15V, V _{GS} = 0V, f = 1.0MHz	
Reverse Transfer Capacitance	Crss	_	5.1	_	pF	1 - 1.000112	
Total Gate Charge	Qg	_	0.35	_	nC		
Gate-Source Charge	Qgs	_	0.1	_	nC	$V_{GS} = -4.5V, V_{DS} = -5V,$	
Gate-Drain Charge	Qgd	_	0.1	_	nC	I _D = -200mA	
Turn-On Delay Time	t _{D(ON)}	_	6.3	_	ns		
Turn-On Rise Time	t _R	_	24	_	ns	$V_{DD} = -10V$, $V_{GS} = -4.5V$,	
Turn-Off Delay Time	t _{D(OFF)}	_	143	_	ns	$R_G = 6\Omega$, $I_D = -200mA$	
Turn-Off Fall Time	tF	_	97	_	ns		

Notes:

- 5. Device mounted on FR-4 PCB, with minimum recommended pad layout.
- 6. Device mounted on minimum recommended pad layout test board, 10µs pulse duty cycle = 1%.
- 7. Short duration pulse test used to minimize self-heating effect.

 8. Guaranteed by design. Not subject to product testing.





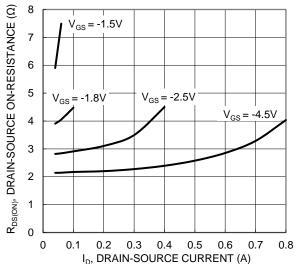


Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage

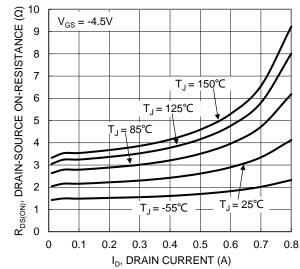
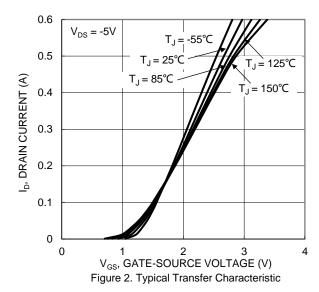
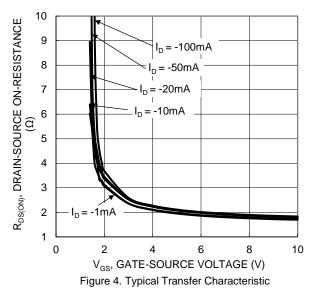


Figure 5. Typical On-Resistance vs. Drain Current and Junction Temperature





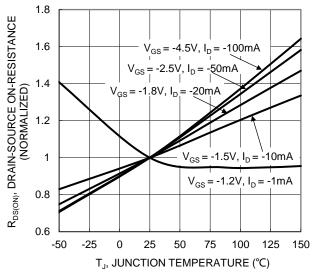


Figure 6. On-Resistance Variation with Junction Temperature



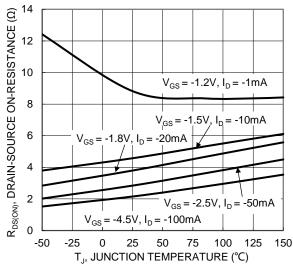
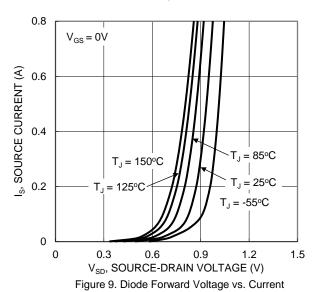
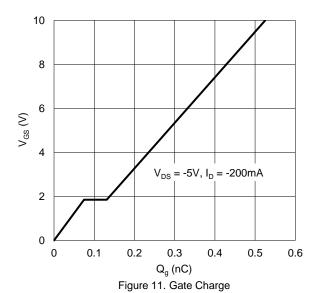


Figure 7. On-Resistance Variation with Junction Temperature





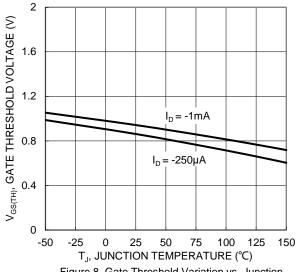
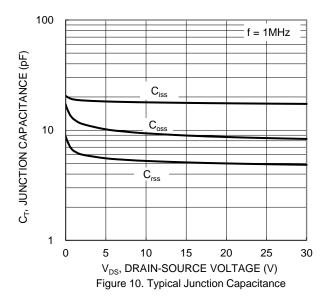
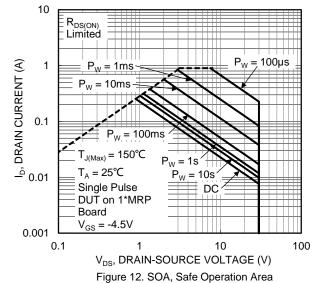


Figure 8. Gate Threshold Variation vs. Junction Temperature







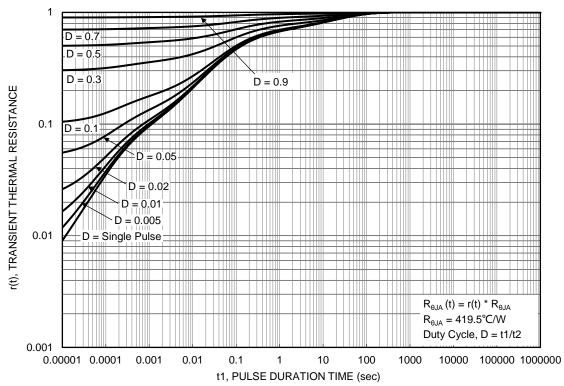


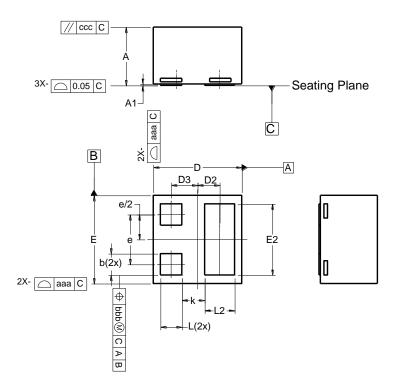
Figure 13. Transient Thermal Resistance



Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

X2-DFN0606-3

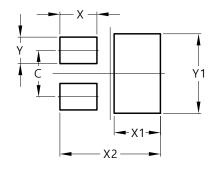


X2-DFN0606-3					
Dim	Min	Max	Тур		
Α	0.36	0.40	0.39		
A 1	0.00	0.05	0.02		
b	0.10	0.20	0.15		
ם	0.57	0.67	0.62		
D2	0.155 BSC				
D3	0.185 BSC				
Е	0.57	0.67	0.62		
E2	0.40	0.60	0.50		
е	0.35 BSC				
k	0.16 REF				
Ĺ	0.10	0.20	0.15		
L2	0.11	0.31	0.21		
aaa	0.08				
bbb	0.07				
CCC	0.05				
All Dimensions in mm					

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

X2-DFN0606-3



Dimensions	Value (in mm)		
С	0.350		
Х	0.280		
X1	0.350		
X2	0.760		
Υ	0.200		
Y1	0.600		



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