



P-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

BV _{DSS}	Rds(on)	I _D T _A = +25°C
0.017	0.9Ω @ V _{GS} = -10V	-0.5A
-30V	1.7Ω @ V _{GS} = -4.5V	-0.36A

Features and Benefits

- Low On-Resistance
- Low Gate Threshold Voltage
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- ESD Protected Gate
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- The DMP31D7LTQ is suitable for automotive applications requiring specific change control; this part is AEC-Q101 qualified, PPAP capable, and manufactured in IATF 16949 certified facilities.

https://www.diodes.com/quality/product-definitions/

Description and Applications

This new generation MOSFET is designed to minimize the on-state resistance (RDS(ON)) yet maintain superior switching performance, making it ideal for high-efficiency power-management applications.

- DC-DC converters
- Load switches
- Power-management functions

Mechanical Data

- Package: SOT523
- Package Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish—Matte Tin Annealed over Alloy 42 Leadframe.
 Solderable per MIL-STD-202, Method 208 (3)
- Terminal Connections: See Diagram
- Weight: 0.002 grams (Approximate)

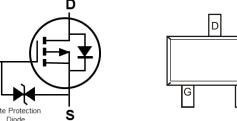




SOT523

Gate Protection Diode S

Top View Equivalent Circuit



Top View

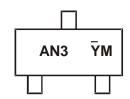
Ordering Information (Note 4)

Port Number	Part Number Package		king
Part Number	Fackage	Qty.	Carrier
DMP31D7LTQ-7	SOT523	3,000	Tape & Reel
DMP31D7LTQ-13	SOT523	10,000	Tape & Reel

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/

Marking Information



AN3 = Product Type Marking Code YM = Date Code Marking \overline{Y} = Year (ex: K = 2023) M = Month (ex: 9 = September)

Date Code Key

Date Code Rey												
Year	2023	2024	2025	2026	2027	2028	2029	2030	2031	2032	2033	2034
Code	K	L	М	N	Р	R	S	Т	U	V	W	Х
	1	l l	l l	l l	l I	l I	1		l I	l I		l I
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec



Maximum Ratings (@ $T_A = +25$ °C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit		
Drain-Source Voltage	VDSS	-30	V		
Gate-Source Voltage	V _{GSS}	±20	V		
Continuous Drain Current (Note 6) Vgs = -4.5V	ID	-0.36 -0.28	А		
Maximum Continuous Body Diode Forward Current	Is	-0.36	A		
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)		IDM	-2.6	A

Thermal Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	
Total Power Dissipation (Note 5)	Steady State	PD	0.26	W
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	Reja	488	°C/W
Total Power Dissipation (Note 6)	Steady State	P _D	0.33	W
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	Reja	377	°C/W
Operating and Storage Temperature Range		TJ, TSTG	-55 to +150	°C

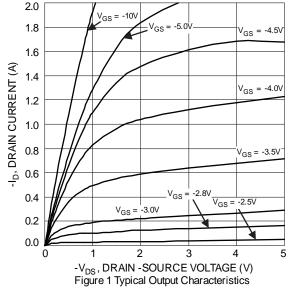
Electrical Characteristics (@TA = +25°C, unless otherwise specified.)

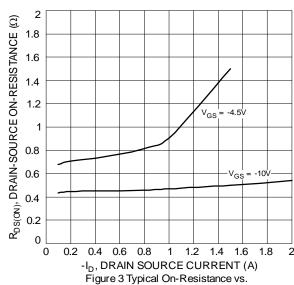
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)							
Drain-Source Breakdown Voltage	BV _{DSS}	-30	_	_	V	$V_{GS} = 0V, I_D = -250\mu A$	
Zero Gate Voltage Drain Current T _J = +25°C	IDSS		_	-1	μΑ	V _{DS} = -24V, V _{GS} = 0V	
Gate-Source Leakage	Igss	_	_	±10	μΑ	$V_{GS} = \pm 16V$, $V_{DS} = 0V$	
ON CHARACTERISTICS (Note 7)							
Gate Threshold Voltage	Vgs(TH)	-1	_	-2.6	V	$V_{DS} = V_{GS}$, $I_D = -250\mu A$	
Static Drain-Source On-Resistance	Descour		0.45	0.9	Ω	$V_{GS} = -10V, I_D = -0.42A$	
Static Drain-Source Off-Resistance	RDS(ON)	_	0.7	1.7	12	$V_{GS} = -4.5V$, $I_{D} = -0.2A$	
Diode Forward Voltage (Note 7)	V_{SD}	_	-0.8	-1.2	V	$V_{GS} = 0V$, $I_{S} = -0.23A$	
DYNAMIC CHARACTERISTICS (Note 8)							
Input Capacitance	Ciss	_	19	_	pF	451/11/ 01/	
Output Capacitance	Coss	_	16	_	pF	V _{DS} = -15V, V _{GS} = 0V, -f = 1.0MHz	
Reverse Transfer Capacitance	Crss	_	3	_	pF	1 = 1.0WHZ	
Gate Resistance	Rg	_	729	_	Ω	$V_{DS} = V_{GS} = 0V, f = 1.0MHz$	
Total Gate Charge	Qg	_	0.36	_	nC	4.51/.)/	
Gate-Source Charge	Qgs	_	0.1	_	nC	Vgs = -4.5V, Vps = -10V,	
Gate-Drain Charge	Qgd	_	0.1	_	nC	$I_D = -250 \text{mA}$	
Turn-On Delay Time	t _D (ON)	_	30	_	ns		
Turn-On Rise Time	t _R	_	74	_	ns	V _{DD} = -10V, V _{GS} = -4.5V,	
Turn-Off Delay Time	tD(OFF)	_	28	_	ns	$R_L = 47\Omega$, $R_G = 10\Omega$,	
Turn-Off Fall Time	tF	_	31	_	ns	$I_D = -200 \text{mA}$	

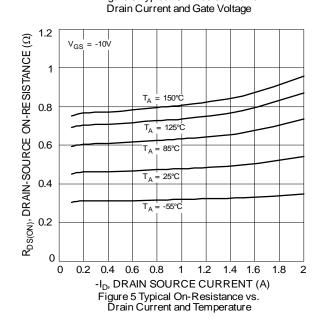
Notes:

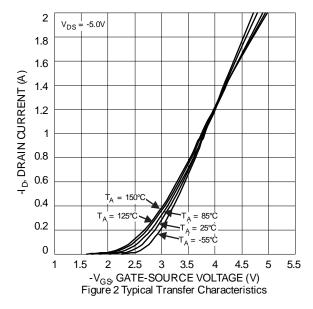
- 5. Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.
- 6. Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.
- 7. Short duration pulse test used to minimize self-heating effect.
- 8. Guaranteed by design. Not subject to production testing.

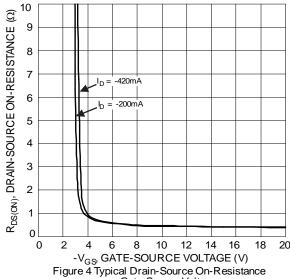


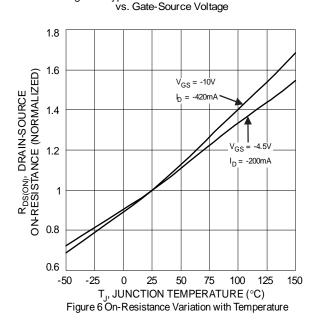




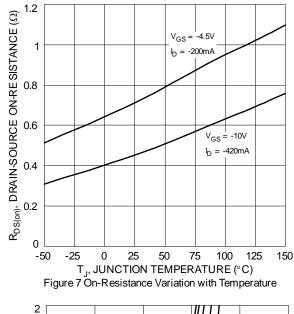


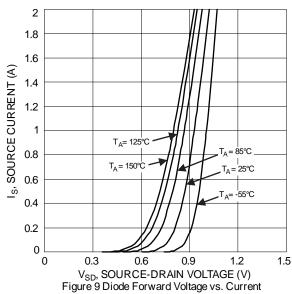


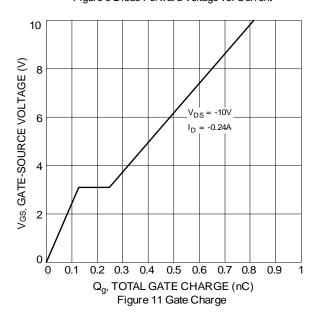












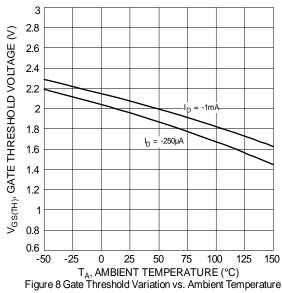
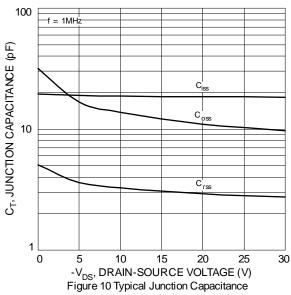
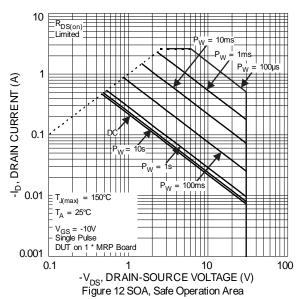
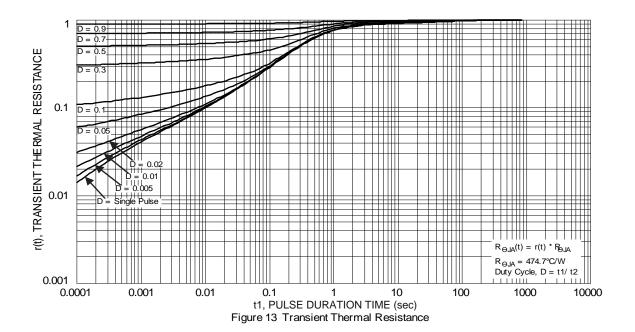


Figure 8 Gate Threshold Variation vs. Ambient Temperature







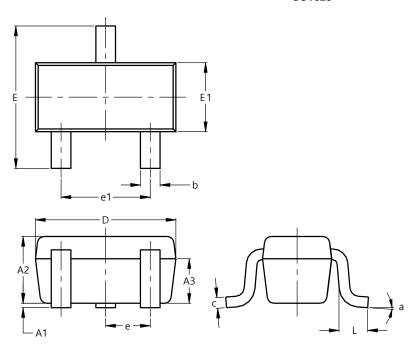




Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT523

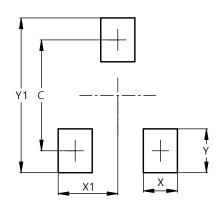


SOT523							
Dim	Min	Max	Тур				
A1	0.00	0.10	0.05				
A2	0.60	0.80	0.75				
A3	0.45	0.65	0.50				
b	0.15	0.30	0.22				
С	0.10	0.20	0.12				
D	1.50	1.70	1.60				
E	1.45	1.75	1.60				
E1	0.75	0.85	0.80				
е	0.50 BSC						
e1	0.90	1.10	1.00				
L	0.20	0.40	0.33				
а	0°		8°				
A	All Dimensions in mm						

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT523



Dimensions	Value (in mm)
С	1.29
Х	0.40
X1	0.70
Y	0.51
Y1	1.80



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