



P-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

BVDSS	Rds(on)	I _D T _A = +25°C
-30V	0.9Ω @ V _{GS} = -10V	-0.81 A
	1.7Ω @ Vgs = -4.5V	-0.58 A

Description and Applications

This MOSFET is designed to minimize the on-state resistance (R_{DS(ON)}) yet maintain superior switching performance, making it ideal for high-efficiency power management applications.

Load Switch

Features and Benefits

- 0.6mm² Footprint—Thirteen Times Smaller than SOT23
- Low Gate Threshold Voltage
- Fast Switching Speed
- ESD Protected Gate
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please contact us or your local Diodes representative. https://www.diodes.com/quality/product-definitions/

Mechanical Data

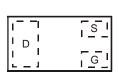
- Package: X1-DFN1006-3
- Package Material: Molded Plastic, "Green" Molding Compound.
 UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish—NiPdAu over Copper Leadframe. Solderable per MIL-STD-202, Method 208
- Weight: 0.001 grams (Approximate)



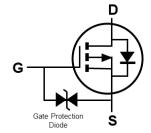




Bottom View



Top View Internal Schematic



Equivalent Circuit

Ordering Information (Note 4)

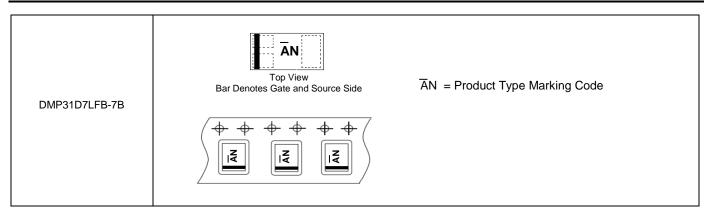
	Part Number	Bookaga	Marking	Reel Size (inches)	Tape Width	Tape Pitch	Packing	
Fait	Fait Nullibei	Package			(mm)	(mm)	Qty.	Carrier
	DMP31D7LFB-7B	X1-DFN1006-3	ĀΝ	7	8	2	10,000	Reel

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3).compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.



Marking Information



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit	
Drain-Source Voltage	VDSS	-30	V		
Gate-Source Voltage	V_{GSS}	±20	V		
Continuous Drain Current (Note 6) $V_{GS} = 4.5V$ Steady $T_A = +25^{\circ}C$ State $T_A = +70^{\circ}C$		l In	-0.81 -0.64	А	
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)			I _{DM}	-2.4	А

Thermal Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Total Power Dissipation (Note 5)	PD	0.53	W
Thermal Resistance, Junction to Ambient (Note 5)	RөJA	236	°C/W
Total Power Dissipation (Note 6)	PD	0.89	W
Thermal Resistance, Junction to Ambient (Note 6)	RөJA	141	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C

Notes:

- 5. Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout. 6. Device mounted on FR-4 substrate PC board, 2oz copper, with 25mm \times 25mm square copper plate.



Electrical Characteristics (@TA = +25°C, unless otherwise specified.)

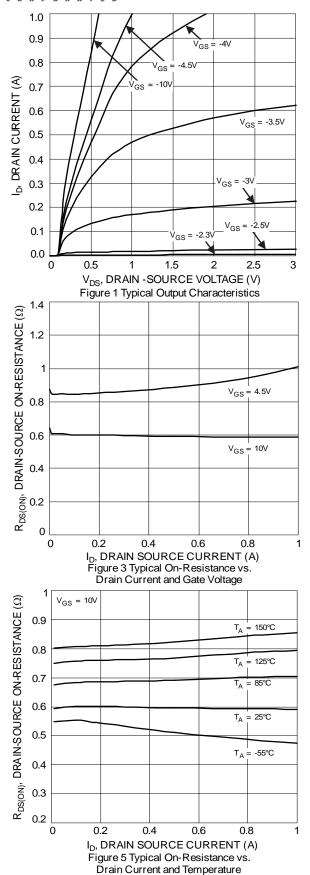
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)							
Drain-Source Breakdown Voltage	BVDSS	-30	1	_	V	$V_{GS} = 0V, I_{D} = -250\mu A$	
Zero Gate Voltage Drain Current T _J = +25°C	I _{DSS}			-1	μΑ	$V_{DS} = -24V$, $V_{GS} = 0V$	
Gate-Source Leakage	Igss	_	_	±10	μΑ	$V_{GS} = \pm 16V$, $V_{DS} = 0V$	
ON CHARACTERISTICS (Note 7)							
Gate Threshold Voltage	Vgs(TH)	-1	_	-2.6	V	$V_{DS} = V_{GS}$, $I_D = -250\mu A$	
Static Drain-Source On-Resistance	D		0.5	0.9	Ω	$V_{GS} = -10V$, $I_{D} = -0.42A$	
Static Dialif-Source Off-Resistance	RDS(ON)		0.8	1.7		$V_{GS} = -4.5V$, $I_D = -0.2A$	
Diode Forward Voltage	VsD		-0.8	-1.2	V	V _G S = 0V, I _S = -0.23A	
DYNAMIC CHARACTERISTICS (Note 8)							
Input Capacitance	Ciss		19	_	pF	.,	
Output Capacitance	Coss		16	_	pF	$V_{DS} = -15V, V_{GS} = 0V,$ f = 1.0MHz	
Reverse Transfer Capacitance	Crss	_	3	_	pF	T = T.OIVII IZ	
Gate Resistance	Rg		729	_	Ω	$V_{DS} = V_{GS} = 0V, f = 1.0MHz$	
Total Gate Charge (VGS = 4.5V)	Qg	_	0.36	_	nC	15/// 45/// 40//	
Gate-Source Charge	Q_{gs}	_	0.1	_	nC	$V_{GS} = -4.5V, V_{DS} = -10V,$ $I_{D} = -250\text{mA}$	
Gate-Drain Charge	Q_{gd}	_	0.1	_	nC		
Turn-On Delay Time	tD(ON)	_	30	_	ns	$V_{DD} = -10V, V_{GS} = -4.5V,$ $R_{L} = 47\Omega, R_{G} = 10\Omega,$ $I_{D} = -200\text{mA}$	
Turn-On Rise Time	t _R	_	74	_	ns		
Turn-Off Delay Time	t _{D(OFF)}	_	28	_	ns		
Turn-Off Fall Time	tF	_	31	_	ns	1D = -20011IA	

Notes:

^{7.} Short duration pulse test used to minimize self-heating effect. 8. Guaranteed by design. Not subject to product testing.







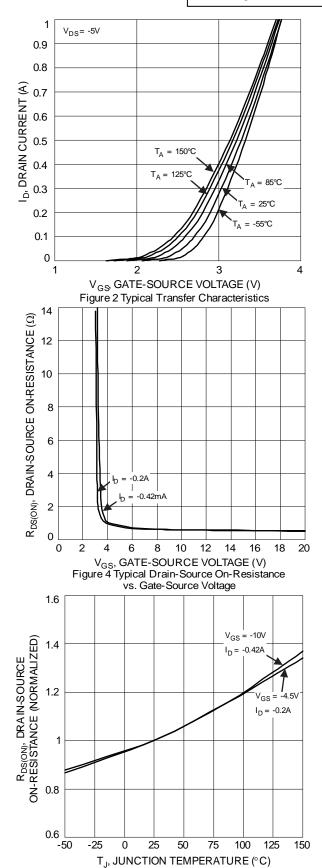
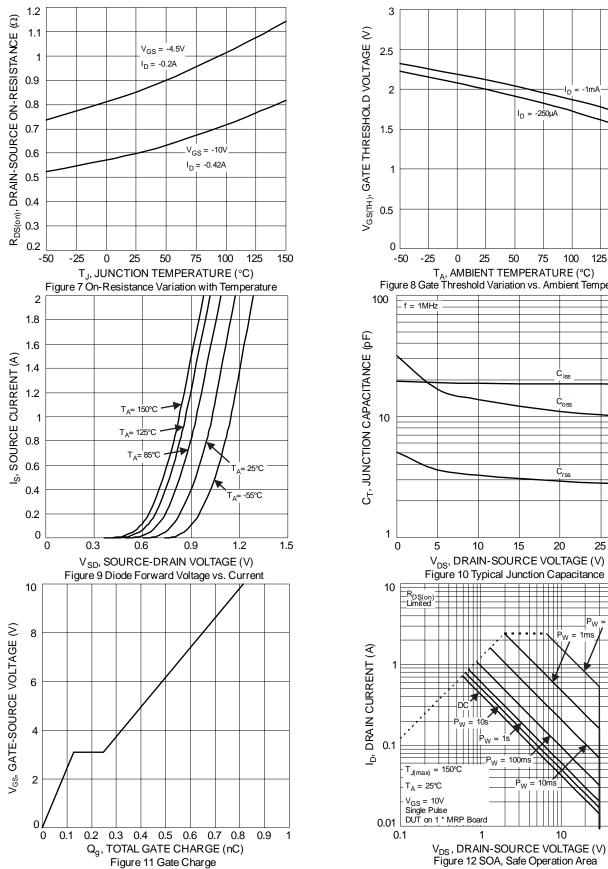
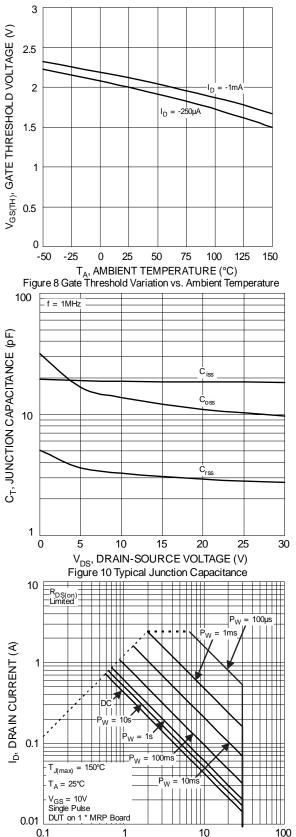


Figure 6 On-Resistance Variation with Temperature

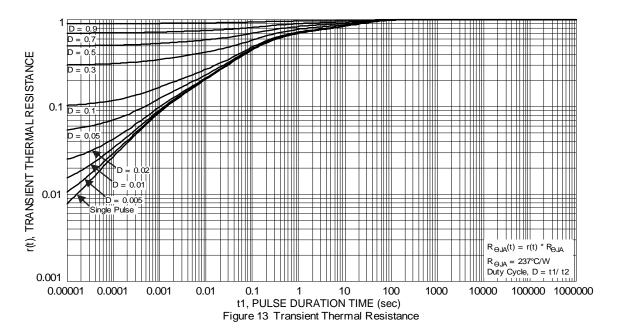










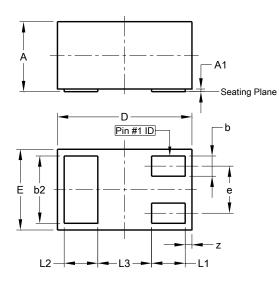




Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

X1-DFN1006-3

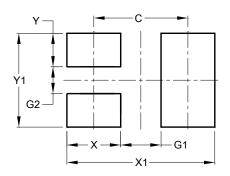


X1-DFN1006-3						
Dim	Min	Max	Тур			
Α	0.47	0.53	0.50			
A1	0.00	0.05	0.03			
b	0.10	0.20	0.15			
b2	0.45	0.55	0.50			
D	0.95	1.075	1.00			
Е	0.55	0.675	0.60			
е	•	-	0.35			
L1	0.20	0.30	0.25			
L2	0.20	0.30	0.25			
L3	-	-	0.40			
z	0.02	0.08	0.05			
All Dimensions in mm						

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

X1-DFN1006-3



Dimensions	Value (in mm)
С	0.70
G1	0.30
G2	0.20
Х	0.40
X1	1.10
Y	0.25
V1	0.70



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