



DMP3028LPSQ

P-CHANNEL ENHANCEMENT MODE MOSFET PowerDI5060-8

Product Summary

BV _{DSS}	R _{DS(ON)} Max	I _D Max T _C = +25°C
	28mΩ @ V _{GS} = -10V	-21A
-30V	38mΩ @ V _{GS} = -4.5V	-18A

Description and Applications

This MOSFET is designed to meet the stringent requirements of Automotive applications. It is qualified to AEC-Q101, supported by a PPAP and is ideal for use in:

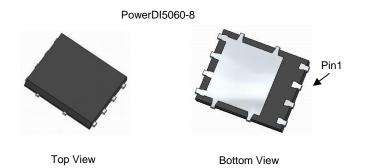
- Backlighting
- Power Management Functions
- DC-DC Converters

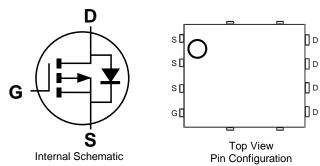
Features and Benefits

- Low R_{DS(ON)} Minimizes On-State Losses
- Small Form Factor Thermally Efficient Package Enables Higher Density End Products
- 100% Unclamped Inductive Switching Ensures More Reliability
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability
- PPAP Capable (Note 4)

Mechanical Data

- Case: PowerDI5060-8
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections Indicator: See Diagram
- Terminals: Finish Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 3
- Weight: 0.097 grams (Approximate)





Ordering Information (Note 5)

Part Number	Case	Packaging
DMP3028LPSQ-13	PowerDI5060-8	2,500/Tape & Reel

Notes:

tes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.

2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. Automotive products are AEC-Q101 qualified and are PPAP capable. Refer to https://www.diodes.com/quality/product-compliance-definitions/.

5. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information



) || = Manufacturer's Marking P3028LP = Product Type Marking Code YYWW = Date Code Marking YY = Year (ex: 17 = 2017) WW = Week (01 to 53)

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Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Drain-Source Voltage		V _{DSS}	-30	V
Gate-Source Voltage		V _{GSS}	±20	V
Continuous Drain Current, $V_{GS} = -10V$ (Note 8)	T _C = +25°C T _C = +70°C	ID	-21 -17	А
Maximum Continuous Body Diode Forward Current (Note 8)		Is	-20	А
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)		I _{DM}	-70	А
Pulsed Body Diode Forward Current (10µs Pulse, Duty Cycle	= 1%)	I _{SM}	-70	А
Avalanche Current, L = 0.1mH (Note 9)		I _{AS}	-21.4	А
Avalanche Energy, L = 0.1mH (Note 9)		EAS	22	mJ

Thermal Characteristics

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 6)		PD	1.28	W
Thermal Resistance, Junction to Ambient (Note 6) Steady State		$R_{\theta JA}$	100	°C/W
Total Power Dissipation (Note 7)		PD	2.12	W
Thermal Resistance, Junction to Ambient (Note 7)	Steady State	R _{0JA}	60	°C/W
Total Power Dissipation (Note 8)	•	PD	35	W
Thermal Resistance, Junction to Case (Note 8)		$R_{\theta JC}$	3.0	°C/W
Operating and Storage Temperature Range		TJ, TSTG	-55 to +150	°C

Electrical Characteristics (T_A = +25°C, unless otherwise specified.)

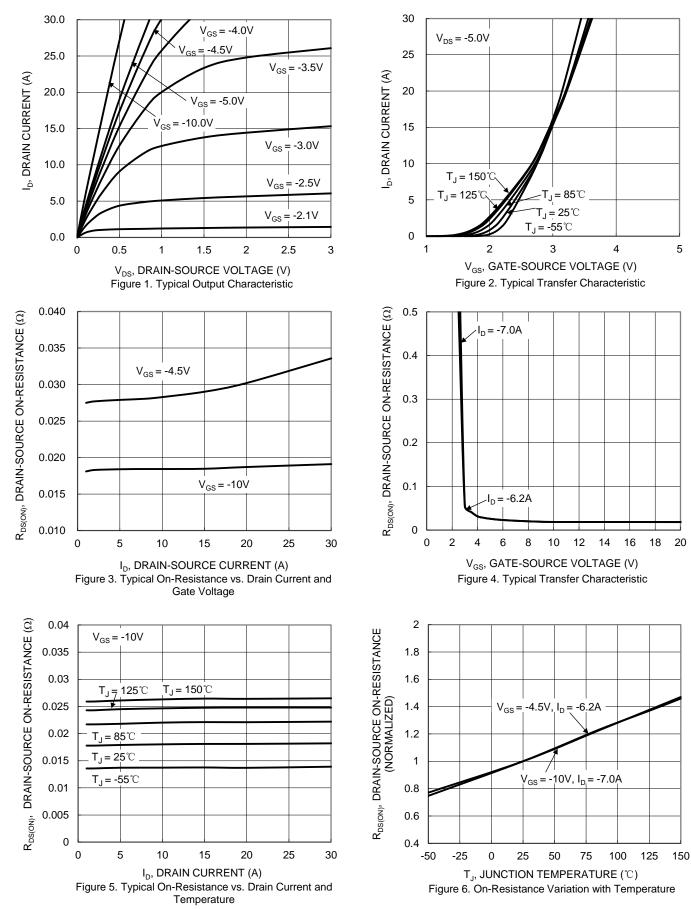
Characteristic	Symbol	Min	Тур	Мах	Unit	Test Condition	
OFF CHARACTERISTICS (Note 10)	Symbol	IVIIII	тур	IVIAX	Unit	Test condition	
Drain-Source Breakdown Voltage	BV _{DSS}	-30	_		V	$V_{GS} = 0V, I_{D} = -250 \mu A$	
Zero Gate Voltage Drain Current	I _{DSS}	_	_	-1	μA	$V_{DS} = -30V, V_{GS} = 0V$	
Gate-Source Leakage	IGSS	_	_	±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 10)	000			I	l		
Gate Threshold Voltage	V _{GS(TH)}	-1.0	-1.3	-2.4	V	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$	
		—	18	28	_	V _{GS} = -10V, I _D = -7A	
Static Drain-Source On-Resistance	R _{DS(ON)}	—	28	38	mΩ	V _{GS} = -4.5V, I _D = -6.2A	
Diode Forward Voltage	V _{SD}	_	-0.7	-1.2	V	$V_{GS} = 0V, I_{S} = -2.1A$	
DYNAMIC CHARACTERISTICS (Note 11)			•			•	
Input Capacitance	C _{iss}	_	1372	_	pF		
Output Capacitance	C _{oss}	_	161	_	pF	V _{DS} = -15V, V _{GS} = 0V, − f = 1.0MHz	
Reverse Transfer Capacitance	C _{rss}	_	127	_	pF		
Gate Resistance	Rg	_	8.5	_	Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1.0MHz$	
Total Gate Charge (V _{GS} = -4.5V)	Qg	_	11	_	nC		
Total Gate Charge (V _{GS} = -10V)	Qg	_	22	_	nC		
Gate-Source Charge	Q _{gs}	_	3.0	_	nC	V _{DS} = -15V, I _D = -7A	
Gate-Drain Charge	Q _{gd}	_	3.7	_	nC	-	
Turn-On Delay Time	t _{D(ON)}	_	4.8		ns		
Turn-On Rise Time	t _R	_	5.5		ns	$V_{DD} = -15V, V_{GS} = -10V,$ $R_g = 6\Omega, I_D = -7A$	
Turn-Off Delay Time	t _{D(OFF)}	_	32.8		ns		
Turn-Off Fall Time	tF	_	17.74		ns		
Reverse Recovery Time	t _{RR}	_	10.8		ns		
Reverse Recovery Charge	Q _{RR}	_	3.4	_	nC	I _S = -7A, dl/dt = 100A/µs	

 Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.
Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.
Thermal resistance from junction to soldering point (on the exposed drain pad). Notes:

9. I_{AS} and E_{AS} ratings are based on low frequency and duty cycles to keep $T_J = +25^{\circ}C$. 10. Short duration pulse test used to minimize self-heating effect. 11. Guaranteed by design. Not subject to product testing.

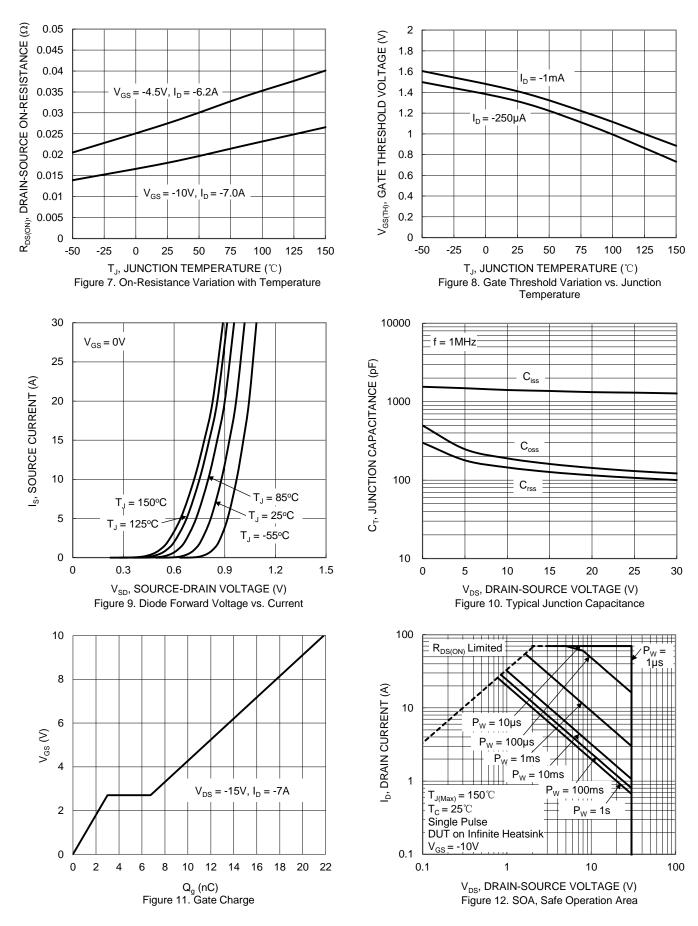


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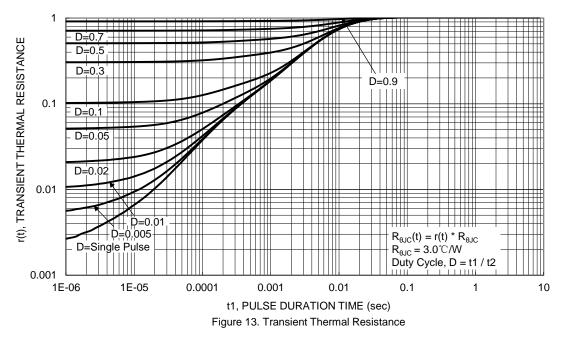


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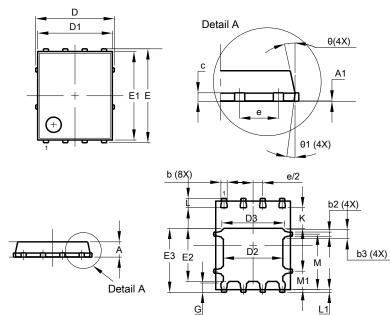




Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

PowerDI5060-8



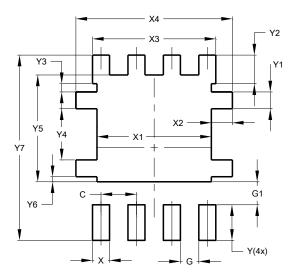
	PowerDI5060-8					
Dim	Min	Max	Тур			
Α	0.90	1.10	1.00			
A1	0.00	0.05	-			
b	0.33	0.51	0.41			
b2	0.200	0.350	0.273			
b3	0.40	0.80	0.60			
С	0.230	0.330	0.277			
D		5.15 BSC	;			
D1	4.70	5.10	4.90			
D2	3.70	4.10	3.90			
D3	3.90	4.30	4.10			
E		6.15 BSC				
E1	5.60	6.00	5.80			
E2	3.28	3.68	3.48			
E3	3.99	4.39	4.19			
е		1.27 BSC				
G	0.51	0.71	0.61			
K	0.51	-	-			
L	0.51	0.71	0.61			
L1	0.100	0.200	0.175			
М	3.235	4.035	3.635			
M1	1.00	1.40	1.21			
Θ	10°	12°	11°			
Θ1	6°	8°	7°			
AI	All Dimensions in mm					

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

PowerDI5060-8

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Dimensions	Value (in mm)			
С	1.270			
G	0.660			
G1	0.820			
Х	0.610			
X1	4.100			
X2	0.755			
X3	4.420			
X4	5.610			
Y	1.270			
Y1	0.600			
Y2	1.020			
Y3	0.295			
Y4	1.825			
Y5	3.810			
Y6	0.180			
Y7	6.610			



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