



P-CHANNEL ENHANCEMENT MODE MOSFET PowerDI5060-8

Product Summary

BV _{DSS}	R _{DS(ON)}	I _D Tc = +25°C
-30V	$7.5 \text{m}\Omega$ @ V _{GS} = -10V	-92A
-307	$11m\Omega$ @ V _{GS} = -4.5V	-76A

Description

This new generation MOSFET is designed to minimize R_{DS(ON)} yet maintain superior switching performance. This device is ideal for use in notebook battery power-management and load switch.

Applications

- Notebook battery power-managements
- DC-DC converters
- Load switches

Features

- Thermally Efficient Package Cooler Running Applications
- High Conversion Efficiency
- Low R_{DS(ON)} Minimizes On-State Losses
- Low Input Capacitance
- Fast Switching Speed
- <1.1mm Package Profile Ideal for Thin Applications
- ESD HBM Protected up to 1kV
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/104/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please contact us or your local Diodes representative.

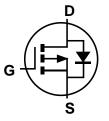
https://www.diodes.com/quality/product-definitions/

Mechanical Data

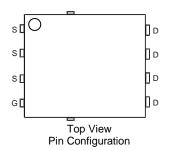
- Package: PowerDI[®]5060-8
- Package Material: Molded Plastic, "Green" Molding Compound.
 UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Diagram Below
- Terminals: Finish 100% Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 (3)
- Weight: 0.097 grams (Approximate)



Top View Bottom View



Internal Schematic



Ordering Information (Note 4)

Part Number	Poekage	Packing		
Part Number	Package	Qty.	Carrier	
DMP3006LPSW-13	PowerDI5060-8 (SWP) (Type UX)	2,500	Tape & Reel	

Notes:

- 1. EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. All applicable RoHS exemptions applied.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information





Maximum Ratings (@ $T_A = +25$ °C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit		
Drain-Source Voltage	VDSS	-30	V		
Gate-Source Voltage			Vgss	±20	V
Continuous Drain Current (Note 7) Vgs = -10V	Steady State	$T_C = +25$ °C $T_C = +70$ °C	lD	-92 -74	А
Continuous Drain Current (Note 6) V _{GS} = -10V	Steady State	$T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$	lo	-15 -12	А
Pulsed Drain Current (380µs Pulse, Duty Cycle = 1%	lрм	-359	Α		
Maximum Continuous Body Diode Forward Current (Is	-76	Α		
Pulsed Body Diode Forward Current (380µs Pulse, D	I _{SM}	-359	Α		
Avalanche Current, L = 0.1mH (Note 8)	las	-55	Α		
Avalanche Energy, L = 0.1mH (Note 8)	Eas	153	mJ		

Thermal Characteristics

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 5)		PD	1.6	W
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	RθJA	77.9	°C/W
Total Power Dissipation (Note 6)		P_{D}	2.4	W
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	RθJA	50.5	°C/W
Thermal Resistance, Junction to Case (Note 7)		Rejc	1.3	°C/W
Operating and Storage Temperature Range		TJ, TSTG	-55 to +150	°C

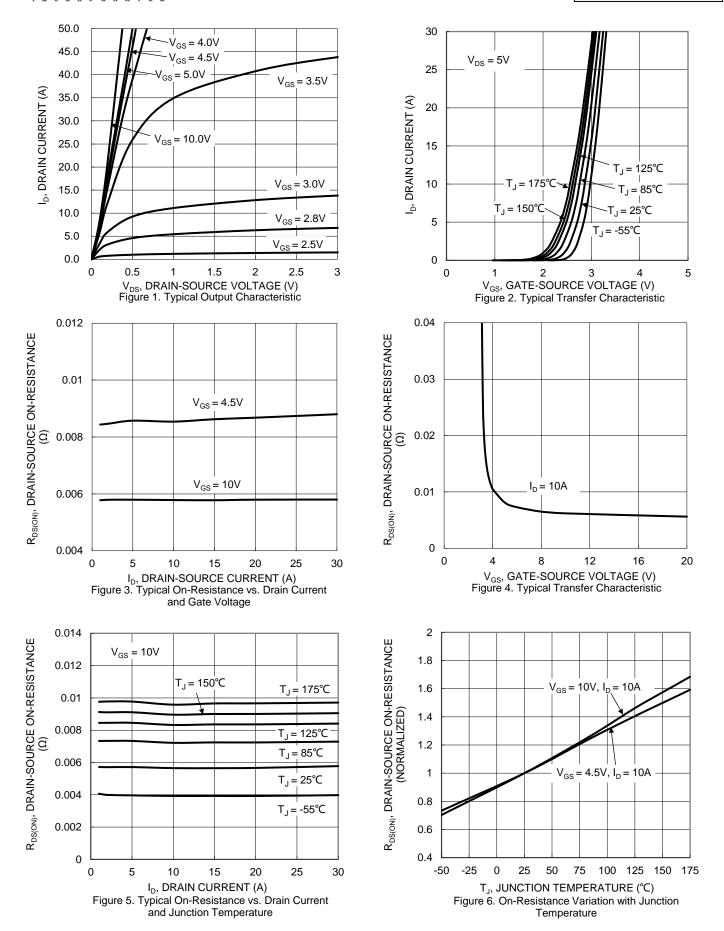
Electrical Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 9)							
Drain-Source Breakdown Voltage	BVDSS	-30	_		V	V _G S = 0V, I _D = -250µA	
Zero Gate Voltage Drain Current	I _{DSS}	-	_	-1.0	μA	V _{DS} = -30V, V _{GS} = 0V	
Gate-Source Leakage	Igss	1	_	±100	nA	$V_{GS} = \pm 20V$, $V_{DS} = 0V$	
ON CHARACTERISTICS (Note 9)							
Gate Threshold Voltage	V _{GS(TH)}	-1.1	_	-2.1	V	$V_{DS} = V_{GS}$, $I_D = -250\mu A$	
Static Drain-Source On-Resistance	D- avanti	1	6	7.5	mΩ	Vgs = -10V, ID = -10A	
Static Diani-Source On-Resistance	R _{DS(ON)}		8.8	11	11152	$V_{GS} = -4.5V$, $I_{D} = -10A$	
Diode Forward Voltage	VsD	1	-0.7	-1.0	V	V _G S = 0V, I _S = -1A	
DYNAMIC CHARACTERISTICS (Note 10)							
Input Capacitance	Ciss	_	5639	_	pF	\\ 45\\\\\ 0\\	
Output Capacitance	Coss	_	500	_	pF	$V_{DS} = -15V, V_{GS} = 0V,$ - f = 1.0MHz	
Reverse Transfer Capacitance	Crss	_	394	_	pF	1 = 1.000112	
Gate Resistance	Rg	_	6.4	_	Ω	$V_{DS} = 0V$, $V_{GS} = 0V$, $f = 1MHz$	
Total Gate Charge (V _{GS} = -10V)	Qg	_	106	_	nC	V _{DS} = -11.5V, I _D = -10A	
Total Gate Charge (V _{GS} = -4.5V)	Qg	_	51	_	nC	V 45V V 45V	
Gate-Source Charge	Q _{gs}	_	15	_	nC	V _{DS} = -15V, V _{GS} = -4.5V,	
Gate-Drain Charge	Q _{gd}	_	20	_	nC	I _D = -11.5A	
Turn-On Delay Time	t _{D(ON)}		6.3	_	ns		
Turn-On Rise Time	t _R	-	7.8	_	ns	V _{DS} = -15V, V _{GEN} = -10V,	
Turn-Off Delay Time	tD(OFF)	_	226.7	_	ns	$R_G = 6\Omega$, $I_D = -11.5A$	
Turn-Off Fall Time	tF	-	93		ns	<u>]</u>	

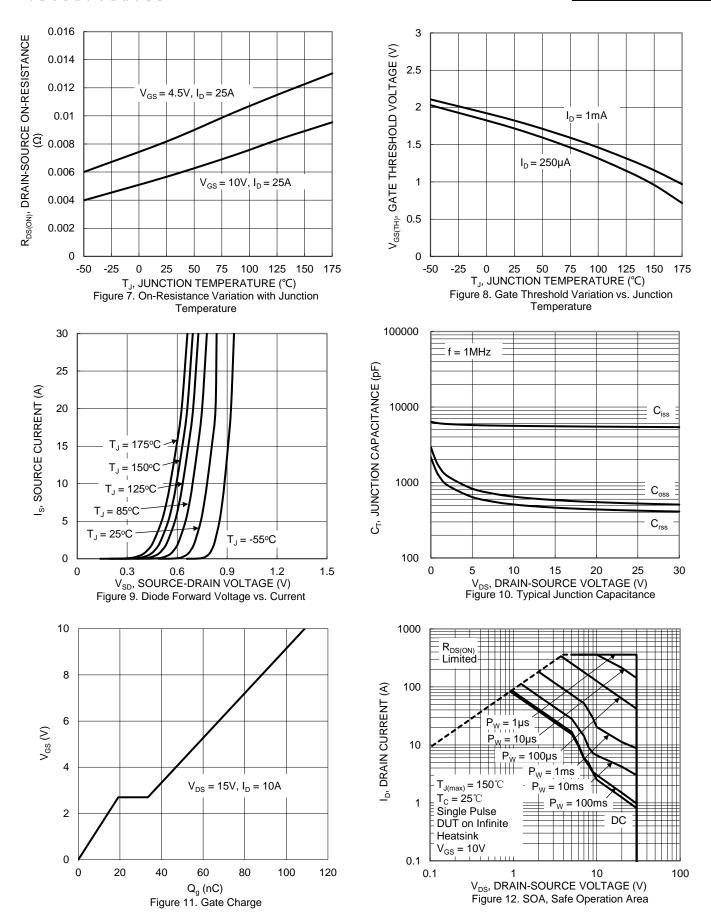
 Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.
 Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.
 Thermal resistance from junction to soldering point (on the exposed drain pad). Notes:

- 8. I_{AS} and E_{AS} ratings are based on low frequency and duty cycles to keep T_J = +25°C.
- 9. Short duration pulse test used to minimize self-heating effect.
- 10. Guaranteed by design. Not subject to product testing.











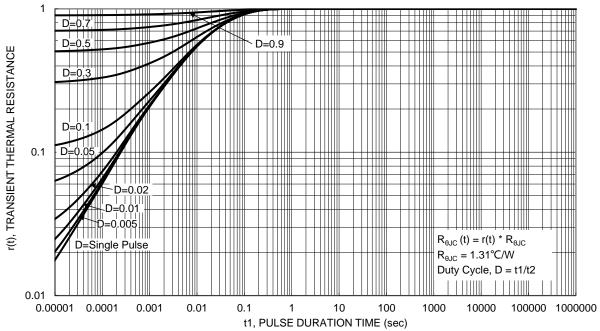


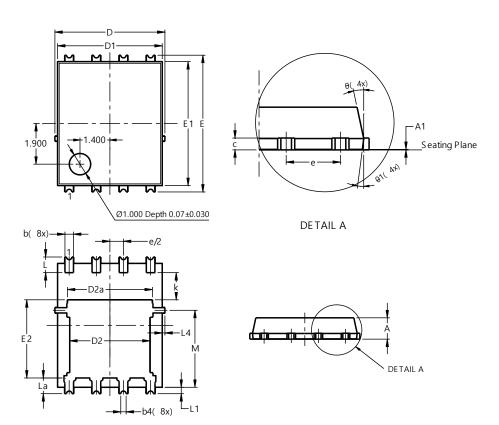
Figure 13. Transient Thermal Resistance



Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

PowerDI5060-8 (SWP) (Type UX)

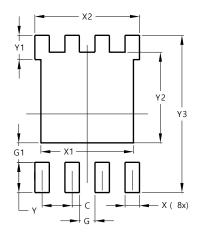


PowerDI5060-8 (SWP)					
(Type UX)					
Dim	Min	Max	Тур		
Α	0.90	1.10	1.00		
A1	0	0.05			
b	0.30	0.50	0.41		
b2	0.20	0.35	0.25		
b4).25REF	-		
С	0.230	0.330	0.277		
D	5	.15 BS0)		
D1	4.70	5.10	4.90		
D2	3.56	3.96	3.76		
D2a	3.78 4.18 3.98				
Е	6	.40 BS0)		
E1	5.60	6.00	5.80		
E2	3.46	3.86	3.66		
E2a	4.195	4.595	4.395		
е	1.27BSC				
k	1.05				
L	0.635	0.835	0.735		
La	0.635	0.835	0.735		
L1	0.200	0.400	0.300		
L1a	0.050REF				
L4	0.025	0.225	0.125		
М	3.205	4.005	3.605		
θ	10°	12°	11°		
θ1	6°	8°	7°		
All Dimensions in mm					

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

PowerDI5060-8 (SWP) (Type UX)



Dimensions	Value (in mm)		
С	1.270		
U	1.270		
G	0.660		
G1	0.820		
Х	0.610		
X1	4.100		
X2	4.420		
Y	1.270		
Y1	1.020		
Y2	3.810		
Y3	6.610		



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