



20V P-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

BV _{DSS}	R _{DS(ON)} Max	I _D Max T _A = +25°C
-20V	1.9Ω @ V _{GS} = -4.5V	-360mA
	2.4Ω @ V _{GS} = -2.5V	-320mA
	3.4Ω @ V _{GS} = -1.8V	-270mA
	5.0Ω @ V _{GS} = -1.5V	-220mA

Description

This MOSFET has been designed to minimize the on-state resistance (RDS(ON)) yet maintain superior switching performance, making it ideal for high-efficiency power-management applications.

Applications

- · General-purpose interfacing switches
- Power-management functions
- Analog switches

Features and Benefits

- Low Package Profile, 0.4mm Maximum Package Height
- 0.48mm² Package Footprint, 16 Times Smaller than SOT23
- Low On-Resistance
- Very Low Gate Threshold Voltage, 1.0V max
- ESD Protected Gate
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/104/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please contact us or your local Diodes representative. https://www.diodes.com/quality/product-definitions/

Mechanical Data

S G

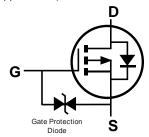
- Package: X2-DFN0806-3
- Package Material: Molded Plastic, "Green" Molding Compound.
 UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish NiPdAu over Copper Leadframe.
 Solderable per MIL-STD-202, Method 208 @4
- Weight: 0.001 grams (Approximate)





X2-DFN0806-3





Equivalent Circuit

Ordering Information (Note 4)

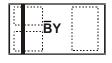
Part Number	Paskage	Packing		
Fait Number	Package	Qty.	Carrier	
DMP22D5UFA-7B	X2-DFN0806-3	10k	Tape & Reel	

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information

DMP22D5UFA-7B



BY = Product Type Marking Code

Top View Bar Denotes Gate and Source Side



Maximum Ratings (@ $T_A = +25$ °C, unless otherwise specified.)

Characteristic	Symbol	Value	Units		
Drain-Source Voltage			VDSS	-20	V
Gate-Source Voltage	Vgss	±8	V		
Continuous Drain Current (Note 5) VGS = 4.5V	Steady State	$T_A = +25$ °C $T_A = +70$ °C	l _D	-360 -280	mA
Maximum Continuous Body Diode Forward Current (Note 5)			Is	-560	mA
Pulsed Drain Current (Note 6)	I _{DM}	-800	mA		

Thermal Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Units	
Total Power Dissipation (Note 5)	Steady State	PD	370	mW
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	$R_{\theta JA}$	337	°C/W
Operating and Storage Temperature Range	TJ, TSTG	-55 to +150	°C	

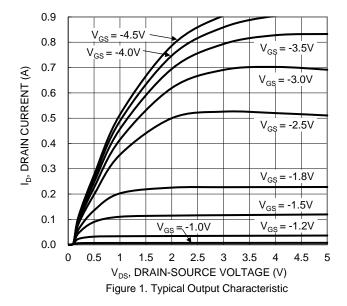
Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

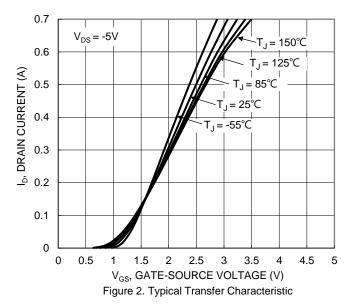
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)						
Drain-Source Breakdown Voltage	BVDSS	-20	_	_	V	$V_{GS} = 0V, I_{D} = -250\mu A$
Zero Gate Voltage Drain Current @T _C = +25°C				-1	μA	$V_{DS} = -16V, V_{GS} = 0V$
Gate-Source Leakage	Igss			±10	μΑ	$V_{GS} = \pm 5V$, $V_{DS} = 0V$
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	V _{GS(TH)}	-0.4	_	-1.0	V	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$
			1.7	1.9	Ω	$V_{GS} = -4.5V, I_{D} = -100mA$
Static Drain-Source On-Resistance	Dagger	-	2.1	2.4		$V_{GS} = -2.5V, I_{D} = -50mA$
Static Dialii-Source Off-Resistance	Rds(on)	_	3.0	3.4		$V_{GS} = -1.8V, I_{D} = -20mA$
			3.7	5.0		$V_{GS} = -1.5V, I_D = -10mA$
Diode Forward Voltage		-0.4	-0.6	-1.0	V	$V_{DS} = V_{GS}$, $I_D = -250\mu A$
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	C _{iss}	_	17	_	pF	.,
Output Capacitance		_	4.1	_	pF	V _{DS} = -16V, V _{GS} = 0V -f = 1.0MHz
Reverse Transfer Capacitance	Crss	_	2.7	_	pF	1 - 1.000112
Total Gate Charge	Qg	_	0.3	_	nC	15)/)/ 10)/
Gate-Source Charge	Qgs	_	0.04	_	nC	$V_{GS} = -4.5V, V_{DS} = -10V$ - In = -250mA
Gate-Drain Charge	Q_{gd}	_	0.1	_	nC	- ID = -230ITIA
Turn-On Delay Time	tD(ON)	_	7.3	_	ns	
Turn-On Rise Time		_	20.7	_	ns	$V_{DD} = -15V, V_{GS} = -4.5V$
Turn-Off Delay Time		_	185	_	ns	$R_G = 2\Omega$, $I_D = -200 \text{mA}$
Turn-Off Fall Time	tF	_	97	_	ns	

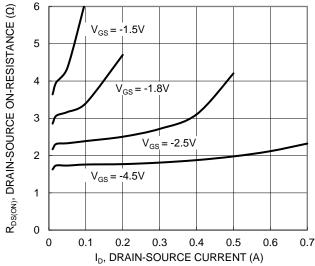
Notes:

- 5. Device mounted on FR-4 PCB, with minimum recommended pad layout.
 6. Device mounted on minimum recommended pad layout test board, 10µs pulse duty cycle = 1%.
 7. Short duration pulse test used to minimize self-heating effect.
 8. Guaranteed by design. Not subject to product testing.









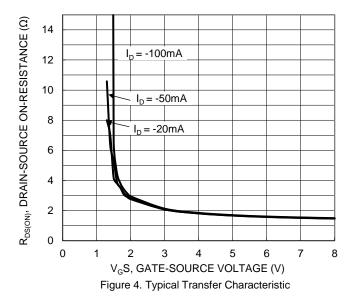
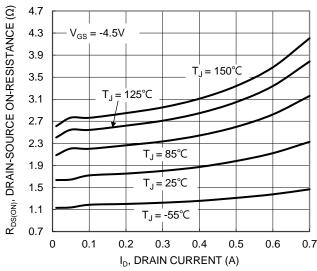


Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage



1.7 R_{DS(ON)}, DRAIN-SOURCE ON-RESISTANCE (NORMALIZED) $-1.8V, I_D = -20mA$ 1.5 $V_{GS} = -2.5V, I_{D} = -50mR$ $V_{GS} = -4.5 \text{V}, I_D = -100 \text{m/s}$ 1.3 1.1 $V_{GS} = -1.5V, I_{D} = -10mA$ 0.9 0.7 0.5 -50 -25 25 50 75 100 150 T_J, JUNCTION TEMPERATURE (°C)

Figure 5. Typical On-Resistance vs. Drain Current and Junction Temperature

Figure 6. On-Resistance Variation with Junction Temperature



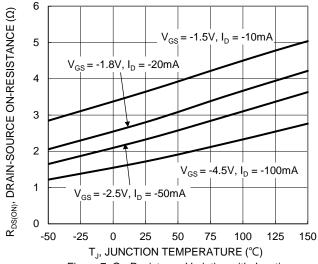


Figure 7. On-Resistance Variation with Junction Temperature

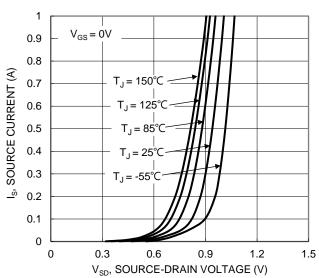


Figure 9. Diode Forward Voltage vs. Current

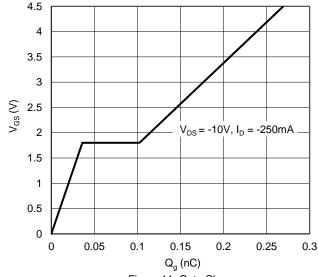


Figure 11. Gate Charge

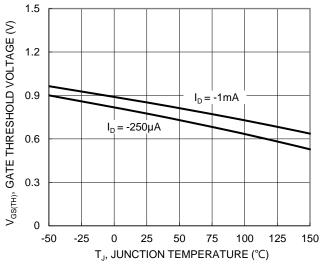
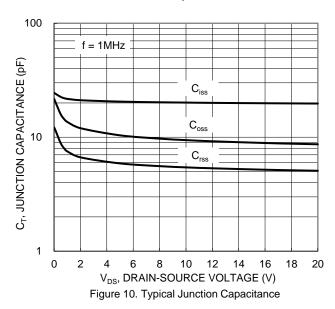


Figure 8. Gate Threshold Variation vs. Junction Temperature



 $\begin{array}{c} R_{\text{DS(ON)}} \\ \text{Limited} \end{array}$ $= 100 \mu s$ ID, DRAIN CURRENT (A) 0.1 = 10 ms $T_{J(Max)} = 150$ °C = 100 ms $T_A = 25^{\circ}C$ Single Pulse DUT on 1*MRP Board $V_{GS} = -4.5V$ 0.01 0.1 100 10 V_{DS} , DRAIN-SOURCE VOLTAGE (V)



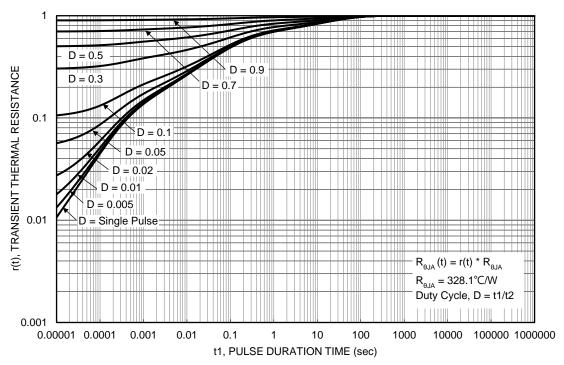


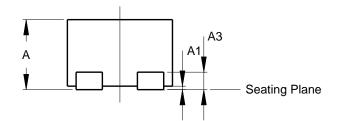
Figure 13. Transient Thermal Resistance

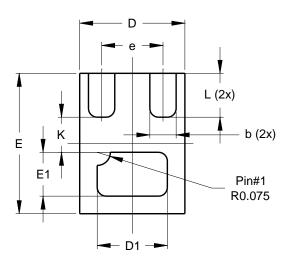


Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

X2-DFN0806-3



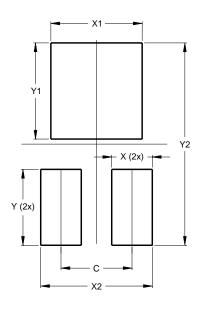


X2-DFN0806-3					
Dim	Min	Max	Тур		
Α	0.375	0.40	0.39		
A1	0	0.05	0.02		
A3	-	-	0.10		
b	0.10	0.20	0.15		
D	0.55	0.65	0.60		
D1	0.35	0.45	0.40		
Е	0.75	0.85	0.80		
E1	0.20	0.30	0.25		
е	-	-	0.35		
K	-	-	0.20		
L	0.20	0.30	0.25		
All Dimensions in mm					

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

X2-DFN0806-3



Dimensions	Value		
Dilliensions	(in mm)		
C	0.350		
Х	0.200		
X1	0.450		
X2	0.550		
Υ	0.375		
Y1	0.475		
Y2	1 000		



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