

P-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

BV _{DSS}	R _{DS(ON)} max	I _D max T _A = +25°C
-20V	100mΩ @ V _{GS} = -4.5V	-2.0A
	120mΩ @ V _{GS} = -2.5V	-1.9A
	160mΩ @ V _{GS} = -1.8V	-1.6A

Description

This MOSFET is designed to minimize the on-state resistance (RDS(ON)) yet maintain superior switching performance, making it ideal for high-efficiency power management applications.

Applications

- Motor Control
- Power Management Functions
- Backlighting

Features and Benefits

- Low Input Capacitance
- · Fast Switching Speed
- Low Input/Output Leakage
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please <u>contact us</u> or your local Diodes representative. https://www.diodes.com/quality/product-definitions/

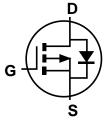
Mechanical Data

- Case: SOT323
- Case Material: Molded Plastic, "Green" Molding Compound.
 UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals Connections: See Diagram Below
- Terminals: Finish—Matte Tin Annealed over Alloy 42 Leadframe.
 Solderable per MIL-STD-202, Method 208[®]
- Weight: 0.006 grams (Approximate)

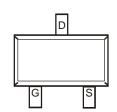




Top View



Internal Schematic



Top View

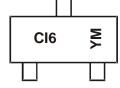
Ordering Information (Note 4)

Part Number	Case	Packaging
DMP2110UW-7	SOT323	3000/Tape & Reel
DMP2110UW-13	SOT323	10,000/Tape & Reel

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information



CI6 = Marking Code YM = Date Code Marking \overline{Y} = Year (ex: H = 2020) M = Month (ex: 9 = September)

Date Code Key

Year	2018	2019	2020	2021	2022	2023	2024	2025	2026	2027	2028	2029
Code	F	G	Н	ı	J	K	L	М	N	0	Р	R
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	Ω	N	D



Maximum Ratings (@ $T_A = +25^{\circ}C$, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Drain-Source Voltage		V _{DSS}	-20	V
Gate-Source Voltage		V _{GSS}	±12	V
Continuous Drain Current (Note 6) V _{GS} = -4.5V	I _D	-2.0 -1.6	А	
Maximum Continuous Body Diode Forward Curre	nt (Note 6)	Is	-0.9	А
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1	1%) (Note	Ідм	-15	Α

Thermal Characteristics

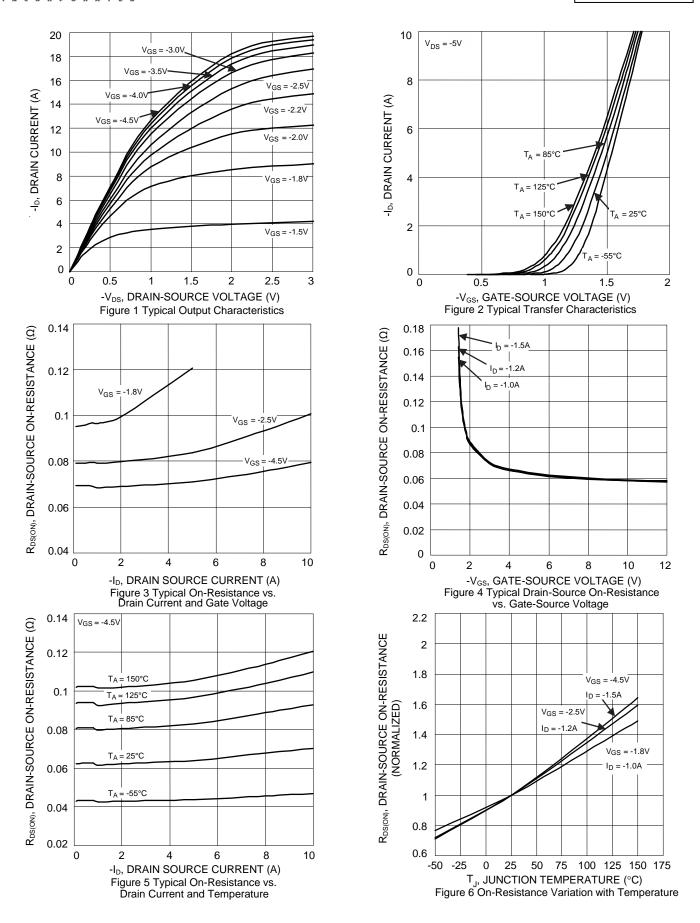
Characteristic	Symbol	Value	Unit
Total Power Dissipation (Note 5)	PD	0.49	W
Thermal Resistance, Junction to Ambient	RθJA	253	°C/W
Total Power Dissipation (Note 6)	PD	0.65	W
Thermal Resistance, Junction to Ambient	RθJA	192	°C/W
Operating and Storage Temperature Range	TJ, TSTG	-55 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

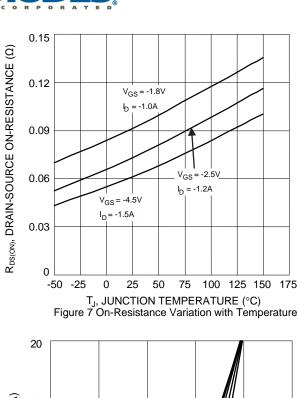
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition		
OFF CHARACTERISTICS (Note 7)								
Drain-Source Breakdown Voltage	BV _{DSS}	-20	_	_	V	$V_{GS} = 0V, I_D = -250\mu A$		
Zero Gate Voltage Drain Current T _J = +25°C	IDSS	_	_	-1.0	μΑ	V _{DS} = -20V, V _{GS} = 0V		
Gate-Source Leakage	I _{GSS}	_		±100 ±800	nA	$V_{GS} = \pm 8V, V_{DS} = 0V$ $V_{GS} = \pm 12V, V_{DS} = 0V$		
ON CHARACTERISTICS (Note 7)								
Gate Threshold Voltage	V _{GS(TH)}	-0.4	_	-0.9	V	$V_{DS} = V_{GS}$, $I_D = -250\mu A$		
			63	100		$V_{GS} = -4.5V$, $I_D = -1.5A$		
Static Drain-Source On-Resistance	RDS(ON)	_	75	120	mΩ	Vgs = -2.5V, ID = -1.2A		
			89	160		Vgs = -1.8V, ID = -1A		
Diode Forward Voltage	V _{SD}	_	-0.7	-1.0	V	V _{GS} = 0V, I _S = -1.0A		
DYNAMIC CHARACTERISTICS (Note 8)								
Input Capacitance	Ciss	_	443	1	pF			
Output Capacitance	Coss	_	59	1	pF	V _{DS} = -6V, V _{GS} = 0V f = 1.0MHz		
Reverse Transfer Capacitance	Crss	_	47	1	pF	1 - 1.000 12		
Gate Resistance	Rg	_	8.5		Ω	$V_{GS} = 0V$, $V_{DS} = 0V$, $f = 1.0MHz$		
Total Gate Charge	Qg	_	6.0	_	nC			
Gate-Source Charge	Qgs	_	0.6		nC	VGS = -4.5V, VDS = -10V, ID = - 3A		
Gate-Drain Charge	Qgd	_	1.8		nC			
Turn-On Delay Time	td(on)	_	4.0		ns			
Turn-On Rise Time	t _R	_	3.7	_	ns	V _{DS} = -10V, V _{GS} = -4.5V,		
Turn-Off Delay Time	tD(OFF)	_	24.5		ns	$R_L = 10\Omega$, $R_G = 1.0\Omega$, $I_D = -1A$		
Turn-Off Fall Time	tF	_	9.5	_	ns]		
Reverse Recovery Time	trr		8.3		ns	IF = -1.0A, di/dt = 100A/µs		
Reverse Recovery Charge	Qrr	_	2.0	_	nC	IF = -1.0A, di/dt = 100A/µs		

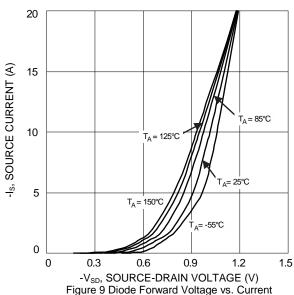
Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.
 Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.
 Short duration pulse test used to minimize self-heating effect.
 Guaranteed by design. Not subject to product testing.

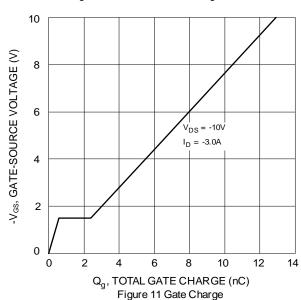


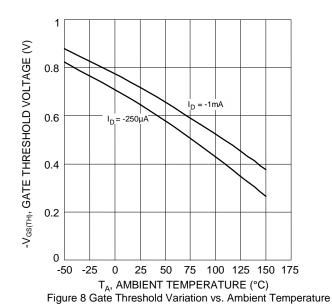


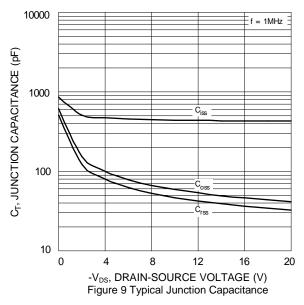












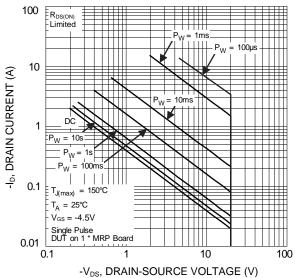
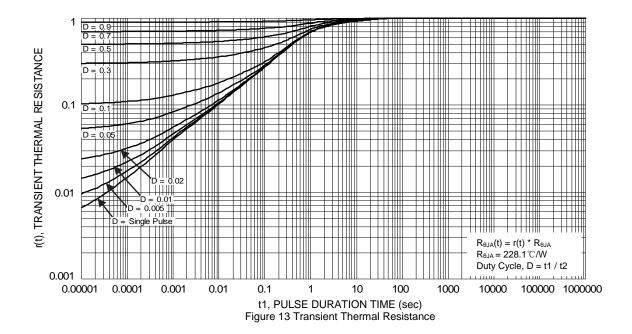


Figure 12 SOA, Safe Operation Area



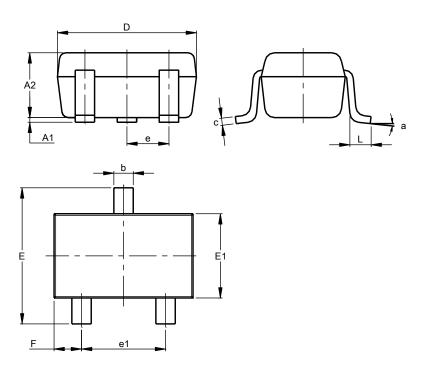




Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT323

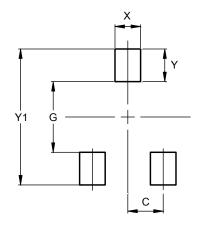


SOT323							
Dim	Min	Max	Тур				
A1	0.00	0.10	0.05				
A2	0.90	1.00	0.95				
b	0.25	0.40	0.30				
С	0.10	0.18	0.11				
D	1.80	2.20	2.15				
Е	2.00	2.20	2.10				
E1	1.15	1.35	1.30				
е	0.650 BSC						
e1	1.20	1.40	1.30				
F	0.375	0.475	0.425				
L	0.25	0.40	0.30				
а	0°	8°					
All Dimensions in mm							

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT323



Dimensions	Value (in mm)				
С	0.650				
G	1.300				
Х	0.470				
Y	0.600				
Y1	2 500				

July 2020

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