

Features

- Low On-Resistance
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- ESD Protected Up To 2kV
- **Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**
- **Qualified to AEC-Q101 Standards for High Reliability**
- **PPAP Capable (Note 4)**

Mechanical Data

- Case: SOT23
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish — Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 (E3)
- Terminal Connections: See Diagram Below
- Weight: 0.009 grams (Approximate)

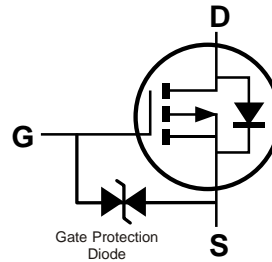


ESD PROTECTED TO 2kV

SOT23

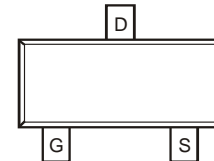


Top View



Gate Protection Diode

Internal Schematic



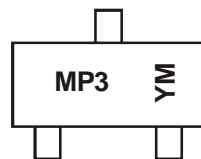
Top View

Ordering Information (Note 5 & 6)

Part Number	Compliance	Case	Packaging
DMP2035U-7	Standard	SOT23	3,000 / 7" Tape & Reel
DMP2035UQ-7	Automotive	SOT23	3,000 / 7" Tape & Reel
DMP2035U-13	Standard	SOT23	10,000 / 13" Tape & Reel

- Notes:
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
 2. See <https://www.diodes.com/quality/lead-free/> for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
 4. Automotive products are AEC-Q101 qualified and are PPAP capable. Automotive, AEC-Q101 and standard products are electrically and thermally the same, except where specified. For more information, please refer to <https://www.diodes.com/quality/>.
 5. The ESD gate protection diode is only designed to protect against ESD events. No gate-source voltage greater than the maximum V_{GSS} rating (given on page 2) can be applied.
 6. For packaging details, go to our website at <https://www.diodes.com/design/support/packaging/diodes-packaging/>.

Marking Information



MP3 = Product Type Marking Code
 YM = Date Code Marking
 Y or \bar{Y} = Year (ex: F = 2018)
 M = Month (ex: 9 = September)

Date Code Key

Year	2009	~	2017	2018	2019	2020	2021	2022	2023	2024	2025
Code	W	~	E	F	G	H	I	J	K	L	M

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	O	N	D

Maximum Ratings (@ $T_A = +25^\circ\text{C}$, unless otherwise specified.)

Characteristic			Symbol	Value	Unit
Drain-Source Voltage			V_{DSS}	-20	V
Gate-Source Voltage			V_{GSS}	± 10	V
Continuous Drain Current (Note 8) $V_{GS} = -4.5\text{V}$	Steady State	$T_A = +25^\circ\text{C}$ $T_A = +70^\circ\text{C}$	I_D	-4.9 -4.0	A
Pulsed Drain Current (Note 9)			I_{DM}	-24	A
Maximum Continuous Body Diode Forward Current (Note 8)			I_S	-1.2	A
Pulsed Body Diode Forward Current (Note 9)			I_{SM}	-24	A

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Total Power Dissipation (Note 7)	P_D	0.81	W
Thermal Resistance, Junction to Ambient (Note 7)	$R_{\theta JA}$	153.5	$^\circ\text{C/W}$
Total Power Dissipation (Note 8)	P_D	1.2	W
Thermal Resistance, Junction to Ambient (Note 8)	$R_{\theta JA}$	100	$^\circ\text{C/W}$
Operating and Storage Temperature Range	T_J, T_{STG}	-55 to +150	$^\circ\text{C}$

Electrical Characteristics (@ $T_A = +25^\circ\text{C}$, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 10)						
Drain-Source Breakdown Voltage	BV_{DSS}	-20	—	—	V	$V_{GS} = 0\text{V}, I_D = -250\mu\text{A}$
Zero Gate Voltage Drain Current $T_J = +25^\circ\text{C}$	I_{DSS}	—	—	-1.0	μA	$V_{DS} = -20\text{V}, V_{GS} = 0\text{V}$
Gate-Source Leakage	I_{GSS}	—	—	± 10	μA	$V_{GS} = \pm 8\text{V}, V_{DS} = 0\text{V}$
ON CHARACTERISTICS (Note 10)						
Gate Threshold Voltage	$V_{GS(TH)}$	-0.4	-0.7	-1.0	V	$V_{DS} = V_{GS}, I_D = -250\mu\text{A}$
Static Drain-Source On-Resistance	$R_{DS(ON)}$	—	23	35	m Ω	$V_{GS} = -4.5\text{V}, I_D = -4.0\text{A}$
			30	45		$V_{GS} = -2.5\text{V}, I_D = -4.0\text{A}$
			41	62		$V_{GS} = -1.8\text{V}, I_D = -2.0\text{A}$
Forward Transfer Admittance	$ Y_{fs} $	—	14	—	s	$V_{DS} = -5\text{V}, I_D = -4\text{A}$
Diode Forward Voltage	V_{SD}	—	-0.7	-1.0	V	$V_{GS} = 0\text{V}, I_S = -1\text{A}$
DYNAMIC CHARACTERISTICS (Note 11)						
Input Capacitance	C_{iss}	—	1,610	—	pF	$V_{DS} = -10\text{V}, V_{GS} = 0\text{V}$ $f = 1.0\text{MHz}$
Output Capacitance	C_{oss}	—	157	—	pF	
Reverse Transfer Capacitance	C_{rss}	—	145	—	pF	
Gate Resistance	R_g	—	9.45	—	Ω	$V_{DS} = 0\text{V}, V_{GS} = 0\text{V}, f = 1\text{MHz}$
Total Gate Charge	Q_g	—	15.4	—	nC	$V_{GS} = -4.5\text{V}, V_{DS} = -10\text{V},$ $I_D = -4\text{A}$
Gate-Source Charge	Q_{gs}	—	2.5	—	nC	
Gate-Drain Charge	Q_{gd}	—	3.3	—	nC	
Turn-On Delay Time	$t_{D(ON)}$	—	16.8	—	ns	$V_{DS} = -10\text{V}, V_{GS} = -4.5\text{V},$ $R_L = 10\Omega, R_g = 6.0\Omega, I_D = -1\text{A}$
Turn-On Rise Time	t_R	—	12.4	—	ns	
Turn-Off Delay Time	$t_{D(OFF)}$	—	94.1	—	ns	
Turn-Off Fall Time	t_F	—	42.4	—	ns	

- Notes:
- Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.
 - Device mounted on FR-4 substrate PC board, 2oz copper, with 25mm X 25mm square copper plate.
 - Repetitive rating, pulse width limited by junction temperature.
 - Short duration pulse test used to minimize self-heating effect.
 - Guaranteed by design. Not subject to product testing.

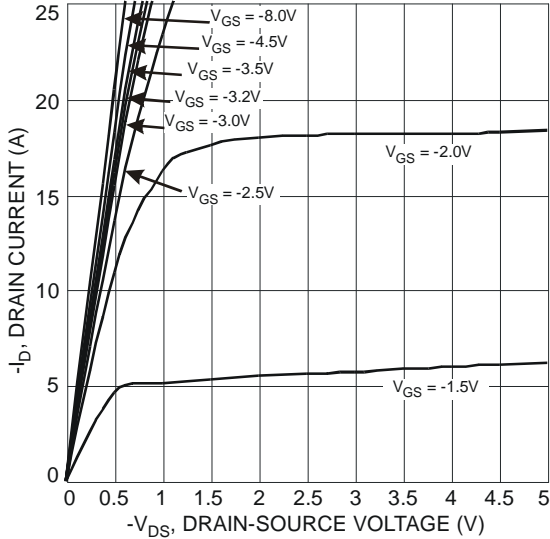


Fig. 1 Typical Output Characteristic

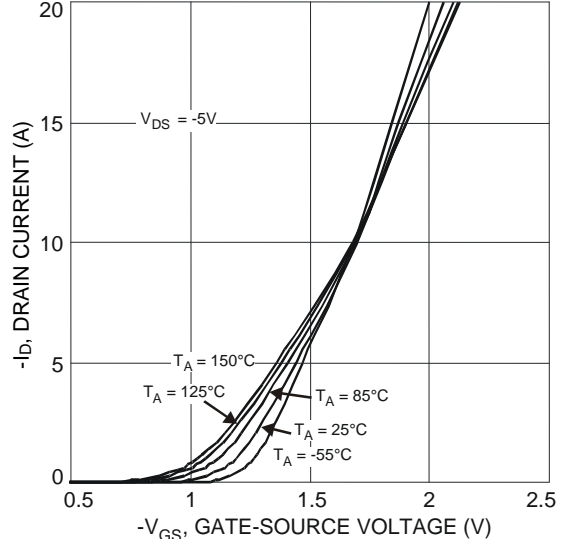


Fig. 2 Typical Transfer Characteristic

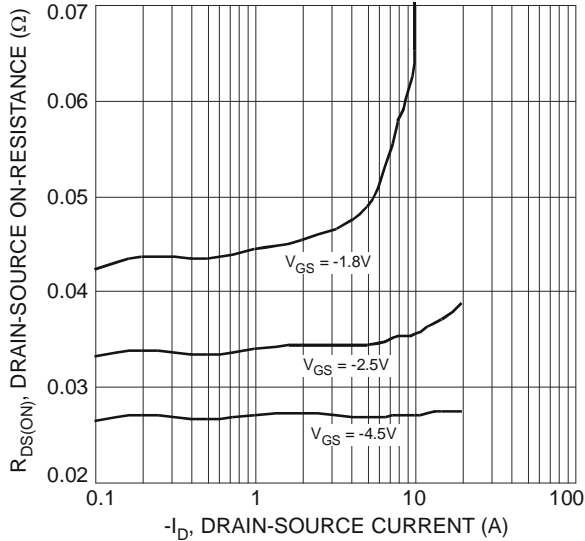


Fig. 3 Typical On-Resistance vs. Drain Current and Gate Voltage

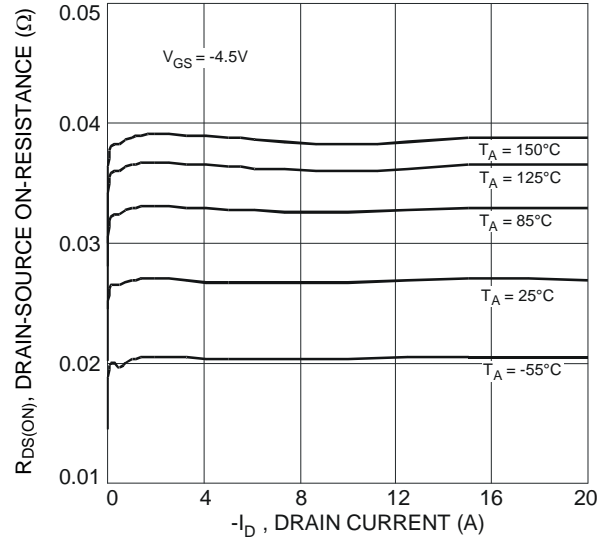


Fig. 4 Typical On-Resistance vs. Drain Current and Temperature

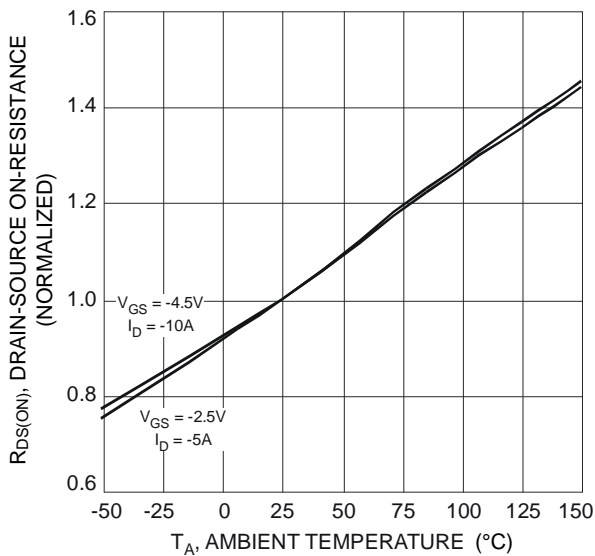


Fig. 5 On-Resistance Variation with Temperature

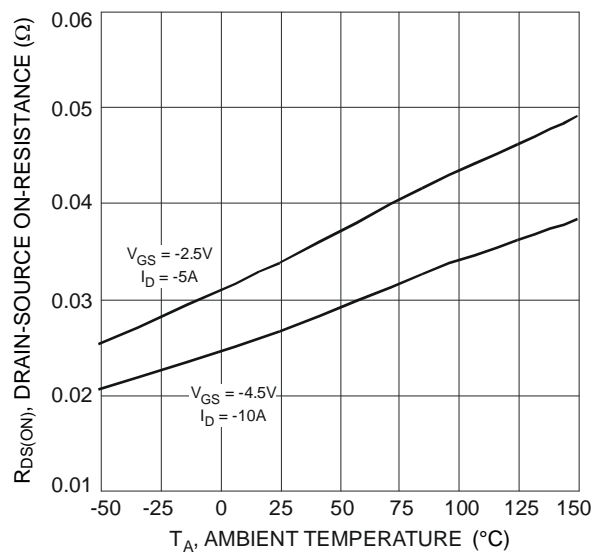


Fig. 6 On-Resistance Variation with Temperature

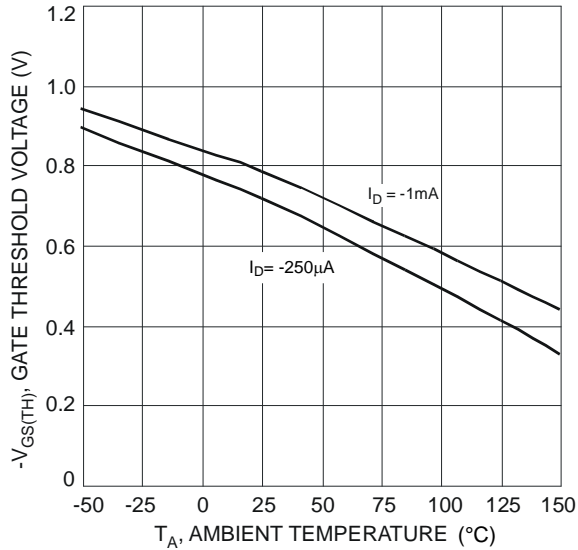


Fig. 7 Gate Threshold Variation vs. Ambient Temperature

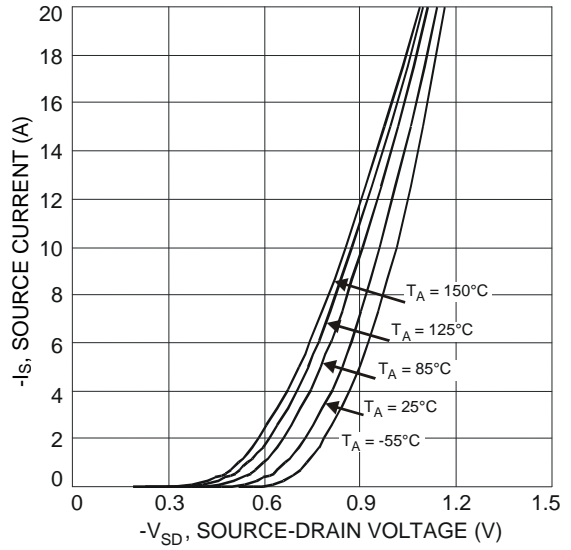


Fig. 8 Diode Forward Voltage vs. Current

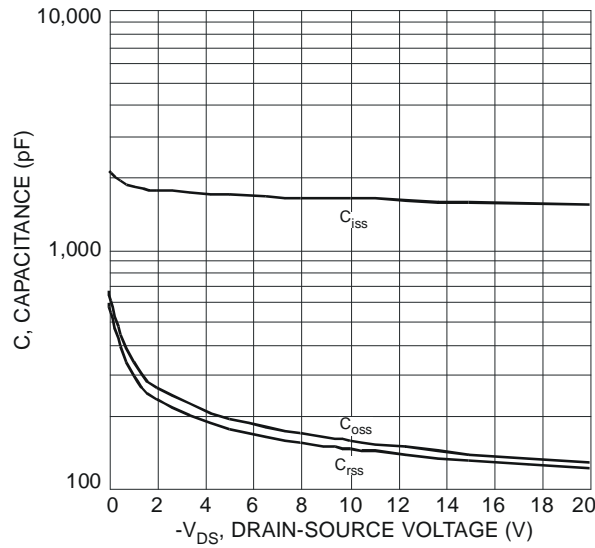


Fig. 9 Typical Total Capacitance

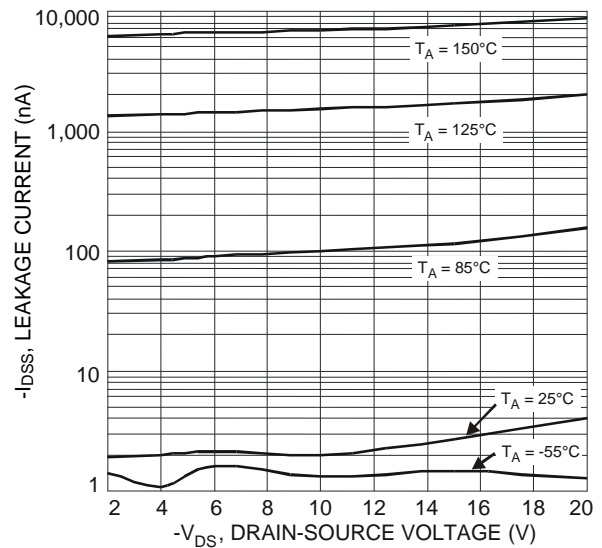


Fig. 10 Typical Leakage Current vs. Drain-Source Voltage

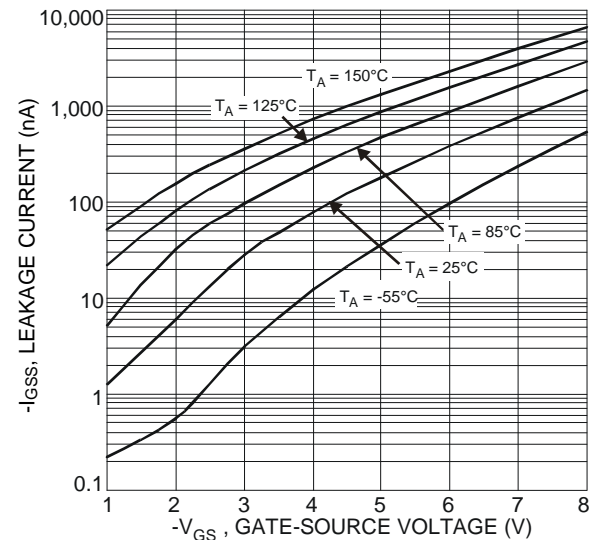


Fig. 11 Gate-Source Leakage Current vs. Voltage

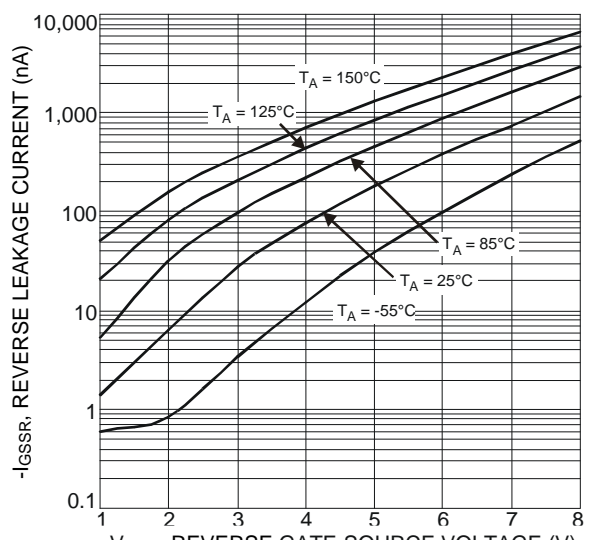
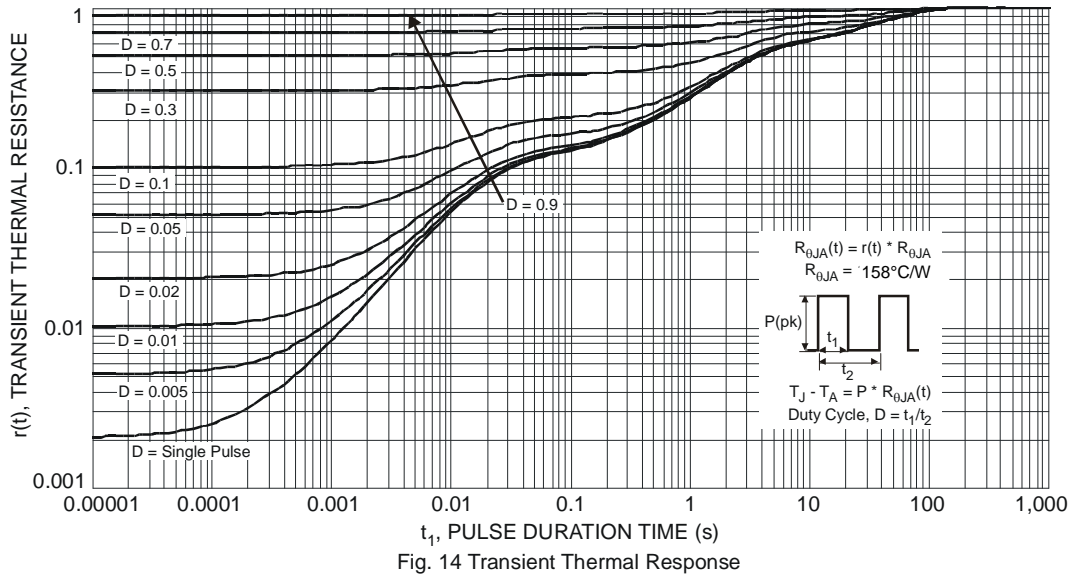
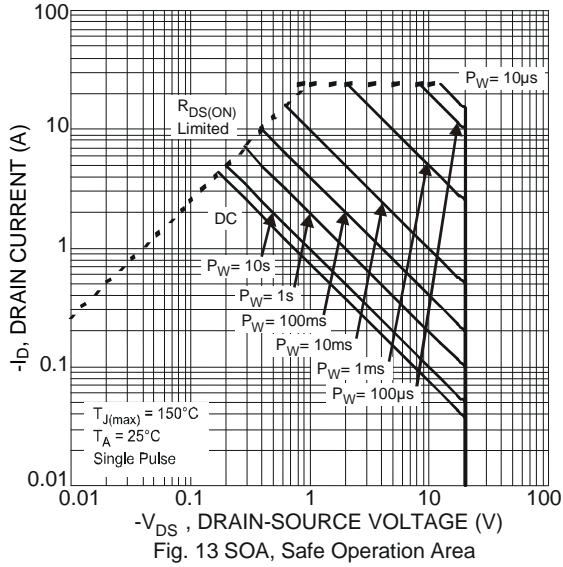


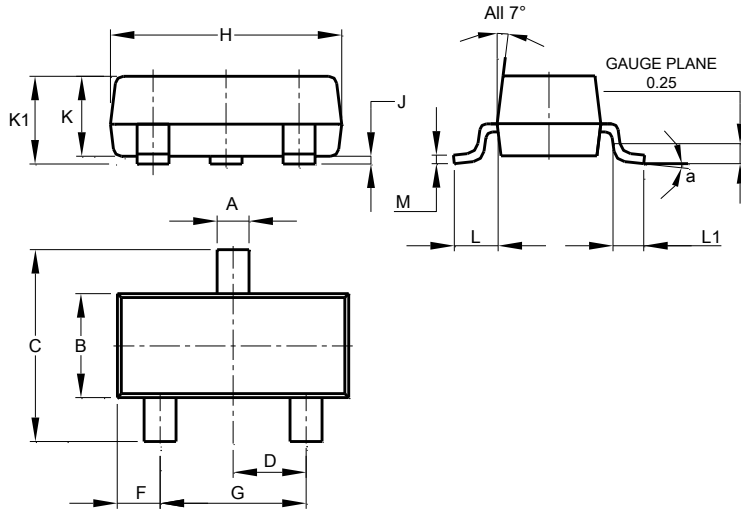
Fig. 12 Reverse Gate-Source Leakage Current vs. Voltage



Package Outline Dimensions

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

SOT23

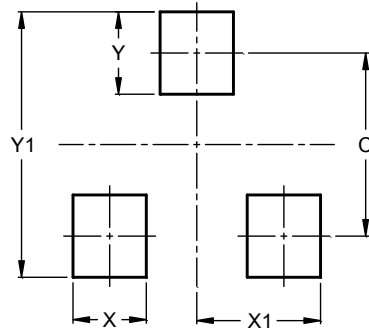


SOT23			
Dim	Min	Max	Typ
A	0.37	0.51	0.40
B	1.20	1.40	1.30
C	2.30	2.50	2.40
D	0.89	1.03	0.915
F	0.45	0.60	0.535
G	1.78	2.05	1.83
H	2.80	3.00	2.90
J	0.013	0.10	0.05
K	0.890	1.00	0.975
K1	0.903	1.10	1.025
L	0.45	0.61	0.55
L1	0.25	0.55	0.40
M	0.085	0.150	0.110
a	0°	8°	--
All Dimensions in mm			

Suggested Pad Layout

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

SOT23



Dimensions	Value (in mm)
C	2.0
X	0.8
X1	1.35
Y	0.9
Y1	2.9

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