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60V +175°C N-CHANNEL ENHANCEMENT MODE MOSFET PowerDI5060-8

Product Summary

| BV _{DSS} | R _{DS(ON)} Max | I _D Max Tc = +25°C |
|-------------------|------------------------------|----------------------------------|
| 60V | 23mΩ @ V _{GS} = 10V | 55A |
| 00 v | $28m\Omega @ V_{GS} = 4.5V$ | 48A |

Description and Applications

This MOSFET is designed to minimize the on-state resistance (RDS(ON)) yet maintain superior switching performance, making it ideal for high-efficiency power-management applications.

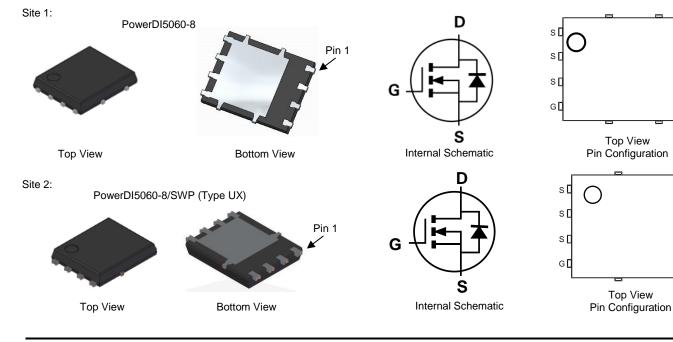
- Driving solenoids
- Driving relays
- Power-management functions

Features and Benefits

- Rated to +175°C Ideal for High Ambient Temperature Environments
- 100% Unclamped Inductive Switching Ensures More Reliable and Robust End Application
- High-Conversion Efficiency
- Low R_{DS(ON)} Minimizes On-State Losses
- Low Input Capacitance
- Fast Switching Speed
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- An automotive-compliant part is available under separate datasheet (<u>DMNH6021SPSQ</u>)

Mechanical Data

- Package: PowerDI[®]5060-8
- Package Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Finish Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 (3)
- Weight: 0.097 grams (Approximate)



Ordering Information (Note 4)

| Part Number | Paakaga | Packing | | |
|-------------------|-----------------------------|---------|-------------|--|
| Fart Nulliber | Package | Qty. | Carrier | |
| DMNH6021SPS-13 | PowerDI5060-8 | 2,500 | Tape & Reel | |
| DIVINFIOU215P5-13 | PowerDI5060-8/SWP (Type UX) | 2,500 | Tape & Reel | |

Notes: 1. EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. All applicable RoHS exemptions applied.

2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

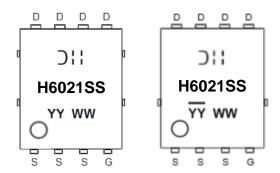
3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

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Marking Information



 \exists = Manufacturer's Marking H6021SS = Product Type Marking Code YYWW = Date Code Marking YY or \overline{YY} = Year (ex: 23 = 2023) WW = Week (01 to 53)

Maximum Ratings (@TA = +25°C, unless otherwise specified.)

| Characteristic | | Symbol | Value | Units |
|--|---------------------------------------|------------------|----------|-------|
| Drain-Source Voltage | | V _{DSS} | 60 | V |
| Gate-Source Voltage | | Vgss | ±20 | V |
| Continuous Drain Current, V _{GS} = 10V (Note 5) | Tc = +25°C T _C = +100°C | lD | 55 39 | А |
| Maximum Continuous Body Diode Forward Current (No | ls | 55 | А | |
| Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%) | | I _{DM} | 88 | A |
| Avalanche Current, L = 0.1mH (Note 6) | | las | 35 | A |
| Avalanche Energy, L = 0.1mH (Note 6) | | Eas | 64 | mJ |

Thermal Characteristics

| Characteristic | | Symbol | Value | Units |
|--|------------------------|------------------|-------------|-------|
| Total Power Dissipation (Note 7) | T _A = +25°C | PD | 1.6 | W |
| Thermal Resistance, Junction to Ambient (Note 7) | Steady State | Reja | 96 | °C/W |
| Total Power Dissipation (Note 8) | T _A = +25°C | PD | 3.0 | W |
| Thermal Resistance, Junction to Ambient (Note 8) | Steady State | R _{0JA} | 50 | °C/W |
| Total Power Dissipation (Note 5) | T _C = +25°C | PD | 53 | W |
| Thermal Resistance, Junction to Case (Note 5) | | Rejc | 1.5 | °C/W |
| Operating and Storage Temperature Range | | TJ, TSTG | -55 to +175 | °C |

Notes: 5. Thermal resistance from junction to soldering point (on the exposed drain pad).

6. I_{AS} and E_{AS} ratings are based on low frequency and duty cycles to keep $T_J = +25^{\circ}C$.

7. Device mounted on FR-4 PC board, with minimum recommended pad layout, single sided.

8. Device mounted on FR-4 substrate PC board, 2oz copper, with thermal bias to bottom layer 1inch square copper plate.

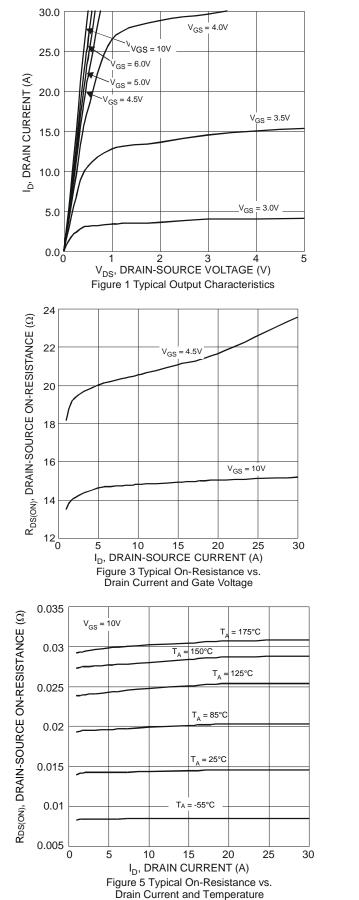


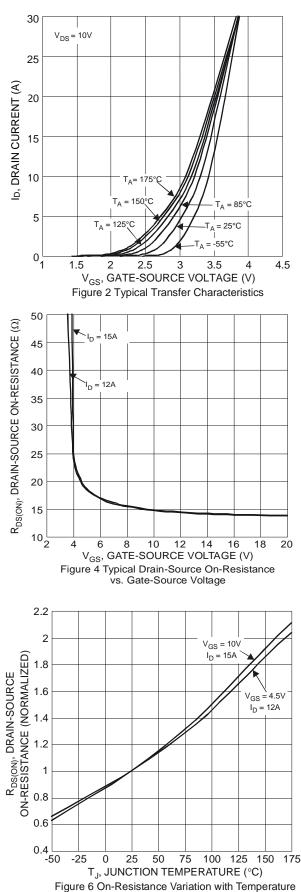
Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

| Characteristic | Symbol | Min | Тур | Max | Unit | Test Condition |
|--|---------------------|-----|-------|------|------|--|
| OFF CHARACTERISTICS (Note 9) | | | | | | |
| Drain-Source Breakdown Voltage | BVDSS | 60 | | _ | V | V _{GS} = 0V, I _D = 250µA |
| Zero Gate Voltage Drain Current | IDSS | _ | _ | 1 | μA | $V_{DS} = 60V, V_{GS} = 0V$ |
| Gate-Source Leakage | lgss | _ | _ | ±100 | nA | $V_{GS} = \pm 20V, V_{DS} = 0V$ |
| ON CHARACTERISTICS (Note 9) | | | | | | |
| Gate Threshold Voltage | Vgs(th) | 1 | — | 3 | V | $V_{DS} = V_{GS}$, $I_D = 250 \mu A$ |
| Static Drain-Source On-Resistance | Deserver | — | 12 | 23 | | $V_{GS} = 10V, I_D = 12A$ |
| Static Drain-Source On-Resistance | R _{DS(ON)} | _ | 18 | 28 | mΩ | $V_{GS} = 4.5V, I_D = 12A$ |
| Diode Forward Voltage | Vsd | _ | 0.75 | 1.2 | V | V _{GS} = 0V, I _S = 20A |
| DYNAMIC CHARACTERISTICS (Note 10) | | | | | | |
| Input Capacitance | Ciss | _ | 1,016 | — | pF | $V_{DS} = 30V, V_{GS} = 0V$ f = 1MHz |
| Output Capacitance | Coss | _ | 153 | — | | |
| Reverse Transfer Capacitance | Crss | _ | 76.8 | _ | | |
| Gate Resistance | Rg | _ | 2.5 | — | Ω | $V_{DS} = 0V, V_{GS} = 0V, f = 1MHz$ |
| Total Gate Charge (V _{GS} = 4.5V) | Qg | _ | 9.5 | _ | | V _{DS} = 30V, I _D = 20A |
| Total Gate Charge (V _{GS} = 10V) | Qg | | 19.7 | _ | nC | |
| Gate-Source Charge | Qgs | | 3.6 | _ | nc | |
| Gate-Drain Charge | Q _{gd} | _ | 4.8 | _ | | |
| Turn-On Delay Time | td(on) | — | 4.2 | — | | $V_{DD} = 30V, V_{GS} = 10V$ $I_D = 10A, R_g = 4.7\Omega$ |
| Turn-On Rise Time | tR | _ | 13 | _ | ns | |
| Turn-Off Delay Time | tD(OFF) | _ | 27.5 | — | | |
| Turn-Off Fall Time | t _F | _ | 15.3 | — |] | |
| Body Diode Reverse Recovery Time | trr | _ | 20.8 | — | ns | |
| Body Diode Reverse Recovery Charge | Qrr | _ | 13.9 | — | nC | IF = 20A, dI/dt = 100A/µs |

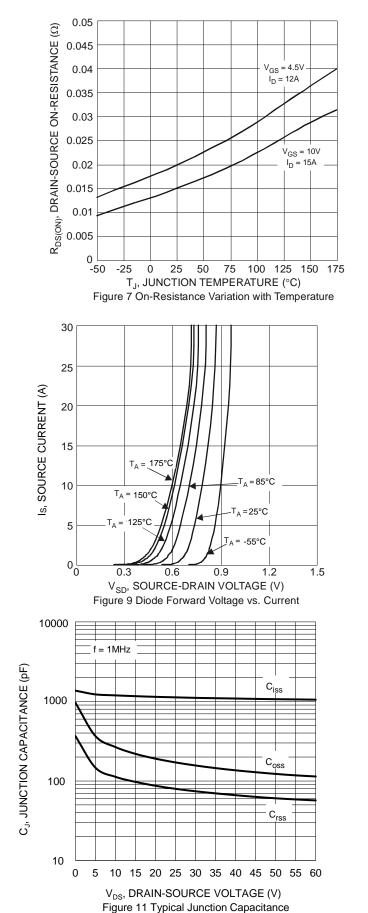
Notes: 9. Short duration pulse test used to minimize self-heating effect. 10. Guaranteed by design. Not subject to product testing.

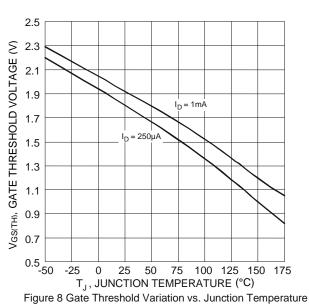


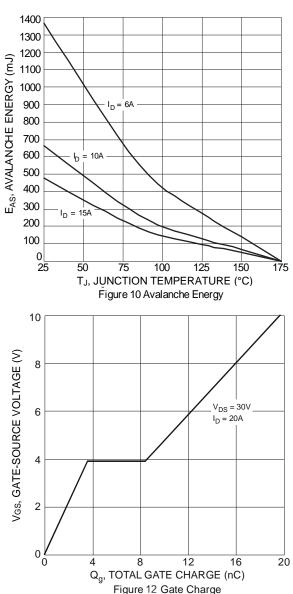




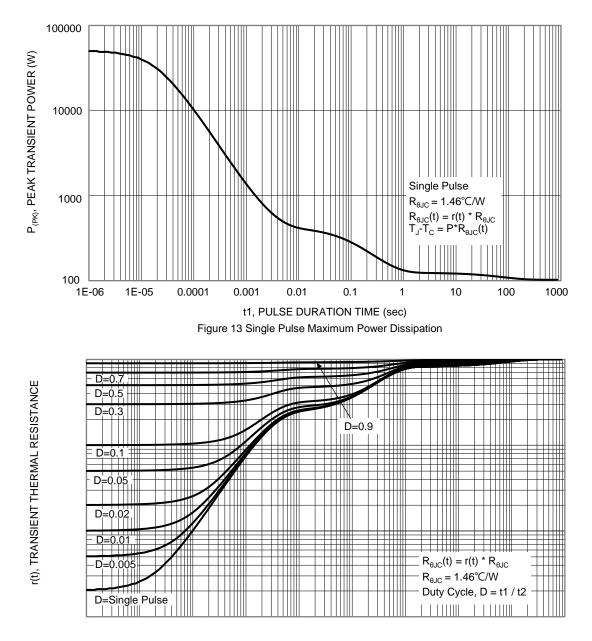












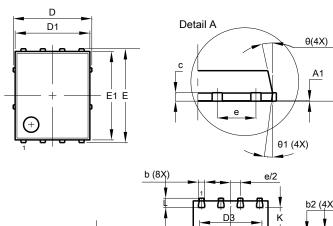
t1, PULSE DURATION TIME (sec) Figure 14 Transient Thermal Resistance

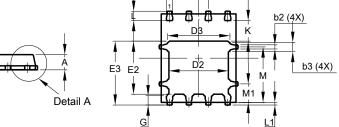


Package Outline

Please see http://www.diodes.com/package-outlines.html for the latest version.

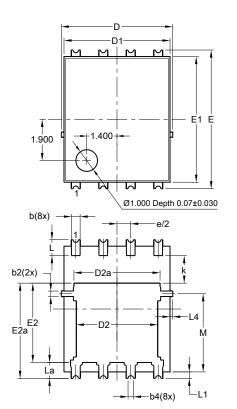
Site 1:





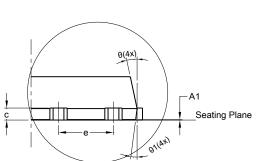
| | PowerDI5060-8 | | | | |
|----------------------|---------------|----------|-------|--|--|
| Dim | Min | Max | Тур | | |
| Α | 0.90 | 1.10 | 1.00 | | |
| A1 | 0.00 | 0.05 | - | | |
| b | 0.33 | 0.51 | 0.41 | | |
| b2 | 0.200 | 0.350 | 0.273 | | |
| b3 | 0.40 | 0.80 | 0.60 | | |
| c | 0.230 | 0.330 | 0.277 | | |
| D | | 5.15 BSC | | | |
| D1 | 4.70 | 5.10 | 4.90 | | |
| D2 | 3.70 | 4.10 | 3.90 | | |
| D3 | 3.90 | 4.30 | 4.10 | | |
| Е | (| 6.15 BSC | ; | | |
| E1 | 5.60 | 6.00 | 5.80 | | |
| E2 | 3.28 | 3.68 | 3.48 | | |
| E3 | 3.99 | 4.39 | 4.19 | | |
| е | | 1.27 BSC | ; | | |
| G | 0.51 | 0.71 | 0.61 | | |
| K | 0.51 | - | - | | |
| 1 | 0.51 | 0.71 | 0.61 | | |
| L1 | 0.100 | 0.200 | 0.175 | | |
| М | 3.235 | 4.035 | 3.635 | | |
| M1 | 1.00 | 1.40 | 1.21 | | |
| Θ | 10° | 12° | 11° | | |
| Θ1 | 6° | 8° | 7° | | |
| All Dimensions in mm | | | | | |

Site 2:

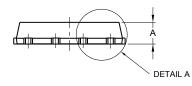


PowerDI5060-8/SWP (Type UX)

PowerDI5060-8



DETAIL A



| PowerDI5060-8/SWP | | | | | |
|-------------------|----------------------|---------|-------|--|--|
| | (Type UX) | | | | |
| Dim | Min | Max | Тур | | |
| Α | 0.90 | 1.10 | 1.00 | | |
| A1 | 0 | 0.05 | | | |
| b | 0.30 | 0.50 | 0.41 | | |
| b2 | 0.20 | 0.35 | 0.25 | | |
| b4 | (|).25REF | - | | |
| c | 0.230 | 0.330 | | | |
| D | 5 | .15 BS0 | 2 | | |
| D1 | 4.70 | 5.10 | 4.90 | | |
| D2 | 3.56 | 3.96 | 3.76 | | |
| D2a | 3.78 | 4.18 | 3.98 | | |
| ш | 6 | .40 BS0 | 2 | | |
| E1 | 5.60 | 6.00 | 5.80 | | |
| E2 | 3.46 | 3.86 | 3.66 | | |
| E2a | 4.195 | 4.595 | 4.395 | | |
| e | 1 | .27BSC |) | | |
| k | 1.05 | | | | |
| L | 0.635 | 0.835 | 0.735 | | |
| La | 0.635 | 0.835 | 0.735 | | |
| L1 | 0.200 | 0.400 | 0.300 | | |
| L1a | 0 | .050RE | F | | |
| L4 | 0.025 | 0.225 | 0.125 | | |
| М | 3.205 | 4.005 | 3.605 | | |
| θ | 10° | 12° | 11° | | |
| θ1 | 6° | 8° | 7° | | |
| All | All Dimensions in mm | | | | |
| | | | | | |

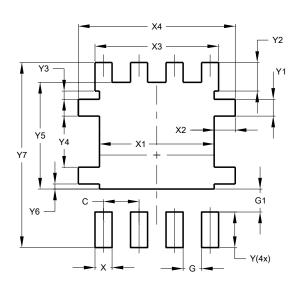
DMNH6021SPS Document number: DS37685 Rev. 4 - 2



Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

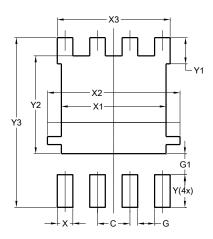




| Dimensions | Value (in mm) |
|------------|---------------|
| С | 1.270 |
| G | 0.660 |
| G1 | 0.820 |
| Х | 0.610 |
| X1 | 4.100 |
| X2 | 0.755 |
| X3 | 4.420 |
| X4 | 5.610 |
| Y | 1.270 |
| Y1 | 0.600 |
| Y2 | 1.020 |
| Y3 | 0.295 |
| Y4 | 1.825 |
| Y5 | 3.810 |
| Y6 | 0.180 |
| Y7 | 6.610 |

Site 2:

PowerDI5060-8/SWP (Type UX)



| 0 | | | |
|------------|---------|--|--|
| Dimensions | Value | | |
| Dimensions | (in mm) | | |
| С | 1.270 | | |
| G | 0.660 | | |
| G1 | 0.820 | | |
| Х | 0.610 | | |
| X1 | 4.100 | | |
| X2 | 5.190 | | |
| X3 | 4.420 | | |
| Y | 1.270 | | |
| Y1 | 1.020 | | |
| Y2 | 3.810 | | |
| Y3 | 6.610 | | |
| | | | |

Site 1:



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