



60V +175°C DUAL N-CHANNEL ENHANCEMENT MODE MOSFET PowerDI5060-8

Product Summary

BV _{DSS}	R _{DS(ON)} Max	I _D Max T _C = +25°C
601/	25mΩ @ V _G S = 10V	32A
60V	40mΩ @ V _{GS} = 4.5V	25A

Features and Benefits

- Rated to +175°C—Ideal for High Ambient Temperature Environments
- 100% Unclamped Inductive Switching—Ensures More Reliable and Robust End Application
- High Conversion Efficiency
- Low RDS(ON)—Minimizes On-State Losses
- Low Input Capacitance
- Fast Switching Speed
- Wettable Flank for Improved Optical Inspection
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- The DMNH6021SPDWQ is suitable for automotive applications requiring specific change control; this part is AEC-Q101 qualified, PPAP capable, and manufactured in IATF 16949 certified facilities.

https://www.diodes.com/quality/product-definitions/

Description and Applications

This MOSFET is designed to meet the stringent requirements of automotive applications. It is qualified to AEC-Q101, supported by a PPAP, and is ideal for use in:

- Backlighting
- Power-management functions
- DC-DC converters

PowerDI5060-8 (SWP) (Type R) PowerDI5060-8/SWP (Type UXD)



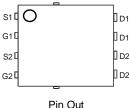


Top View

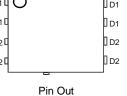
Bottom View

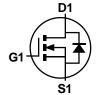
Mechanical Data

- Package: PowerDI®5060-8
- Package Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 3 per J-STD-020
- Terminal Connections Indicator: See Diagram
- Terminals: Finish—Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 @3
- Weight: 0.097 grams (Approximate)



Top View







Equivalent Circuit

Ordering Information (Note 4)

Part Number	Package	Packing		
Part Number	Package	Qty.	Carrier	
DMNH6021SPDWQ-13	PowerDI5060-8 (SWP) (Type R)	2500	Tape & Reel	
DIVINHOUZ 13 PDVV Q-13	PowerDI5060-8/SWP (Type UXD)	2500	Tape & Reel	

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.



Marking Information



OH = Manufacturer's Marking
NH6021DW = Product Type Marking Code
YYWW = Date Code Marking
YY = Year (ex: 23 = 2023)
WW = Week (01 to 53)

Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	
Drain-Source Voltage		VDSS	60	V
Gate-Source Voltage	Vgss	±20	V	
Continuous Drain Current (Note 5) V _{GS} = 10V	$T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$	l _D	8.2 6.5	А
Continuous Drain Current (Note 6) V _{GS} = 10V	$T_{C} = +25^{\circ}C$ $T_{C} = +100^{\circ}C$	lo	32 22	А
Pulsed Drain Current (380µs Pulse, Duty Cycle = 1%)	I _{DM}	80	А	
Maximum Continuous Body Diode Forward Current (Note 5)		Is	32	А
Avalanche Current, L = 0.1mH (Note 7)		las	35	Α
Avalanche Energy, L = 0.1mH (Note 7)		Eas	64	mJ

Thermal Characteristics ($@T_A = +25^{\circ}C$, unless otherwise specified.)

Characteristic		Symbol	Value	Unit	
Total Power Dissipation (Note 8)		PD	1.5	W	
St. St.		-	99	9000	
Thermal Resistance, Junction to Ambient (Note 8)	t < 10s	Reja	53	°C/W	
Total Power Dissipation (Note 5)		P _D	2.8	W	
Thermal Desistance Junction to Ambient (Note E)	Steady State	Devi	54	°C/W	
Thermal Resistance, Junction to Ambient (Note 5)	t < 10s	RөJA	27	C/VV	
Thermal Resistance, Junction to Case (Note 6)		R ₀ JC	2.2	°C/W	
Operating and Storage Temperature Range		T _J , T _{STG}	-55 to +175	°C	

Notes:

- 5. Device mounted on FR-4 substrate PC board, 2oz copper, with thermal bias to bottom layer 1inch square copper plate.
- 6. Thermal resistance from junction to soldering point (on the exposed drain pad).
- 7. I_{AS} and E_{AS} ratings are based on low frequency and duty cycles to keep $T_J = +25^{\circ}C$.
- 8. Device mounted on FR-4 PC board, with minimum recommended pad layout, single sided.



Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

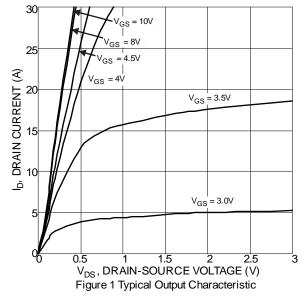
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 9)							
Drain-Source Breakdown Voltage	BV _{DSS}	60	_	_	V	$V_{GS} = 0V, I_{D} = 250\mu A$	
Zero Gate Voltage Drain Current T _J = +25°C	IDSS	1	_	1	μA	V _{DS} = 60V, V _{GS} = 0V	
Gate-Source Leakage	Igss		ı	±100	nA	$V_{GS} = \pm 20V$, $V_{DS} = 0V$	
ON CHARACTERISTICS (Note 9)							
Gate Threshold Voltage	V _{GS(TH)}	1	1	3	V	$V_{DS} = V_{GS}$, $I_D = 250\mu A$	
Static Drain-Source On-Resistance	D-scars		15	25	mΩ	$V_{GS} = 10V, I_D = 15A$	
Static Dialii-Source Oil-Resistance	R _{DS(ON)}		21	40	11177	$V_{GS} = 4.5V, I_D = 12A$	
Diode Forward Voltage	VsD	-	0.75	1.2	V	VGS = 0V, IS = 2.6A	
DYNAMIC CHARACTERISTICS (Note 10)	DYNAMIC CHARACTERISTICS (Note 10)						
Input Capacitance	Ciss	1	1,143	_	pF), o5),), o),	
Output Capacitance	Coss		168	_	pF	V _{DS} = 25V, V _{GS} = 0V -f = 1MHz	
Reverse Transfer Capacitance	Crss		69	_	pF	1 - 11/11/2	
Gate Resistance	Rg	_	2.5	_	Ω	$V_{DS} = 0V$, $V_{GS} = 0V$, $f = 1MHz$	
Total Gate Charge (V _{GS} = 10V)	Q_g	-	20.1	_	nC		
Total Gate Charge (VGS = 6V)	Qg		12	_	nC	V _{DS} = 30V. I _D = 20A	
Gate-Source Charge	Qgs		4.3	_	nC	VDS = 30V, ID = 20A	
Gate-Drain Charge	Q_{gd}		5.5	_	nC		
Turn-On Delay Time	td(ON)	_	4.4	_	ns		
Turn-On Rise Time	t _R	-	6.0	_	ns	V _{DD} = 30V, V _{GS} = 10V	
Turn-Off Delay Time	tD(OFF)		14.2	_	ns	$R_g = 4.7\Omega$, $I_D = 20A$	
Turn-Off Fall Time	t _F		5.4	_	ns]	
Body Diode Reverse Recovery Time	t _{RR}		21.2	_	ns	1 20 A dl/dt - 100 A/us	
Body Diode Reverse Recovery Charge	Qrr	_	15.2	_	nC	lF = 20A, dl/dt = 100A/μs	

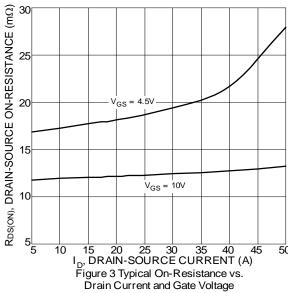
Notes:

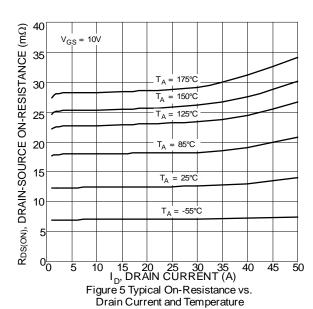
Short duration pulse test used to minimize self-heating effect.
 Guaranteed by design. Not subject to product testing.

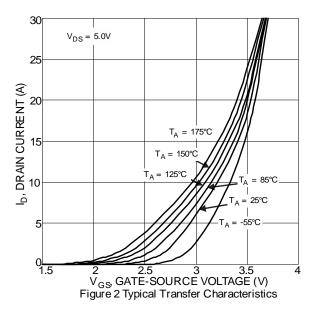


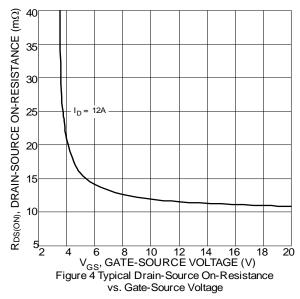


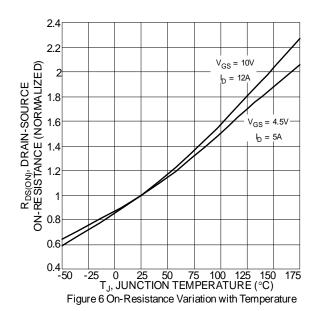






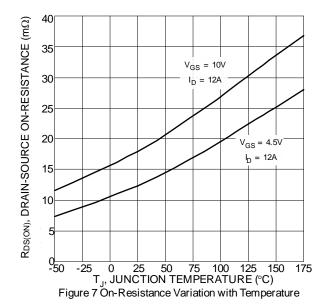


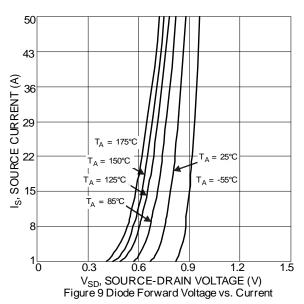


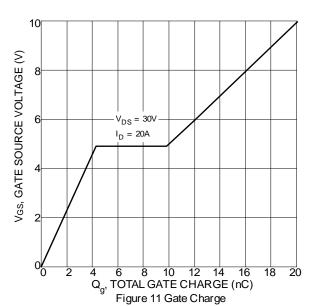












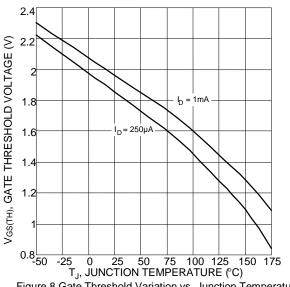
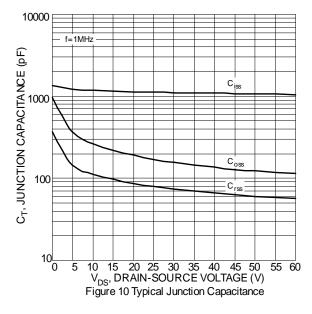
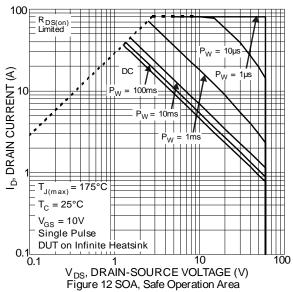
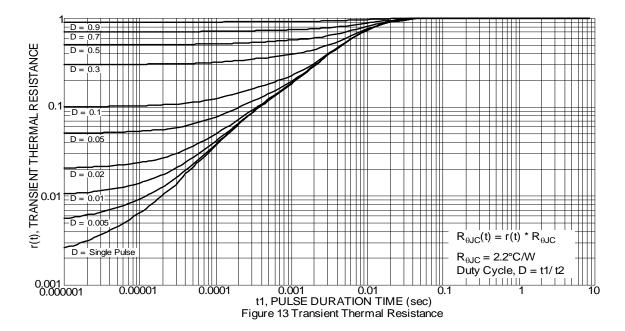


Figure 8 Gate Threshold Variation vs. Junction Temperature









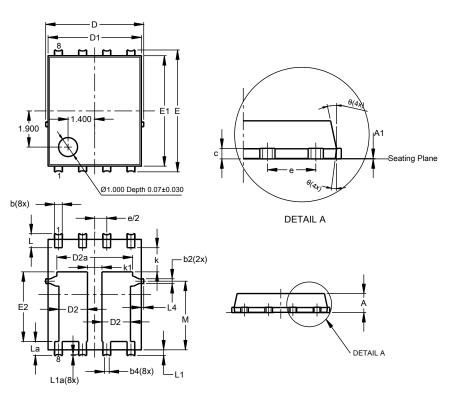


Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

Site 1:

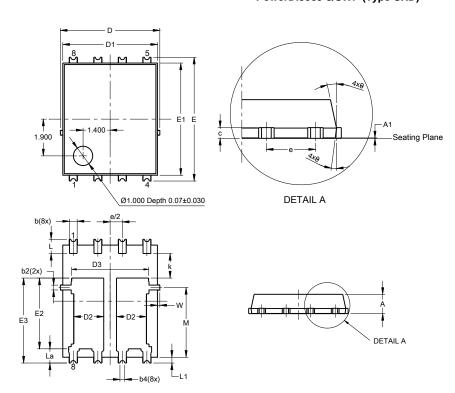
PowerDI5060-8 (SWP) (Type R)



PowerDI5060-8 (SWP) (Type R)			
Dim	Min	Max	Тур
Α	0.90	1.10	1.00
A 1	0	0.05	
b	0.30	0.50	0.41
b2	0.20	0.35	0.25
b4).25REF	
С	0.230	0.330	0.277
D		.15 BS0	
D1	4.70	5.10	4.90
D2	1.40	1.60	1.50
D2a	3.78	4.18	3.98
Е	6	.40 BS0	
E1	5.60	6.00	5.80
E2	3.46	3.86	3.66
е	1	.27BSC	
k	1.05		
k1	0.56		
L	0.635	0.835	0.735
La	0.635	0.835	0.735
L1	0.200	0.400	0.300
L1a	0.050REF		
L4	0.025	0.225	0.125
М	3.205	4.005	3.605
θ	10°	12°	11°
θ1	6°	8°	7°
All Dimensions in mm			

Site 2:

PowerDI5060-8/SWP (Type UXD)



PowerDI5060-8/SWP				
(Type UXD)				
Dim	Min	Max	Тур	
Α	0.90	1.10	1.00	
A 1	0.00	0.05		
b	0.30	0.50	0.41	
b2	0.20	0.35	0.25	
b4	().25REF	=	
C D	0.230	0.330	0.277	
	5	.15 BS0	5	
D1	4.70	5.10	4.90	
D2	1.46	1.66	1.55	
D3	3.78	4.18	3.98	
Е	6	.40 BS0		
E1	5.60	6.00	5.80	
E2	3.46	3.86	3.66	
E2a	4.195	4.595	4.395	
е	1	.27BSC)	
k	1.05			
L	0.635	0.835	0.735	
La	0.635	0.835	0.735	
L1	0.200	0.400	0.300	
М	3.205	4.005	3.605	
W	0.025	0.225	0.125	
θ	10°	12°	11°	
θ1	6°	8°	7°	
All Dimensions in mm				

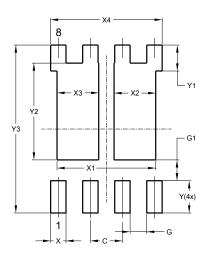


Suggested Pad Layout

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Site 1:

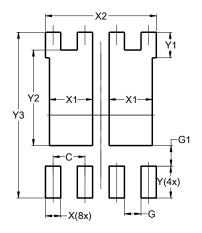
PowerDI5060-8 (SWP) (Type R)



n: :	Value
Dimensions	(in mm)
С	1.270
G	0.660
G1	0.820
Х	0.610
X1	3.910
X2	1.650
Х3	1.650
X4	4.420
Υ	1.270
Y1	1.020
Y2	3.810
Y3	6.610

Site 2:

PowerDI5060-8/SWP (Type UXD)



Dimensions	Value		
Dilliensions	(in mm)		
С	1.270		
G	0.660		
G1	0.820		
Х	0.610		
X1	1.720		
X2	4.420		
Y	1.270		
Y1	1.020		
Y2	3.810		
Y3	6.610		



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