



N-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

BV _{DSS}	Rds(on)	I _D T _A = +25°C
001/	2Ω @ V _{GS} = 10V	407mA
60V	3Ω @ V _{GS} = 5V	332mA

Description and Applications

This new generation MOSFET is designed to minimize the on-state resistance (RDS(ON)) yet maintain superior switching performance, making it ideal for high-efficiency power management applications.

- DC-DC Converters
- Power Management Functions
- Battery Operated Systems and Solid-State Relays

Features and Benefits

- Low On-Resistance
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please contact us or your local Diodes representative.

 https://www.diodes.com/quality/product-definitions/

Mechanical Data

- Case: X1-DFN1006-3
- Case Material: Molded Plastic, "Green" Molding Compound;
 UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish NiPdAu over Copper Leadframe.
 Solderable per MIL-STD-202, Method 208 @
- Weight: 0.001 grams (Approximate)



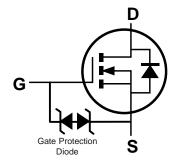




Bottom View



Top View Pin-Out



Equivalent Circuit

Ordering Information (Note 4)

Part Number	Case	Packaging
DMN62D4LFB-7B	X1-DFN1006-3	10,000 / Tape & Reel

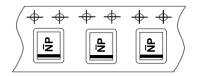
Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information



Top View Bar Denotes Gate and Source Side



NP= Part Marking Code



Maximum Ratings (@ $T_A = +25^{\circ}C$, unless otherwise specified.)

Characteristic			Symbol	Value	Unit
Drain-Source Voltage			VDSS	60	V
Gate-Source Voltage	V _{GSS}	±20	V		
Continuous Drain Current (Note 5) V _{GS} = 10V	Steady State	$T_A = +25$ °C $T_A = +70$ °C	I _D	407 325	mA
Maximum Body Diode Continuous Current (Note 5)			Is	407	mA
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)			I _{DM}	1	Α
Pulsed Source Current (10µs Pulse, Duty Cycle = 1%)			Ism	1	Α

Thermal Characteristics

Characteristic	Symbol	Max	Unit
Power Dissipation (Note 5)	PD	0.5	W
Thermal Resistance, Junction to Ambient @T _A = +25°C (Note 5)	Reja	251	°C/W
Power Dissipation (Note 6)	PD	1.2	W
Thermal Resistance, Junction to Ambient @T _A = +25°C (Note 6)	Reja	103	°C/W
Operating and Storage Temperature Range	TJ, TSTG	-55 to +150	°C

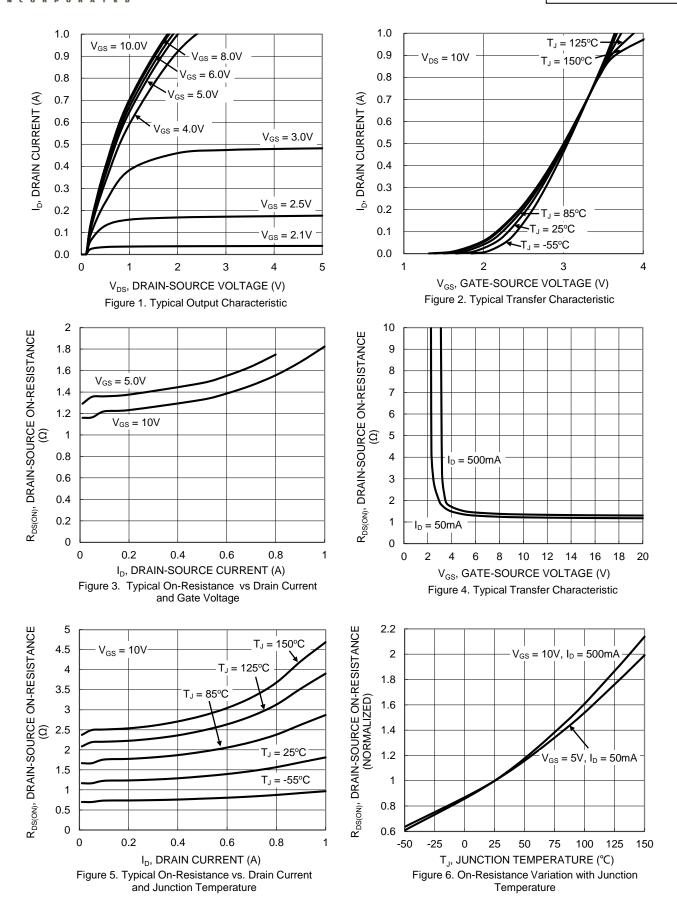
Electrical Characteristics (@ T_A = +25°C, unless otherwise stated.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)							
Drain-Source Breakdown Voltage	BV _{DSS}	60	_	_	V	$V_{GS} = 0V$, $I_{D} = 250\mu A$	
Zero Gate Voltage Drain Current T _J = +25°C	IDSS	1	_	1.0	μΑ	V _{DS} = 60V, V _{GS} = 0V	
Gate-Source Leakage	Igss	_	_	±10	μΑ	$V_{GS} = \pm 20V$, $V_{DS} = 0V$	
ON CHARACTERISTICS (Note 7)							
Gate Threshold Voltage	Vgs(TH)	1.0	_	2.5	V	$V_{DS} = V_{GS}$, $I_D = 250\mu A$	
Static Drain-Source On-Resistance	D	l	1.33	2	Ω	$V_{GS} = 10V, I_D = 0.5A$	
Static Dialif-Source Off-Resistance	RDS(ON)	1	1.36	3	32	$V_{GS} = 5V$, $I_{D} = 0.05A$	
Diode Forward Voltage	VsD	_	0.8	1.1	V	V _G S = 0V, I _S = 115mA	
DYNAMIC CHARACTERISTICS (Note 8)							
Input Capacitance	Ciss	_	40	_		$V_{DS} = 30V, V_{GS} = 0V,$ f = 1.0MHz	
Output Capacitance	Coss	_	4.8	_	pF		
Reverse Transfer Capacitance	Crss	_	2.8	_			
Gate Resistance	Rg	_	240	_	Ω	$V_{DS} = 0V$, $V_{GS} = 0V$, $f = 1MHz$	
Total Gate Charge(V _{GS} = 10V)	Qg	_	1.1	_		V _{DS} = 15V, I _D = 250mA	
Total Gate Charge(V _{GS} = 4.5V)	Qg	_	0.6	_	nC		
Gate-Source Charge	Qgs	_	0.2	_	IIC		
Gate-Drain Charge	Qgd	_	0.3	_		!	
Turn-On Delay Time	t _{D(ON)}	_	5.4	_	ns	101/11/ 001/	
Turn-On Rise Time	t _R	_	2.2	_	ns	$V_{GS} = 10V, V_{DS} = 30V,$	
Turn-Off Delay Time	tD(OFF)	_	34	_	ns	$R_L = 150\Omega$, $R_G = 25\Omega$, $R_D = 200$ mA	
Turn-Off Fall Time	tF	_	11		ns	TID = ZOOTIA	

Notes:

- 5. Device mounted on FR-4 PCB with minimum recommended pad layout, single sided.
- 6. Device mounted on FR-4 substrate PC board, 2oz copper, with thermal vias to bottom layer 1inch square copper plate.
- 7. Short duration pulse test used to minimize self-heating effect.
 8. Guaranteed by design. Not subject to production testing.







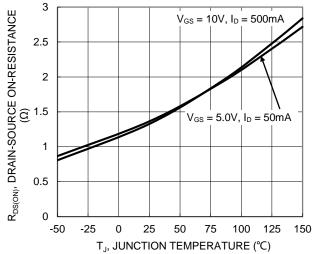


Figure 7. On-Resistance Variation with Junction Temperature

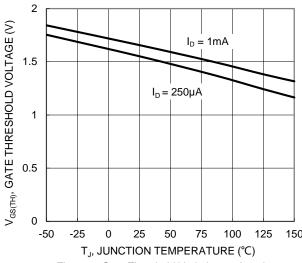


Figure 8. Gate Threshold Variation vs Junction Temperature

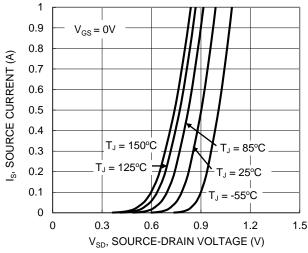


Figure 9. Diode Forward Voltage vs. Current

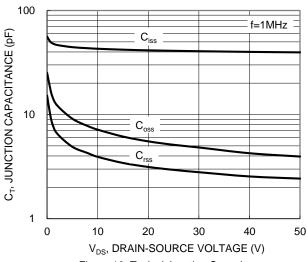
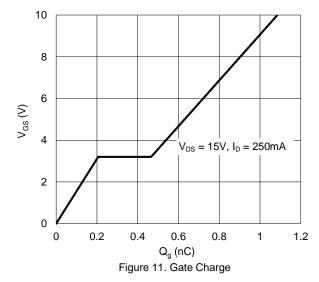


Figure 10. Typical Junction Capacitance



10 R_{DS(ON)} Limited $P_{W} = 100 \mu s$ DRAIN CURRENT (A) 0.1 T_{J (Max)}=150℃ T_A=25 ℃ 0.01 P_{W} Single Pulse DUT on 1*MRP DC board V_{GS}=10V 0.001 0.1 100 V_{DS}, DRAIN-SOURCE VOLTAGE (V) Figure 12. SOA, Safe Operation Area



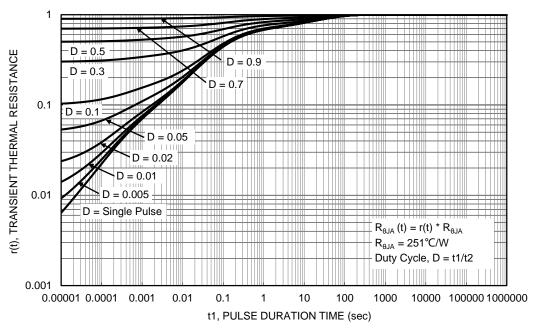


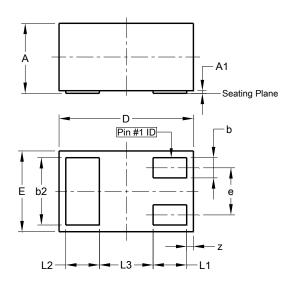
Figure 13. Transient Thermal Resistance



Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

X1-DFN1006-3

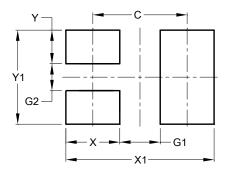


X1-DFN1006-3				
Dim	Min	Max	Тур	
Α	0.47	0.53	0.50	
A1	0.00	0.05	0.03	
b	0.10	0.20	0.15	
b2	0.45	0.55	0.50	
D	0.95	1.075	1.00	
Е	0.55	0.675	0.60	
е	ı	-	0.35	
L1	0.20	0.30	0.25	
L2	0.20	0.30	0.25	
L3	-	-	0.40	
Z	0.02	0.08	0.05	
All Dimensions in mm				

Suggested Pad Layout

 $\label{prop:lease} Please see \ http://www.diodes.com/package-outlines.html for the latest version.$

X1-DFN1006-3



Dimensions	Value (in mm)
С	0.70
G1	0.30
G2	0.20
Х	0.40
X1	1.10
Y	0.25
Y1	0.70



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