



DUAL N-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

BV _{DSS}	Rds(ON) Max	I _D Max T _A = +25°C
60V	2Ω @ $V_{GS} = 4.5V$	350mA
00 V	2.5Ω @ V _{GS} = 2.5V	SSUTIA

Description and Applications

This MOSFET is designed to meet the stringent requirements of Automotive applications. It is qualified to AEC-Q101, supported by a PPAP and is ideal for use in:

- Motor Control
- Power Management Functions

Features

- Dual N-Channel MOSFET
- Low On-Resistance
- Low Gate Threshold Voltage
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- Ultra-Small Surface Mount Package
- ESD Protected Gate
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- The DMN62D0UDWQ is suitable for automotive applications requiring specific change control; this part is AEC-Q101 qualified, PPAP capable, and manufactured in IATF 16949 certified facilities.

https://www.diodes.com/quality/product-definitions/

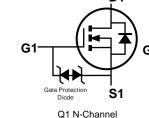
Mechanical Data

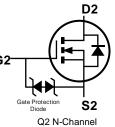
- Case: SOT363
- Case Material: Molded Plastic. "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Matte Tin Finish Annealed over Alloy 42 Leadframe (Lead Free Plating). Solderable per MIL-STD-202, Method 208⁽²⁾
- Terminal Connections: See Diagram
- Weight: 0.006 grams (Approximate)

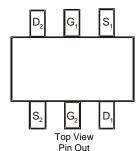




SOT363







Top View

Equivalent Circuit

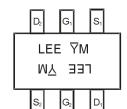
Ordering Information (Note 4)

Part Number	Case	Packaging
DMN62D0UDWQ-7	SOT363	3000/Tape & Reel
DMN62D0UDWQ-13	SOT363	10000/Tape & Reel

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information



LEE = Product Type Marking Code $\overline{Y}M$ = Date Code Marking

 \overline{Y} = Year (ex: H = 2020)

M = Month (ex: 9 = September)

Date Code Key

Date Code	IXCy											
Year	2019	2020	2021	2022	2023	2024	2025	2026	2027	2028	2029	2030
Code	G	Ι		J	K	L	M	N	0	Р	R	S
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D



Maximum Ratings (@ $T_A = +25^{\circ}C$, unless otherwise specified.)

Characteristic		Symbol	Value	Unit	
Drain-Source Voltage		VDSS	60	V	
Gate-Source Voltage	V_{GSS}	±20	V		
Continuous Drain Current (Note 6) $V_{GS} = 4.5V$ Steady $T_{A} = +25^{\circ}C$ State $T_{A} = +70^{\circ}C$			lo	350 290	mA
Maximum Continuous Body Diode Forward Currer	Is	0.4	А		

Thermal Characteristics ($@T_A = +25^{\circ}C$, unless otherwise specified.)

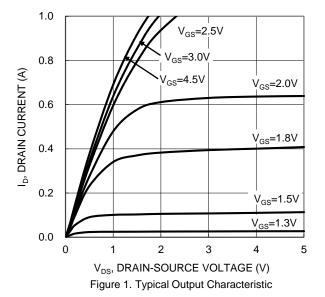
Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 5)		P _D	320	mW
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	Reja	400	°C/W
Total Power Dissipation (Note 6)		PD	410	mW
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	Reja	312	°C/W
Operating and Storage Temperature Range		TJ, TSTG	-55 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Q			-			T 10 III
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)						
Drain-Source Breakdown Voltage	BVDSS	60	_	_	V	$V_{GS} = 0V, I_{D} = 250\mu A$
Zero Gate Voltage Drain Current	IDSS	_	_	1.0	μΑ	$V_{DS} = 60V, V_{GS} = 0V$
Gate-Source Leakage	Igss	_	_	±10	μΑ	$V_{GS} = \pm 20V$, $V_{DS} = 0V$
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	Vgs(TH)	0.5	_	1.1	V	V _{DS} = 10V, I _D = 250µA
			1.2	2.0		$V_{GS} = 4.5V, I_{D} = 0.1A$
Static Drain-Source On-Resistance	R _{DS(ON)}	_	1.4	2.5	Ω	$V_{GS} = 2.5V, I_D = 0.05A$
	, ,		1.8	3.5		$V_{GS} = 1.8V, I_D = 0.05A$
Diode Forward Voltage	VsD	_	0.8	1.3	V	V _{GS} = 0V, I _S = 115mA
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	Ciss	_	32	_	pF	.,
Output Capacitance	Coss	_	3.9	_	pF	V _{DS} = 30V, V _{GS} = 0V f = 1.0MHz
Reverse Transfer Capacitance	Crss	_	2.4	_	pF	1 = 1.0ivin2
Gate Resistance	Rg	_	101	_	Ω	$f = 1MHz$, $V_{GS} = 0V$, $V_{DS} = 0V$
Total Gate Charge	Qg	_	0.5	_	nC	45)/ / 40)/
Gate-Source Charge	Qgs	_	0.09	_	nC	V _G S = 4.5V, V _D S = 10V, I _D = 250mA
Gate-Drain Charge	Qgd	_	0.09	_	nC	1D = 25011A
Turn-On Delay Time	tD(ON)	_	2.4	_	ns	
Turn-On Rise Time	t _R	_	2.5	_	ns	V _{DD} = 30V, V _{GS} = 10V,
Turn-Off Delay Time	tD(OFF)	_	22.6	_	ns	$R_g = 25\Omega$, $I_D = 200mA$
Turn-Off Fall Time	tF	_	12.5	_	ns	

Device mounted on FR-4 PCB, with minimum recommended pad layout.
Device mounted on 1" x 1" FR-4 PCB with high coverage 2oz. copper, single sided.
Short duration pulse test used to minimize self-heating effect.
Guaranteed by design. Not subject to product testing.





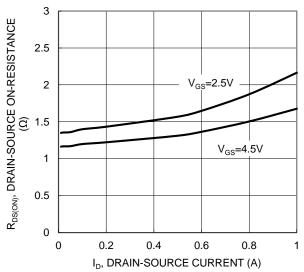


Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage

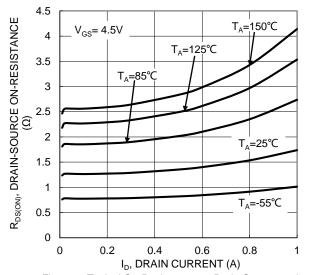
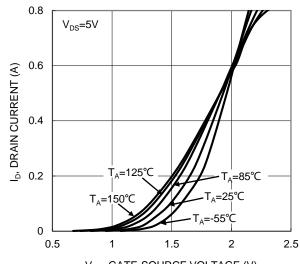


Figure 5. Typical On-Resistance vs. Drain Current and Temperature



V_{GS}, GATE-SOURCE VOLTAGE (V) Figure 2. Typical Transfer Characteristic

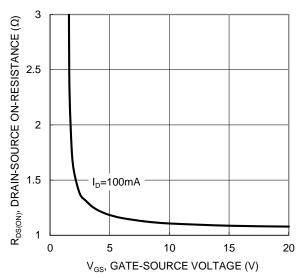


Figure 4. Typical Transfer Characteristic

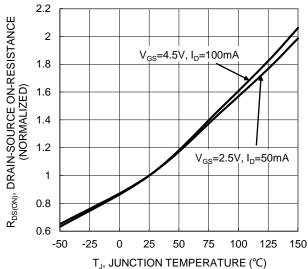


Figure 6. On-Resistance Variation with Junction Temperature



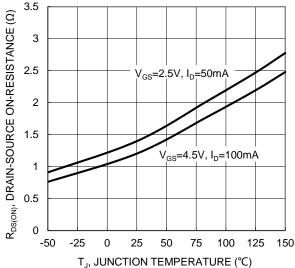


Figure 7. On-Resistance Variation with Junction Temperature

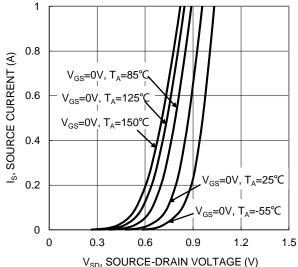


Figure 9. Diode Forward Voltage vs. Current

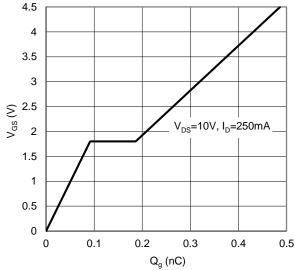


Figure 11. Gate Charge

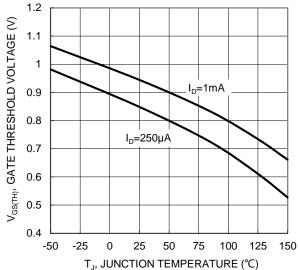
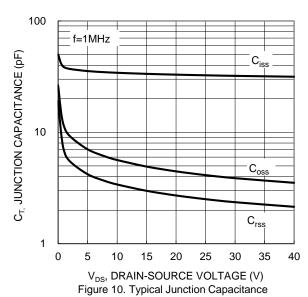


Figure 8. Gate Threshold Variation vs. Junction Temperature



10 R_{DS(ON)} Limited DRAIN CURRENT (A) 0.1 P_W=100ms T_{J(MAX)}=150℃ ے_ 0.01 T_C=25°C Single Pulse DUT on 1*MRP board V_{GS}=4.5V 0.001 0.1 10 100 1 V_{DS}, DRAIN-SOURCE VOLTAGE (V)

Figure 12. SOA, Safe Operation Area



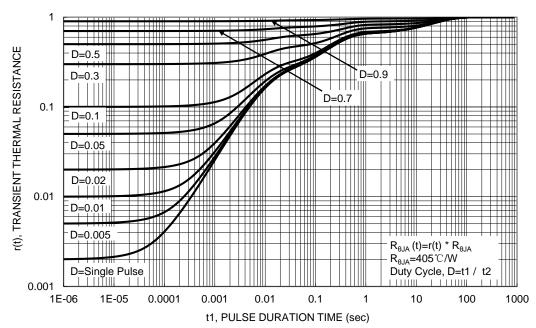


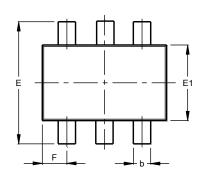
Figure 13. Transient Thermal Resistance

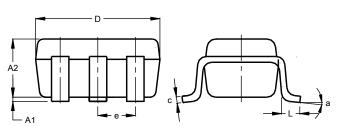


Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT363



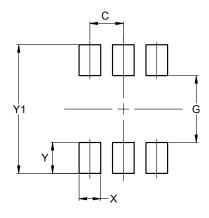


SOT363							
Dim	Min	Max	Тур				
A1	0.00	0.10	0.05				
A2	0.90	1.00	0.95				
b	0.10	0.30	0.25				
С	0.10	0.22	0.11				
D	1.80	2.20	2.15				
E	2.00	2.20	2.10				
E1	1.15	1.35	1.30				
е	0.650 BSC						
F	0.40	0.45	0.425				
L	0.25	0.40	0.30				
а	0°	8°					
All Dimensions in mm							

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT363



Dimensions	Value (in mm)			
С	0.650			
G	1.300			
Х	0.420			
Y	0.600			
Y1	2 500			



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