



N-CHANNEL ENHANCEMENT MODE FIELD EFFECT TRANSISTOR

Product Summary

BV _{DSS}	Rds(on)	I _D T _A = +25°C
50V	1.6Ω @ V _{GS} = 10V	350mA
507	2.5Ω @ V _{GS} = 4.5V	200mA

Description and Applications

This MOSFET is designed to meet the stringent requirements of automotive applications. It is qualified to AEC-Q101, supported by a PPAP and is ideal for use in:

- Motor driving
- Power management functions
- Load switching

Features and Benefits

- N-Channel MOSFET
- Low On-Resistance
- Very Low Gate Threshold Voltage
- Low Input Capacitance
- Fast Switching Speed
- Low Input/ Output Leakage
- Ultra-Small Surface Mount Package
- ESD Protected
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- The DIODES™ DMN53D0LTQ is suitable for automotive applications requiring specific change control; this part is AEC-Q101 qualified, PPAP capable, and manufactured in IATF 16949 certified facilities.

https://www.diodes.com/quality/product-definitions/

Mechanical Data

- Package: SOT523
- Package Material: Molded Plastic. "Green" Molding Compound.
 UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Matte Tin Finish Annealed over Alloy 42 Leadframe (Lead Free Plating). Solderable per MIL-STD-202, Method 208 @3
- Terminal Connections: See Diagram
- Weight: 0.002 grams (Approximate)

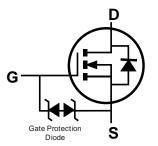


ESD PROTECTED

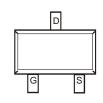


SOT523

Top View



Internal Schematic



Top View

Ordering Information (Note 4)

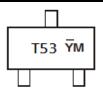
Part Number	Packago	Packing		
Part Number	Package	Qty.	Carrier	
DMN53D0LTQ-7	SOT523	3000	Reel	
DMN53D0LTQ-13	SOT523	10000	Reel	

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.



Marking Information



 $\begin{array}{l} \underline{T53} = Product\ Type\ Marking\ Code\\ \overline{Y}M = Date\ Code\ Marking\\ \overline{Y} = Year\ (ex:\ J=2022)\\ M = Month\ (ex:\ 9=September) \end{array}$

Date Code Key

Year	2022	2023	2024	2025	2026	2027	2028	2029	2030	2031	2032	2033
Code	J	K	L	М	N	0	Р	R	S	T	U	V
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec

Characteristi	С	Symbol	Value	Unit
Drain Source Voltage		VDSS	50	V
Gate-Source Voltage		Vgss	±20	V
Continuous Drain Current (Note 5) V _{GS} = 10V	Steady $T_A = +25^{\circ}C$ State $T_A = +70^{\circ}C$		350 280	mA
Maximum Continuous Body Diode Fo	orward Current (Note 5	Is	350	mA
Pulsed Drain Current (10µs Pulse, D	uty Cycle = 1%)	I _{DM}	1.0	А

Thermal Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Total Power Dissipation (Note 5)	PD	300	mW
Thermal Resistance, Junction to Ambient (Note 5)	$R_{\theta JA}$	420	°C/W
Operating and Storage Temperature Range	TJ, TSTG	-55 to +150	°C

Notes: 5. Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper pad layout..



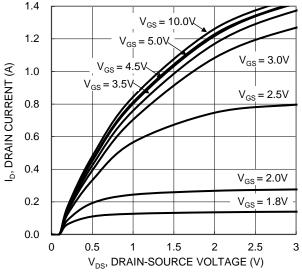
Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

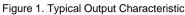
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 6)	•		•	•			
Drain-Source Breakdown Voltage	BVDSS	50	_	_	V	$V_{GS} = 0V$, $I_{D} = 250 \mu A$	
Zero Gate Voltage Drain Current	IDSS	_	_	1	μΑ	V _{DS} = 50V, V _{GS} = 0V	
Gate-Body Leakage	Igss	_	_	10	μΑ	$V_{GS} = \pm 20V$, $V_{DS} = 0V$	
ON CHARACTERISTICS (Note 6)							
Gate Threshold Voltage	V _{GS(TH)}	0.8	_	1.5	V	$V_{DS} = V_{GS}$, $I_D = 250 \mu A$	
Static Drain-Source On-Resistance	R _{DS} (ON)		1.06 1.09 1.45	1.6 2.5 4.5	Ω	$V_{GS} = 10V, I_D = 500mA$ $V_{GS} = 4.5V, I_D = 200mA$ $V_{GS} = 2.5V, I_D = 100mA$	
Source-Drain Diode Forward Voltage	VsD	_	0.88	1.4	V	Vgs = 0V, Is = 500mA	
DYNAMIC CHARACTERISTICS (Note 7)							
Input Capacitance	Ciss		46	_	pF		
Output Capacitance	Coss	_	5.3	_	pF	V _{DS} = 25V, V _{GS} = 0V f = 1.0MHz	
Reverse Transfer Capacitance	Crss	_	4.0	_	pF	1 - 1.000112	
Gate Resistance	Rg	_	51	_	Ω	$f = 1MHz$, $V_{GS} = 0V$, $V_{DS} = 0V$	
Total Gate Charge (V _{GS} = 10V)	Qg	_	1.4	_	nC		
Total Gate Charge (V _{GS} = 4.5V)	Qg	_	0.6	_	nC	V _{DS} = 10V,	
Gate-Source Charge	Q _{gs}	_	0.2	_	nC	I _D = 250mA	
Gate-Drain Charge	Qgd	_	0.1	_	nC		
Turn-On Delay Time	t _D (ON)	_	2.7	_	ns		
Turn-On Rise Time	t _R	_	2.5	_	ns	$V_{DD} = 30V, V_{GS} = 10V,$	
Turn-Off Delay Time	tD(OFF)	_	19	_	ns	$R_G = 25\Omega, I_D = 200mA$	
Turn-Off Fall Time	tF		11		ns		

Notes:

Short duration pulse test used to minimize self-heating effect.
 Guaranteed by design. Not subject to product testing.







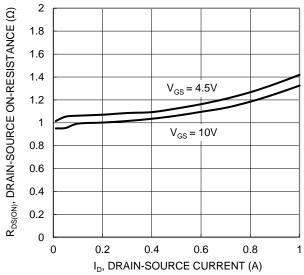


Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage

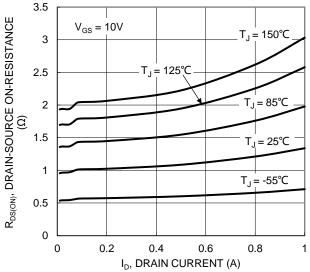


Figure 5. Typical On-Resistance vs. Drain Current and Temperature

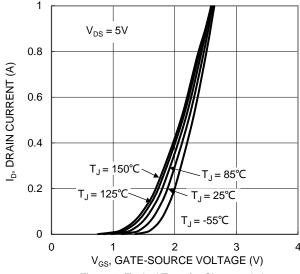


Figure 2. Typical Transfer Characteristic

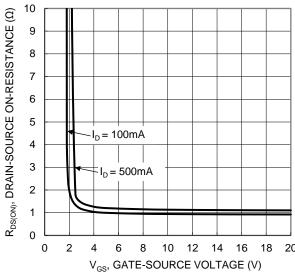


Figure 4. Typical Transfer Characteristic

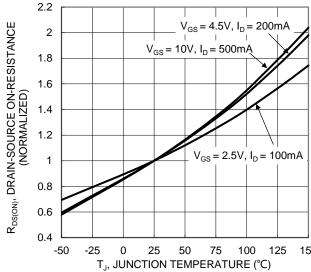


Figure 6. On-Resistance Variation with Temperature



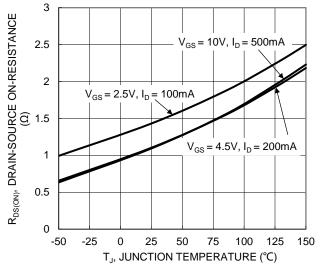


Figure 7. On-Resistance Variation with Temperature

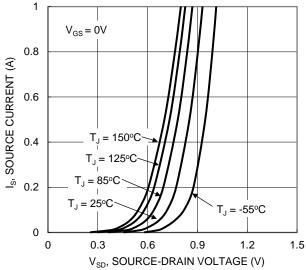


Figure 9. Diode Forward Voltage vs. Current

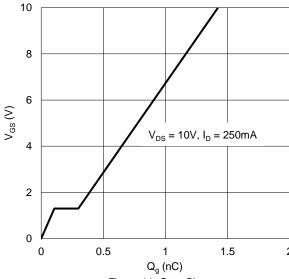


Figure 11. Gate Charge

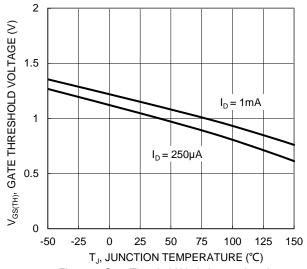


Figure 8. Gate Threshold Variation vs. Junction Temperature

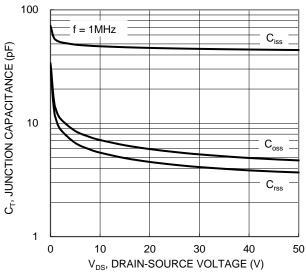


Figure 10. Typical Junction Capacitance

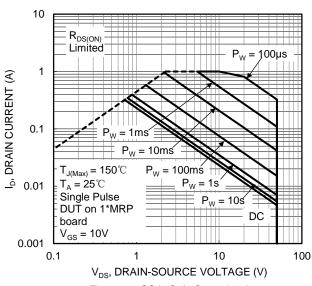


Figure 12. SOA, Safe Operation Area



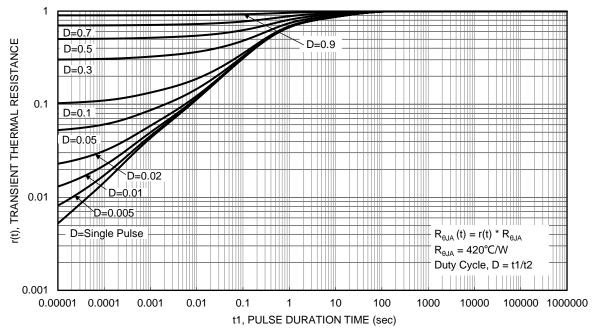


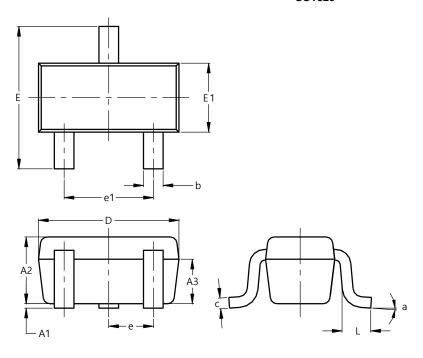
Figure 13. Transient Thermal Resistance



Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT523

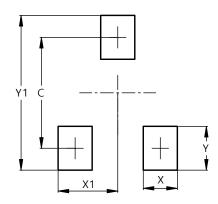


SOT523							
Dim	Min	Max	Тур				
A1	0.00	0.10	0.05				
A2	0.60	0.80	0.75				
A3	0.45	0.65	0.50				
b	0.15	0.30	0.22				
С	0.10	0.20	0.12				
D	1.50	1.70	1.60				
Е	1.45	1.75	1.60				
E1	0.75	0.85	0.80				
е	0.50 BSC						
e1	0.90	1.10	1.00				
L	0.20	0.40	0.33				
а	0°		8°				
All Dimensions in mm							

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT523



Dimensions	Value (in mm)
С	1.29
Х	0.40
X1	0.70
Υ	0.51
Y1	1.80



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