



Product Summary

BV _{DSS}	Rds(on)	ID T _A = +25°C
30V	460mΩ @ V _{GS} = 4.5V	1.1A
307	560mΩ @ V _{GS} = 2.5V	1.0A

Description

This new generation MOSFET is designed to minimize the on-state resistance (R_{DS(ON)}) yet maintain superior switching performance, making it ideal for high-efficiency power-management applications.

Applications

- Backlighting
- DC-DC converters
- Power-management functions

DUAL N-CHANNEL ENHANCEMENT MODE MOSFET

Features and Benefits

- Low On-Resistance
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- An automotive-compliant part is available under separate datasheet (DMN3732UVTQ)

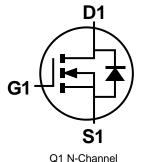
Mechanical Data

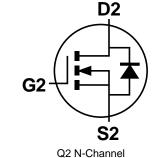
- Package: TSOT26
- Package Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals Connections: See Diagram
- Terminals: Finish Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208
- Weight: 0.013 grams (Approximate)



G1 1	6	D1
S2 2	5	S1
G2 3	4	D2
	I	

Top View





Top View

Ordering Information (Note 4)

Part Number	Packago	Packing			
Fait Nulliber	Package	Qty.	Carrier		
DMN3732UVT-7	TSOT26	3,000	Tape & Reel		
DMN3732UVT-13	TSOT26	10,000	Tape & Reel		

Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and

Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information

	26
BQ	3 ⊮≻
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 $\begin{array}{l} BQ3 = Product Type Marking Code\\ \overline{YM} = Date Code Marking\\ \overline{Y} = Year (ex: K = 2023)\\ M = Month (ex: 9 = September) \end{array}$

Date Code Kev

Date Code Key												
Year	2022	2023	2024	2025	2026	2027	2028	2029	2030	2031	2032	2033
Code	J	K	L	М	Ν	Р	R	S	Т	U	V	W
				1	1		1			1	1	1
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	Ν	D



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit		
Drain-Source Voltage	Vdss	30	V		
Gate-Source Voltage	Vgss	±8	V		
Continuous Drain Current (Note 6) V/cs – 4 5V/		T _A = +25°C T _A = +70°C	lD	1.1 0.9	А
Maximum Continuous Body Diode Forward Current (Note	ls	0.72	A		
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)	IDM	2.4	A		

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 5)	PD	0.5	W
Thermal Resistance, Junction to Ambient $@T_A = +25$ °C (Note 5)	Reja	252	°C/W
Power Dissipation (Note 6)	PD	0.8	W
Thermal Resistance, Junction to Ambient $@T_A = +25$ °C (Note 6)	R _{0JA}	150	°C/W
Operating and Storage Temperature Range	TJ, TSTG	-55 to +150	°C

Electrical Characteristics – N-Channel (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)	• • • • • •		. 76		•••••	
Drain-Source Breakdown Voltage	BV _{DSS}	30		—	V	$V_{GS} = 0V, I_D = 10\mu A$
Zero Gate Voltage Drain Current	IDSS		_	1	μA	$V_{DS} = 30V, V_{GS} = 0V$
Gate-Source Leakage	lgss			±10	μA	$V_{GS} = \pm 8V, V_{DS} = 0V$
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	Vgs(th)	0.45		0.95	V	$V_{DS} = V_{GS}$, $I_D = 250 \mu A$
			310	460		$V_{GS} = 4.5V, I_D = 200mA$
Static Drain-Source On-Resistance	RDS(ON)	—	360 430	560 730	mΩ	$V_{GS} = 2.5V, I_{D} = 100mA$
						V _{GS} = 1.8V, I _D = 75mA
Diode Forward Voltage	V _{SD}	_	0.7	1.2	V	$V_{GS} = 0V, I_{S} = 300mA$
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	Ciss		40.8	—		
Output Capacitance	Coss		7.6	—		V _{DS} = 25V, V _{GS} = 0V, f = 1.0MHz
Reverse Transfer Capacitance	Crss		4.6	_	pF	
Total Gate Charge (V _{GS} = 10V)	Qg	_	0.9	—	рг	
Gate-Source Charge	Qgs		0.05	_		$V_{GS} = 4.5V, V_{DS} = 15V,$
Gate-Drain Charge	Q _{gd}	_	0.3	—		$I_D = 1A$
Turn-On Delay Time	tD(ON)	_	1.1	—		
Turn-On Rise Time	tR		15.9	—		V _{DS} = 10V, I _D = 1A
Turn-Off Delay Time	tD(OFF)		20.7	—	ns	$V_{GS} = 10V, R_g = 6\Omega$
Turn-Off Fall Time	tF		20.0	_]	

Notes:

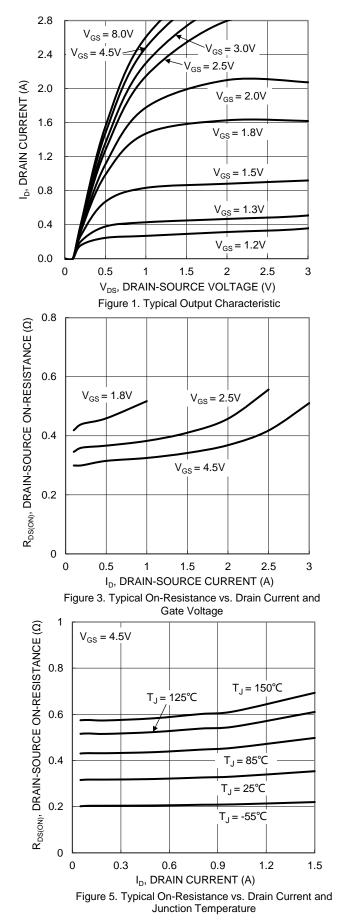
Device mounted on FR-4 substrate PCB, 2oz copper, with minimum recommended pad layout.
Device mounted on FR-4 substrate PCB, 2oz copper, with 1inch square copper plate.

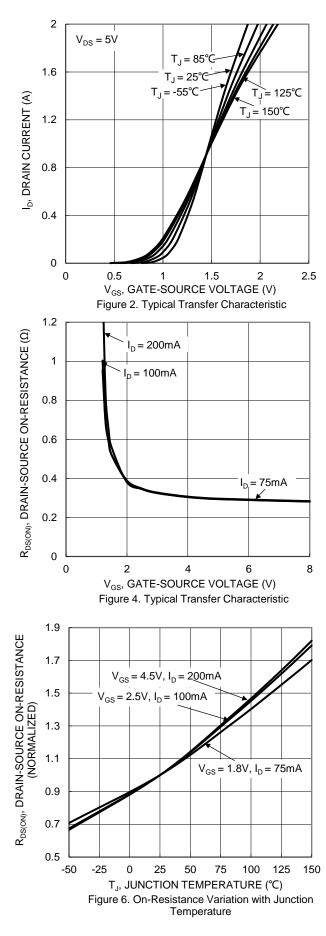
7. Short duration pulse test used to minimize self-heating effect.

8. Guaranteed by design. Not subject to production testing.





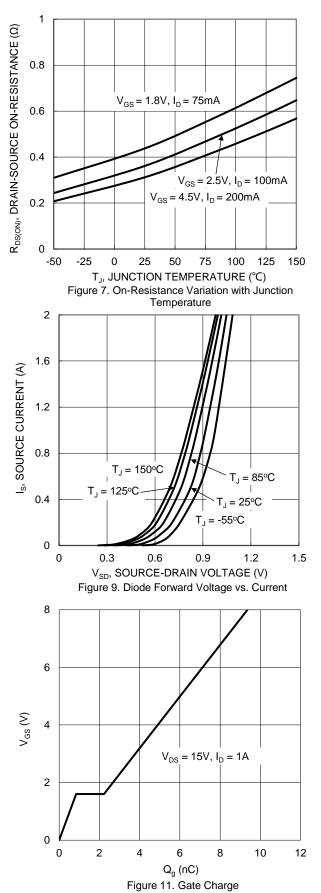


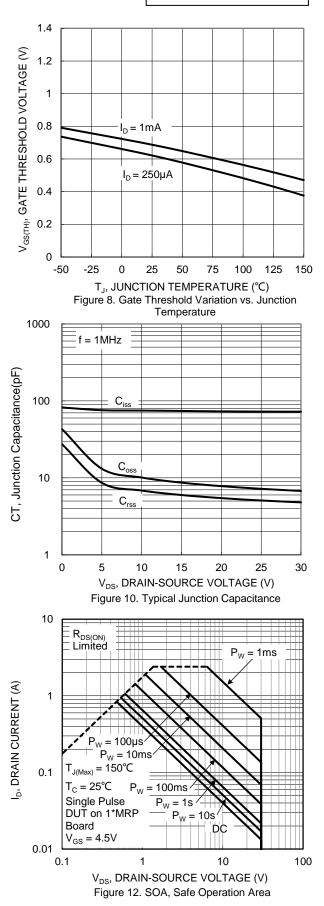


DMN3732UVT Document number: DS45052 Rev. 2 - 2

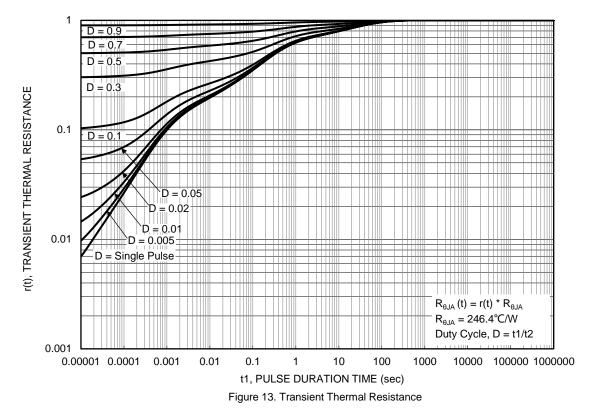


DMN3732UVT





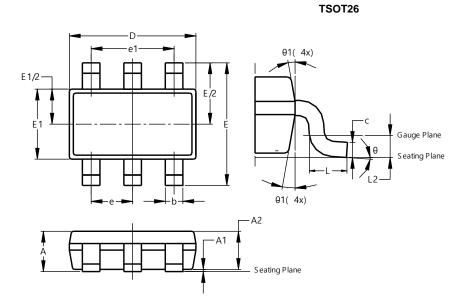






Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

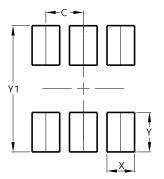


	TSOT26							
Dim	Min	Max	Тур					
Α	-	1.00	-					
A1	0.010	0.100	-					
A2	0.840	0.900	-					
D	2.800	3.000	2.900					
Е	2	.800 BS	С					
E1	1.500	1.700	1.600					
b	0.300	0.450	-					
С	0.120	0.200	-					
е	0	.950 BS	С					
e1	1	.900 BS	С					
L	0.30	0.50	-					
L2	0	.250 BS	С					
θ	0°	8°	4°					
θ1	4°	12°	-					
A	II Dimen	sions in	mm					

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

TSOT26



Dimensions	Value (in mm)
С	0.950
Х	0.700
Y	1.000
Y1	3.200



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