

Product Summary

BV _{DSS}	Rds(ON) Max	I _D Max @T _A = +25°C
30V	1.5Ω @ V _{GS} = 4.5V	0.43A
	2.0Ω @ V _{GS} = 2.5V	0.37A
	3.0Ω @ V _{GS} = 1.8V	0.3A
	4.5Ω @ V _{GS} = 1.5V	0.25A

Description

This new generation MOSFET is designed to minimize the on-state resistance ($R_{DS(ON)}$) yet maintain superior switching performance, making it ideal for high-efficiency power-management applications.

Applications

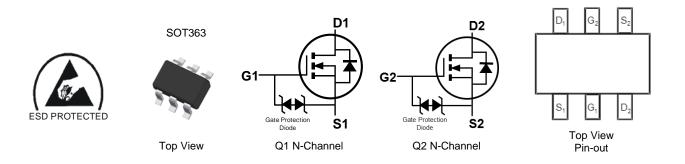
- Battery operated systems and solid-state relays
- Drivers, relays, solenoids, lamps, hammers, displays, memories, transistors, etc.
- Power supply converter circuits

Features and Benefits

- Low On-Resistance
- Low Gate Threshold Voltage
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- Ultra-Small Surface-Mount Package
- ESD Protected
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/104/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please <u>contact us</u> or your local Diodes representative. <u>https://www.diodes.com/quality/product-definitions/</u>

Mechanical Data

- Package: SOT363
- Package Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Matte Tin Finish Annealed over Alloy 42 Leadframe (Lead-Free Plating). Solderable per MIL-STD-202, Method 208 (3)
- Terminal Connections: See Diagram
- Weight: 0.006 grams (Approximate)



Ordering Information (Note 4)

Part Number	Package	Packing		
Fart Nulliber	Fackage	Qty.	Carrier	
DMN31D5UDW-7	SOT363	3,000	Tape & Reel	
DMN31D5UDW-13	SOT363	10,000	Tape & Reel	

Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.

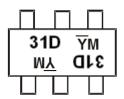
2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.



Marking Information



 $\begin{array}{l} 31D = \mbox{Product Type Marking Code} \\ \overline{Y}M = \mbox{Date Code Marking} \\ \overline{Y} = \mbox{Year (ex: K = 2023)} \\ M = \mbox{Month (ex: 9 = September)} \end{array}$

Date Code Key												
Year	2022	2023	2024	2025	2026	2027	2028	2029	2030	2031	2032	2033
Code	J	К	L	М	Ν	0	Р	R	S	Т	U	V
Month	Jan	Feb	Mar	Apr	Мау	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	Ν	D

Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Character	Symbol	Value	Unit		
Drain-Source Voltage	Vdss	30	V		
Gate-Source Voltage	Vgss	±12	V		
Continuous Drain Current (Note 5) V_{GS} = 4.5V	Steady State	T _A = +25°C T _A = +70°C	ID	0.43 0.28	А
Maximum Continuous Body Diode Forward Curre	ls	0.37	A		
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1	Ідм	0.65	A		

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 6)	T _A = +25°C	PD	0.33	W
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	Reja	380	°C/W
Total Power Dissipation (Note 5)	T _A = +25°C	PD	0.43	W
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	Reja	290	°C/W
Operating and Storage Temperature Range		TJ, TSTG	-55 to +150	°C

Notes:

Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate
Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.



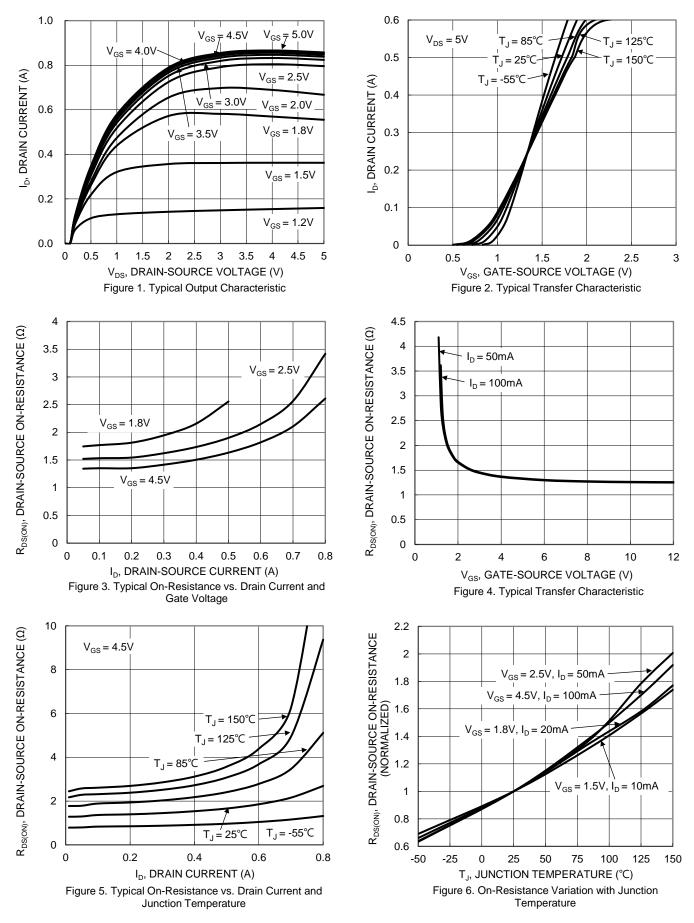
Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)	Cymbol		Typ	Max	onic	Test condition
Drain-Source Breakdown Voltage	BVDSS	30	_	_	V	Vgs = 0V, ID = 250µA
Zero Gate Voltage Drain Current @Tc = +25°C	IDSS	_	_	100	nA	$V_{DS} = 24V, V_{GS} = 0V$
Gate-Source Leakage	lgss	_	_	±10	μA	$V_{GS} = \pm 10V, V_{DS} = 0V$
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	VGS(TH)	0.5	_	0.9	V	Vds = Vgs, Id = 250µA
			1.3	1.5		$V_{GS} = 4.5V, I_D = 100mA$
Static Drain-Source On-Resistance	Basian	_	1.4	2.0	Ω	$V_{GS} = 2.5V, I_D = 50mA$
	Rds(on)		1.7	3.0		$V_{GS} = 1.8V, I_{D} = 20mA$
			1.9	4.5		$V_{GS} = 1.5V, I_{D} = 10mA$
Diode Forward Voltage (Note 7)	Vsd	—	0.7	1.0	mV	Vgs = 0V, Is = 100mA
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	Ciss	—	15.4	—	pF	
Output Capacitance	Coss	—	8	_	pF	VDS = 15V, VGS = 0V f = 1.0MHz
Reverse Transfer Capacitance	Crss	—	5	—	pF	
Total Gate Charge	Qg	—	0.3	—	nC	
Gate-Source Charge	Qgs	—	0.05	—	nC	$V_{GS} = 4.5V, V_{DS} = 15V$ ID = 200mA
Gate-Drain Charge	Q_{gd}	—	0.1	—	nC	ID = 200IIIA
Turn-On Delay Time	td(on)	—	5.7	—	ns	
Turn-On Rise Time	tR	—	9.1	_	ns	V _{DD} = 15V, V _{GS} = 4.5V
Turn-Off Delay Time	td(OFF)	—	146	—	ns	$R_G = 2\Omega$, $I_D = 200mA$
Turn-Off Fall Time	t _F	—	48	—	ns	

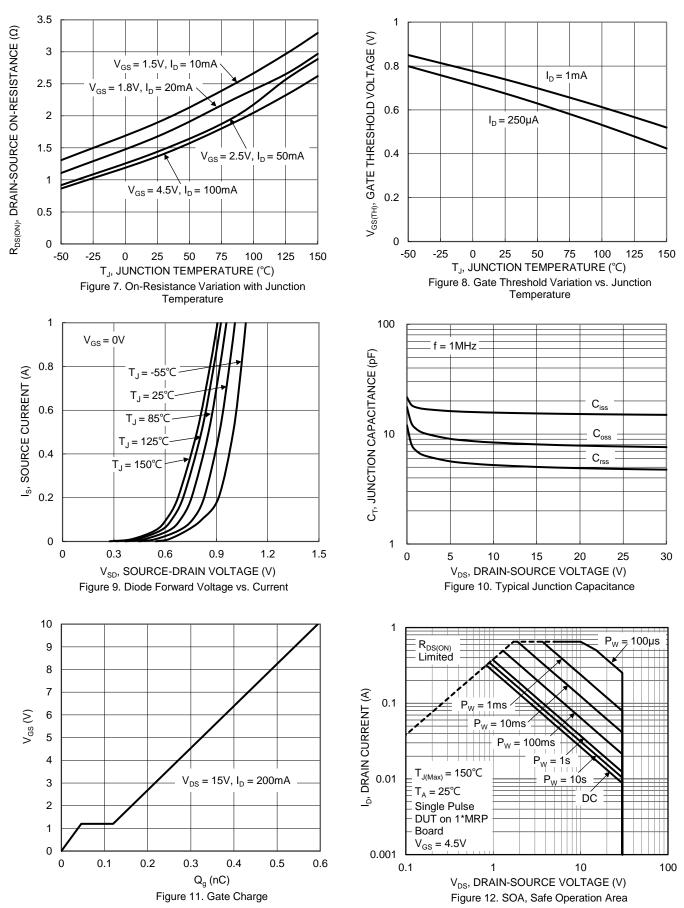
Notes:7. Short duration pulse test used to minimize self-heating effect.
8. Guaranteed by design. Not subject to production testing.



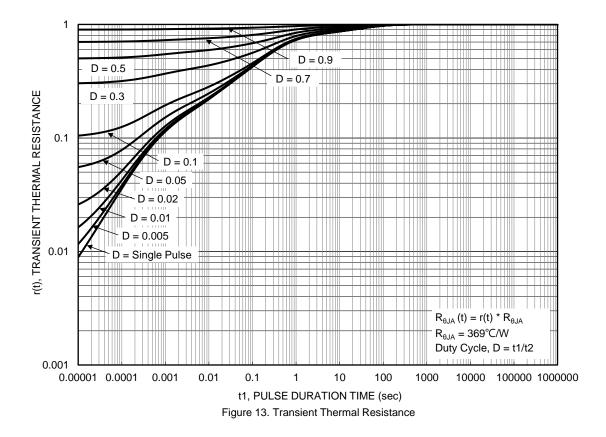
DMN31D5UDW







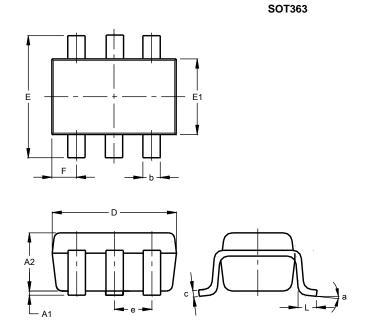






Package Outline Dimensions

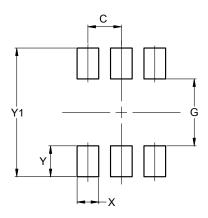
Please see http://www.diodes.com/package-outlines.html for the latest version.



SOT363								
Dim	Min	Max	Тур					
A1	0.00	0.10	0.05					
A2	0.90	1.00	0.95					
b	0.10	0.30	0.25					
c	0.10	0.22	0.11					
D	1.80	2.20	2.15					
Е	2.00	2.20	2.10					
E1	1.15	1.35	1.30					
е	C).650 B	SC					
F	0.40	0.45	0.425					
L	0.25	0.40	0.30					
а	0°	8°						
All I	All Dimensions in mm							

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.



Dimensions	Value (in mm)
С	0.650
G	1.300
Х	0.420
Y	0.600
Y1	2.500

SOT363



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