



N-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

BV _{DSS}	R _{DS(ON)} Max	I _D Max T _A = +25°C
201/	$59mΩ @V_{GS} = 10V$	2.9A
30V	$98m\Omega @V_{GS} = 4.5V$	2.3A

Features

- Low On-Resistance
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- The DIODES™ DMN3061SQ is suitable for automotive applications requiring specific change control; this part is AEC-Q101 qualified, PPAP capable, and manufactured in IATF 16949 certified facilities.

https://www.diodes.com/quality/product-definitions/

Description and Applications

This MOSFET is designed to meet the stringent requirements of automotive applications. It is qualified to AEC-Q101, supported by a PPAP and is ideal for use in:

- General-purpose interfacing switches
- Power-management functions

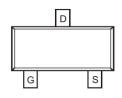
Mechanical Data

- Package: SOT23
- Package Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 (e3)
- Terminals Connections: See Diagram Below
- Weight: 0.008 grams (Approximate)

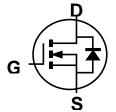








Top View



Equivalent Circuit

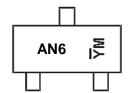
Ordering Information (Note 4)

Part Number	Backago	Packing		
Fait Nullibei	Package	Qty.	Carrier	
DMN3061SQ-7	SOT23	3,000	Tape & Reel	
DMN3061SQ-13	SOT23	10,000	Tape & Reel	

Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.

- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information



AN6 = Product Type Marking Code YM = Date Code Marking \overline{Y} = Year (ex: K = 2023) M = Month (ex: 1 = January)

Date Code Key

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Year	2022	2023	2024	2025	2026	2027	2028	2029	2030	2031	2032	2033
Code	J	K	L	M	N	0	Р	R	S	Т	U	V
Month	lon	Fab	Mar	Amr	May	lum	l. d	A	Con	Ont	May	Daa
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec



Maximum Ratings (@ $T_A = +25^{\circ}C$, unless otherwise specified.)

Characte	eristic		Symbol	Value	Unit
Drain-Source Voltage		VDSS	30	V	
Gate-Source Voltage			Vgss	±20	V
Continuous Drain Current (Note 5) V _{GS} = 4.5V	$T_A = +25$ °C $T_A = +70$ °C	lo	2.3 1.8	А	
Maximum Continuous Body Diode For	ote 6)	Is	1.3	Α	
Pulsed Drain Current (10µs Pulse, Dut	y Cycle = 1%)		I _{DM}	24	Α

Thermal Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 5)	PD	0.77	W
Thermal Resistance, Junction to Ambient @T _A = +25°C (Note 5)	Reja	161	°C/W
Power Dissipation (Note 6)	PD	1.23	W
Thermal Resistance, Junction to Ambient @T _A = +25°C (Note 6)	Reja	101	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C

Electrical Characteristics (@T_A = ±25°C, unless otherwise specified.)

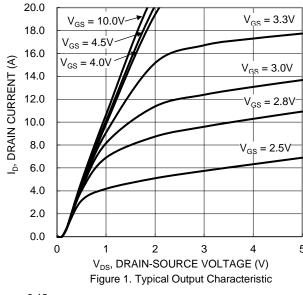
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)							
Drain-Source Breakdown Voltage	BVDSS	30	_	_	V	Vgs = 0V, ID = 250µA	
Zero Gate Voltage Drain Current	IDSS	_	1	1.0	μΑ	V _{DS} = 24V, V _{GS} = 0V	
Gate-Source Leakage	Igss	_	_	±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 7)	ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	Vgs(th)	0.5	1.3	1.8	V	$V_{DS} = V_{GS}$, $I_D = 250\mu A$	
			28	59		Vgs = 10V, ID = 3.1A	
Static Drain-Source On-Resistance	RDS(ON)	_	35	98	$m\Omega$	$V_{GS} = 4.5V, I_{D} = 2A$	
			45	196		$V_{GS} = 3.3V, I_{D} = 1.5A$	
Diode Forward Voltage	VsD	_	0.7	1.0	V	Vgs = 0V, Is = 1A	
DYNAMIC CHARACTERISTICS (Note 8)							
Input Capacitance	Ciss	_	233		pF	V 45V V 0V	
Output Capacitance	Coss	_	45		pF	V _{DS} = 15V, V _{GS} = 0V, f = 1.0MHz	
Reverse Transfer Capacitance	Crss	_	31		pF		
Gate Resistance	Rg	_	5.6		Ω	$V_{DS} = 0V$, $V_{GS} = 0V$, $f = 1MHz$	
Total Gate Charge (V _{GS} = 4.5V)	Qg	_	2.9	1	nC	V _{DS} = 15V, V _{GS} = 4.5V, I _D = 3A	
Total Gate Charge (V _{GS} = 10V)	Qg	_	5.5	1	nC		
Gate-Source Charge	Qgs	_	0.6	1	nC	$V_{DS} = 15V, V_{GS} = 10V, I_{D} = 3A$	
Gate-Drain Charge	Qgd	_	0.9	1	nC		
Turn-On Delay Time	t _D (ON)	_	0.9	1	ns		
Turn-On Rise Time	t _R	_	2.8	1	ns	V _G S = 10V, V _D S = 15V,	
Turn-Off Delay Time	t _{D(OFF)}	_	10.0	_	ns	$R_G = 3\Omega$, $R_L = 1.7\Omega$	
Turn-Off Fall Time	tF	_	2.4	_	ns		

Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.
 Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.

^{7.} Short duration pulse test used to minimize self-heating effect.

^{8.} Guaranteed by design. Not subject to product testing.





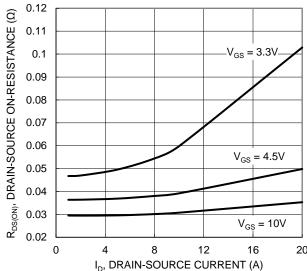


Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage

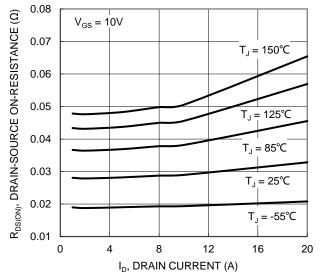
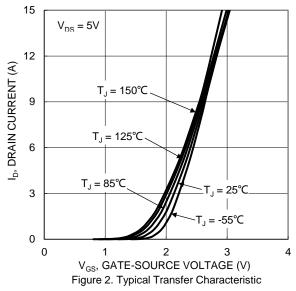
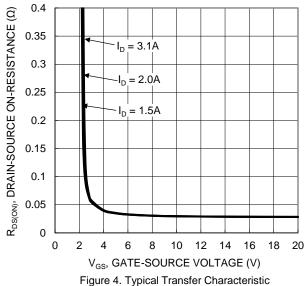


Figure 5. Typical On-Resistance vs. Drain Current and Junction Temperature





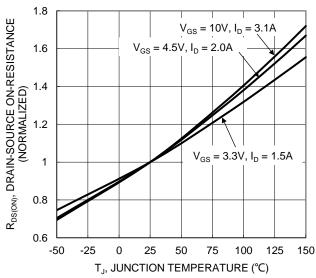


Figure 6. On-Resistance Variation with Junction Temperature



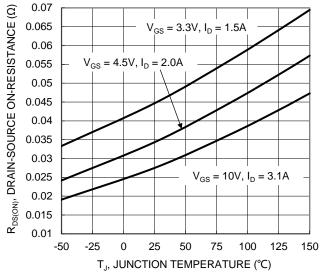


Figure 7. On-Resistance Variation with Junction Temperature

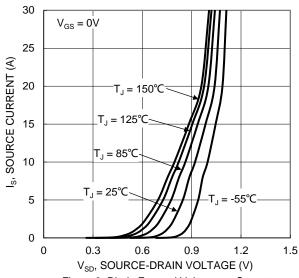


Figure 9. Diode Forward Voltage vs. Current

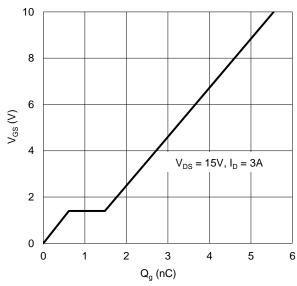


Figure 11. Gate Charge

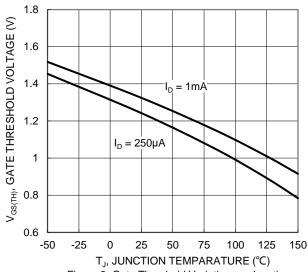


Figure 8. Gate Threshold Variation vs. Junction Temperature

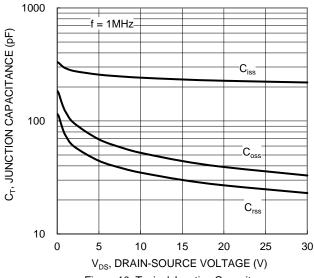


Figure 10. Typical Junction Capacitance

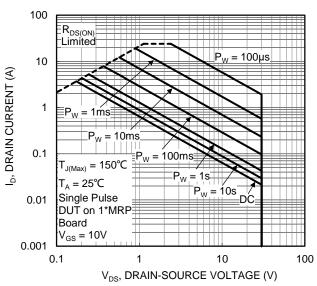


Figure 12. SOA, Safe Operation Area



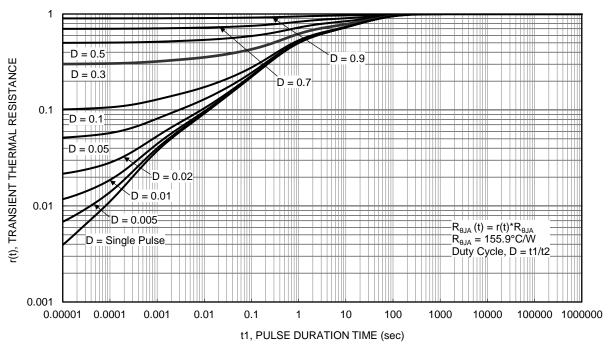


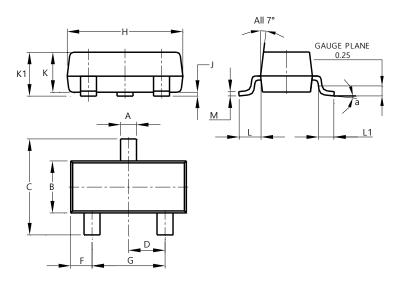
Figure 13. Transient Thermal Resistance



Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT23

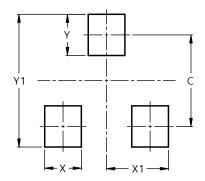


SOT23						
Dim	Min	Max	Тур			
Α	0.37	0.51	0.40			
В	1.20	1.40	1.30			
C	2.30	2.50	2.40			
D	0.89	1.03	0.915			
F	0.45	0.60	0.535			
G	1.78	2.05	1.83			
Η	2.80	3.00	2.90			
7	0.013	0.10	0.05			
K	0.890	1.00	0.975			
K 1	0.903	1.10	1.025			
٦	0.45	0.61	0.55			
L1	0.25	0.55	0.40			
М	0.085	0.150	0.110			
а	0°	8°				
All Dimensions in mm						

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT23



Dimensions	Value (in mm)			
С	2.0			
Х	0.8			
X1	1.35			
Y	0.9			
Y1	29			



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