



20V N-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

| BV _{DSS} | R _{DS(ON)} Max | I _D Max T _A = +25°C |
|-------------------|--------------------------------|----------------------------------------------|
| | 0.99Ω @ V _{GS} = 4.5V | 0.83A |
| 20V | 1.2Ω @ V _{GS} = 2.5V | 0.75A |
| | 1.8Ω @ V _{GS} = 1.8V | 0.61A |

Features and Benefits

- Footprint of Just 0.6mm² Thirteen Times Smaller than SOT23
- 0.4mm Profile Ideal for Low Profile Applications
- Low Gate Threshold Voltage
- Fast Switching Speed
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- The DIODES[™] DMN2992UFB4Q is suitable for automotive applications requiring specific change control; this part is AEC-Q101 qualified, PPAP capable, and manufactured in IATF 16949 certified facilities.

https://www.diodes.com/quality/product-definitions/

Description and Applications

This MOSFET is designed to minimize the on-state resistance (RDS(ON)) yet maintain superior switching performance, making it ideal for high-efficiency power-management applications.

Load switches





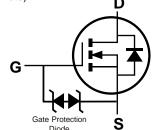
Bottom View

Mechanical Data

Top View Internal Schematic

- Package: X2-DFN1006-3
- Package Material: Molded Plastic, "Green" Molding Compound;
 UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish NiPdAu over Copper Leadframe; Solderable per MIL-STD-202, Method 208 (4)

Weight: 0.001 grams (Approximate)



Equivalent Circuit

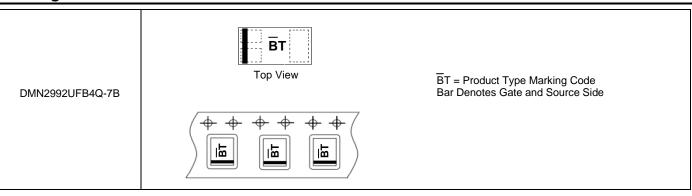
Ordering Information (Note 4)

| Part Number | Packago | Packing | | |
|-----------------|--------------|---------|-------------|--|
| Fait Nulliber | Package | Qty. | Carrier | |
| DMN2992UFB4Q-7B | X2-DFN1006-3 | 10,000 | Tape & Reel | |

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information





Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

| Characteristic | | | Symbol | Value | Unit |
|----------------------------------------------------------------------------------------------------------|--|----|-----------------|-------|------|
| Drain-Source Voltage | | | VDSS | 20 | V |
| Gate-Source Voltage | | | Vgss | ±8 | V |
| Continuous Drain Current (Note 5) $V_{GS} = 4.5V$ Steady $T_A = +25^{\circ}C$ State $T_A = +70^{\circ}C$ | | lo | 0.83 0.66 | А | |
| Maximum Continuous Body Diode Forward Current (Note 5) | | | Is | 0.84 | A |
| Pulsed Drain Current (10μs Pulse, Duty Cycle = 1%) | | | I _{DM} | 1.52 | А |

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

| Characteristic | Symbol | Value | Unit | |
|--------------------------------------------------|--------------|-------------------|-------------|------|
| Total Power Dissipation (Note 6) | | PD | 0.38 | W |
| Thermal Resistance, Junction to Ambient (Note 6) | Steady State | R _θ JA | 328 | °C/W |
| Total Power Dissipation (Note 5) | | P _D | 1.02 | W |
| Thermal Resistance, Junction to Ambient (Note 5) | Steady State | Reja | 122 | °C/W |
| Operating and Storage Temperature Range | | TJ, TSTG | -55 to +150 | °C |

Electrical Characteristics (@TA = +25°C, unless otherwise specified.)

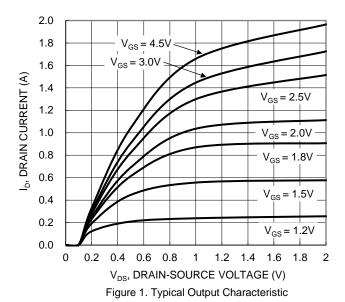
| Characteristic | Symbol | Min | Тур | Max | Unit | Test Condition | |
|----------------------------------------------------------|---------------------|-----|------|------|------|--------------------------------------------------------------|--|
| OFF CHARACTERISTICS (Note 7) | | | | | | | |
| Drain-Source Breakdown Voltage | BV _{DSS} | 20 | _ | _ | V | $V_{GS} = 0V, I_{D} = 250\mu A$ | |
| Zero Gate Voltage Drain Current (T _J = +25°C) | IDSS | _ | _ | 100 | nA | V _{DS} = 16V, V _{GS} = 0V | |
| Gate-Source Leakage | Igss | _ | | ±200 | nA | $V_{GS} = \pm 5V, V_{DS} = 0V$ | |
| ON CHARACTERISTICS (Note 7) | | | | | | | |
| Gate Threshold Voltage | Vgs(th) | 0.4 | _ | 1.0 | V | $V_{DS} = V_{GS}$, $I_D = 250\mu A$ | |
| | | | 0.42 | 0.99 | Ω | V _{GS} = 4.5V, I _D = 100mA | |
| Static Drain-Source On-Resistance | R _{DS(ON)} | _ | 0.52 | 1.2 | | $V_{GS} = 2.5V, I_D = 50mA$ | |
| | | | 0.65 | 1.8 | | V _{GS} = 1.8V, I _D = 20mA | |
| Diode Forward Voltage | VsD | _ | 0.7 | 1.0 | V | V _G S = 0V, I _S = 150mA | |
| DYNAMIC CHARACTERISTICS (Note 8) | | | | | | | |
| Input Capacitance | Ciss | _ | 15.6 | _ | рF | V 40V V 0V | |
| Output Capacitance | Coss | _ | 5.4 | _ | рF | V _{DS} = 16V, V _{GS} = 0V f = 1.0MHz | |
| Reverse Transfer Capacitance | Crss | _ | 4 | _ | pF | 1 = 1.01/11/12 | |
| Total Gate Charge | Q_g | _ | 0.41 | _ | nC | V 45V V 40V | |
| Gate-Source Charge | Qgs | _ | 0.07 | _ | nC | $V_{GS} = 4.5V, V_{DS} = 10V$ $I_{D} = 250 \text{mA}$ | |
| Gate-Drain Charge | Qgd | _ | 0.12 | _ | nC | ID = 230IIIA | |
| Turn-On Delay Time | tD(ON) | _ | 1.77 | _ | ns | | |
| Turn-On Rise Time | t _R | _ | 4.5 | _ | ns | V _{DD} = 10V, V _{GS} = 4.5V | |
| Turn-Off Delay Time | tD(OFF) | _ | 22 | _ | ns | $R_L = 47\Omega$, $R_G = 10\Omega$ $I_D = 200 \text{mA}$ | |
| Turn-Off Fall Time | tF | _ | 8.2 | _ | ns | | |

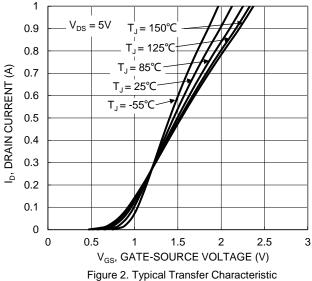
Notes: 5. Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.

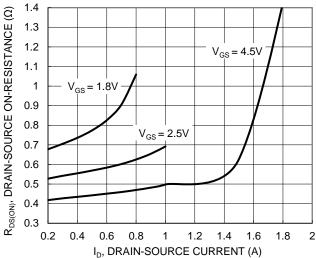
^{6.} Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout. 7. Short duration pulse test used to minimize self-heating effect.

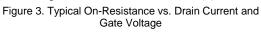
^{8.} Guaranteed by design. Not subject to product testing.











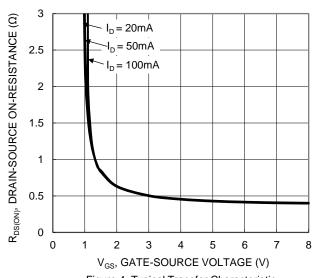


Figure 4. Typical Transfer Characteristic

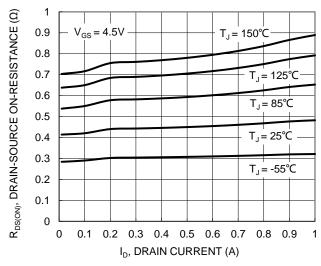


Figure 5. Typical On-Resistance vs. Drain Current and Junction Temperature

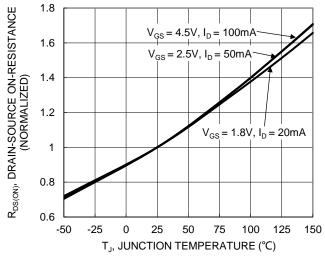


Figure 6. On-Resistance Variation with Junction Temperature



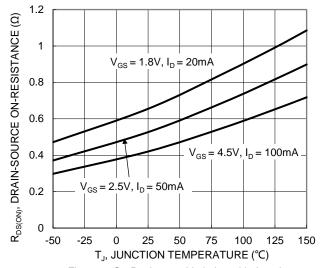


Figure 7. On-Resistance Variation with Junction Temperature

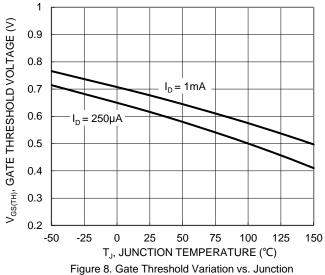


Figure 8. Gate Threshold Variation vs. Junction Temperature

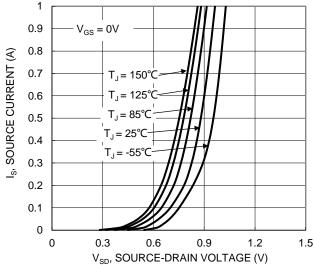


Figure 9. Diode Forward Voltage vs. Current

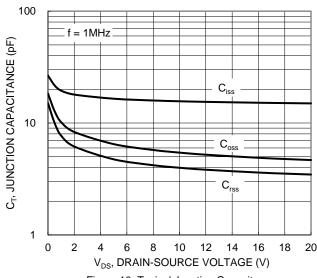


Figure 10. Typical Junction Capacitance

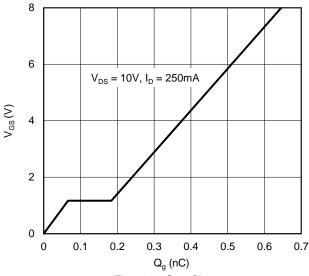


Figure 11. Gate Charge

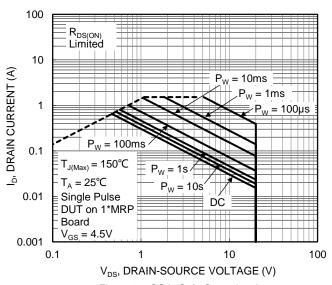


Figure 12. SOA, Safe Operation Area



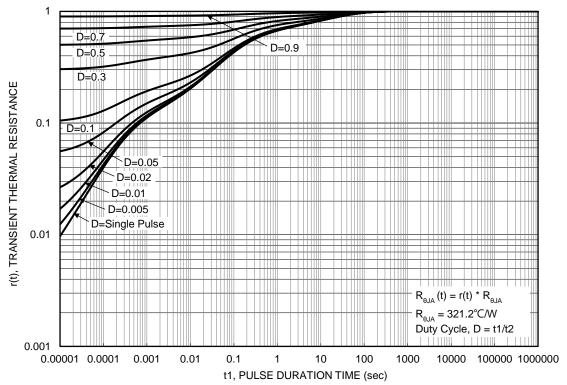


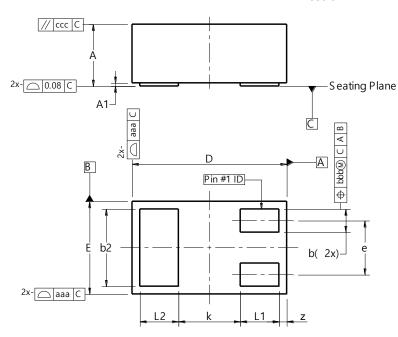
Figure 13. Transient Thermal Resistance



Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

X2-DFN1006-3

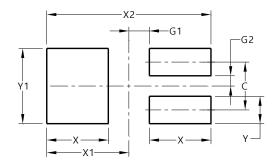


| X2-DFN1006-3 | | | | | |
|----------------------|----------------|------|------|--|--|
| Dim | Min | Max | Тур | | |
| Α | 1 | 0.40 | | | |
| A1 | 0.00 | 0.05 | 0.03 | | |
| b | 0.10 | 0.20 | 0.15 | | |
| b2 | 0.45 | 0.55 | 0.50 | | |
| D | 0.95 | 1.05 | 1.00 | | |
| E | 0.55 | 0.65 | 0.60 | | |
| е | - | - | 0.35 | | |
| L1 | 0.20 | 0.30 | 0.25 | | |
| L2 | 0.20 | 0.30 | 0.25 | | |
| k | ı | - | 0.40 | | |
| z | 0.02 0.08 0.05 | | | | |
| aaa | 0.15 | | | | |
| bbb | 0.05 | | | | |
| CCC | 0.05 | | | | |
| All Dimensions in mm | | | | | |

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

X2-DFN1006-3



| Dimensions | Value (in mm) | | |
|------------|---------------|--|--|
| С | 0.350 | | |
| G1 | 0.150 | | |
| G2 | 0.075 | | |
| X | 0.450 | | |
| X1 | 0.600 | | |
| X2 | 1.200 | | |
| Y | 0.200 | | |
| Y1 | 0.550 | | |



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