



#### N-CHANNEL ENHANCEMENT MODE MOSFET

### **Product Summary**

BV <sub>DSS</sub>	Rds(on)	I <sub>D</sub> Т <sub>A</sub> = +25°С
	3Ω @ V <sub>GS</sub> = 4.5V	0.3A
20V	4Ω @ V <sub>GS</sub> = 2.5V	0.26A
	6Ω @ V <sub>GS</sub> = 1.8V	0.21A

### **Features and Benefits**

- Low On-Resistance
- Very Low Gate Threshold Voltage, 1.0V Max
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- Ultra-Small Surface Mount Package
- **ESD Protected Gate**
- Totally Lead-Free & Fully RoHS compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- The DMN2991UTQ is suitable for automotive applications requiring specific change control; this part is AEC-Q101 qualified, PPAP capable, and manufactured in IATF 16949 certified facilities.

https://www.diodes.com/quality/product-definitions/

### **Description and Applications**

This new generation MOSFET is designed to minimize the on-state resistance (RDS(ON)) yet maintain superior switching performance, making it ideal for high efficiency power management applications.

- **DC-DC Converters**
- Load Switch
- Power Management Functions

#### **Mechanical Data**

- Case: SOT523
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Annealed over Alloy 42 Leadframe. Solderable per MIL-STD-202, Method 208 (63)
- Terminal Connections: See Diagram
- Weight: 0.002 grams (Approximate)

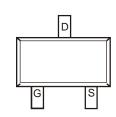




Top View



**SOT523** G **Equivalent Circuit** 



Top View

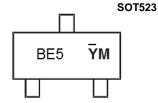
### **Ordering Information** (Note 4)

Part Number	Case	Packaging
DMN2991UTQ-7	SOT523	3000/Tape & Reel
DMN2991UTQ-13	SOT523	10000/Tape & Reel

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/

## **Marking Information**



BE5 = Product Type Marking Code YM = Date Code Marking Y = Year (ex: H = 2020)M = Month (ex: 9 = September)

Date Code Kev

Year	2020	2021	2022	2023	2024	2025	2026	2027	2028	2029	2030	2031
Code	Н		J	K	L	М	N	0	Р	R	S	T
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec



# **Maximum Ratings** (@ $T_A = +25^{\circ}C$ , unless otherwise specified.)

Cha	racteristic		Symbol	Value	Unit
Drain-Source Voltage			VDSS	20	V
Gate-Source Voltage			V <sub>GSS</sub>	±10	V
Continuous Drain Current (Note 6) V <sub>GS</sub> = 4.5V	Steady State	TA = +25°C TA = +75°C	ID	0.3 0.24	А
Maximum Continuous Body	Diode Forward	Current (Note 6)	Is	0.3	A
Pulsed Drain Current (10µs	Pulse, Duty Cy	cle = 1%)	Ідм	1.4	A

## Thermal Characteristics (@TA = +25°C, unless otherwise specified.)

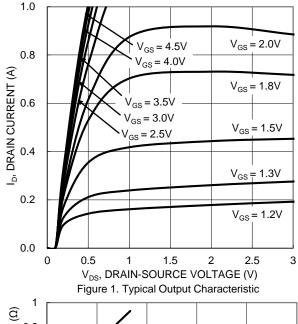
Characteristic	Symbol	Value	Unit	
Total Power Dissipation (Note 5)		PD	0.28	W
Thermal Resistance, Junction to Ambient (Note 5)	RθJA	439	°C/W	
Total Power Dissipation (Note 6)		P <sub>D</sub>	0.43	W
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	RθJA	291	°C/W
Operating and Storage Temperature Range	TJ, TSTG	-55 to +150	°C	

# **Electrical Characteristics** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)							
Drain-Source Breakdown Voltage	BVDSS	20	_	_	V	$V_{GS} = 0V, I_{D} = 250\mu A$	
Zero Gate Voltage Drain Current	IDSS	_	_	1	μA	V <sub>DS</sub> = 16V, V <sub>GS</sub> = 0V	
Gate-Source Leakage	I <sub>GSS</sub>		_	±10	μA	$V_{GS} = \pm 5V$ , $V_{DS} = 0V$	
ON CHARACTERISTICS (Note 7)							
Gate Threshold Voltage	Vgs(TH)	0.5	_	1.0	V	$V_{DS} = V_{GS}$ , $I_D = 250\mu A$	
		_	0.4	3.0		V <sub>G</sub> S = 4.5V, I <sub>D</sub> = 100mA	
Static Drain-Source On-Resistance	D		0.6	4.0	Ω	V <sub>G</sub> S = 2.5V, I <sub>D</sub> = 50mA	
Static Dialii-Source Off-Resistance	RDS(ON)		0.8	6.0	1 12	V <sub>G</sub> S = 1.8V, I <sub>D</sub> = 20mA	
			1.0	10.0		V <sub>G</sub> S = 1.5V, I <sub>D</sub> = 10mA	
Diode Forward Voltage	V <sub>SD</sub>	_	0.8	1.0	V	V <sub>GS</sub> = 0V, I <sub>S</sub> = 150mA	
DYNAMIC CHARACTERISTICS (Note 8)				•	•		
Input Capacitance	Ciss	_	21.5	_	pF	45) ) ( 6)	
Output Capacitance	Coss	_	4.9	_	pF	V <sub>DS</sub> = 15V, V <sub>GS</sub> = 0V, f = 1.0MHz	
Reverse Transfer Capacitance	Crss	_	3.7	_	pF	1 - 1.00012	
Gate Resistance	Rg	_	0.94	_	Ω	$V_{DS} = 0V$ , $V_{GS} = 0V$ , $f = 1.0MHz$	
Total Gate Charge	Qg		0.35	_	nC	V 45V V 40V	
Gate-Source Charge	Qgs		0.07	_	nC	V <sub>GS</sub> = 4.5V, V <sub>DS</sub> = 10V, I <sub>D</sub> = 250mA	
Gate-Drain Charge	Q <sub>gd</sub>		0.08	_	nC	- ID = 250IIIA	
Turn-On Delay Time	tD(ON)	_	5.6	_	ns		
Turn-On Rise Time	t <sub>R</sub>	_	4.9	_	ns	$V_{DD} = 10V, V_{GS} = 4.5V,$	
Turn-Off Delay Time	t <sub>D(OFF)</sub>	_	60.6	_	ns	$R_L = 47\Omega$ , $R_g = 10\Omega$ ,	
Turn-Off Fall Time	tF	_	27.6	_	ns	$I_D = 200 \text{mA}$	

- 5. Device mounted on FR-4 substrate PC board, with minimum recommended pad layout.
- Device mounted on FR-4 substrate PC board, 2oz copper, with thermal bias to bottom layer 1inch square copper plate.
  Short duration pulse test used to minimize self-heating effect.
  Guaranteed by design. Not subject to product testing.





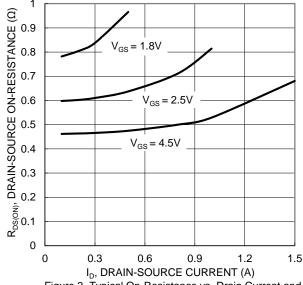


Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage

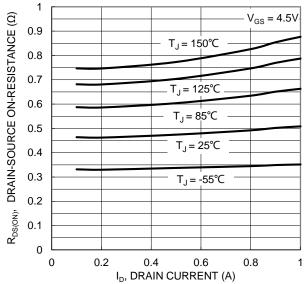


Figure 5. Typical On-Resistance vs. Drain Current and Junction Temperature

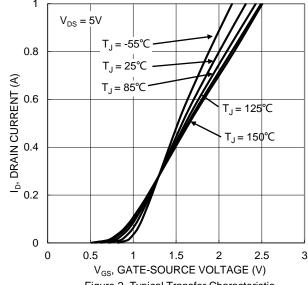
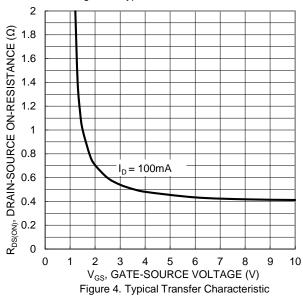


Figure 2. Typical Transfer Characteristic



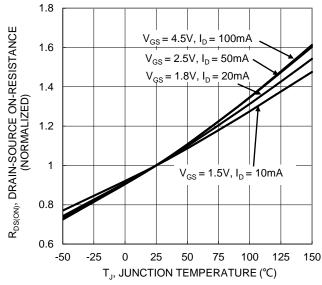
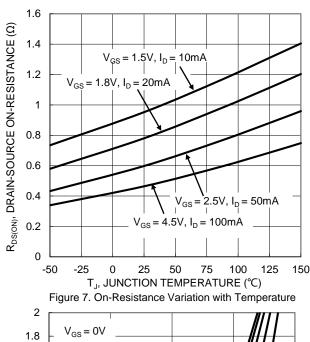
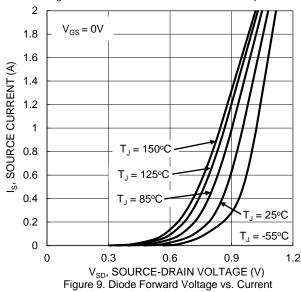


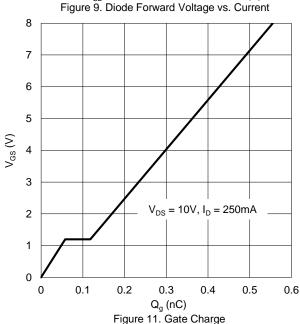
Figure 6. On-Resistance Variation with Temperature

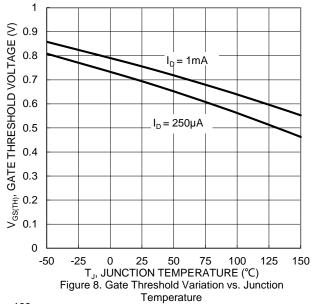


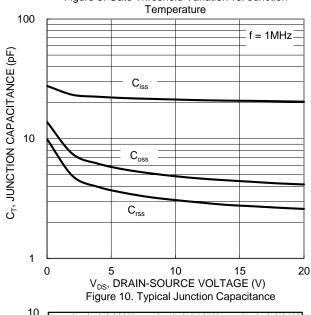


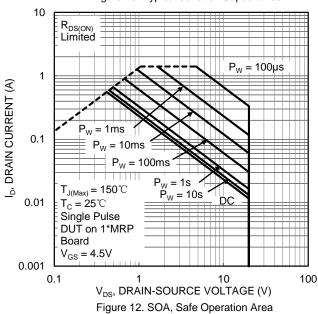














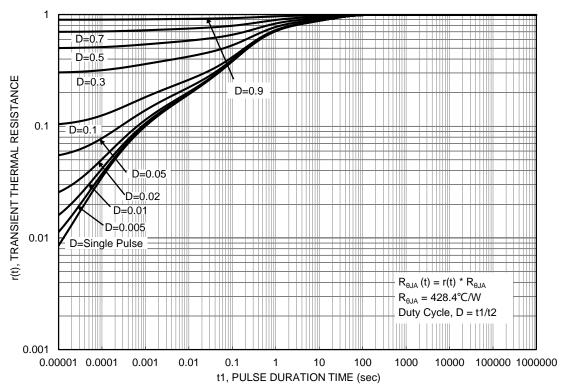


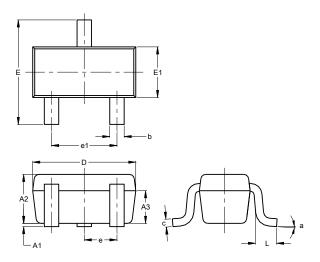
Figure 13. Transient Thermal Resistance



## **Package Outline Dimensions**

Please see http://www.diodes.com/package-outlines.html for the latest version.

### **SOT523**

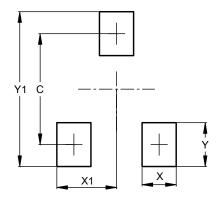


	SOT523						
Dim	Min	Max	Тур				
A1	0.00	0.10	0.05				
A2	0.60	0.80	0.75				
A3	0.45	0.65	0.50				
b	0.15	0.30	0.22				
С	0.10	0.20	0.12				
D	1.50	1.70	1.60				
Е	1.45	1.75	1.60				
E1	0.75	0.85	0.80				
е	0.50 BSC						
e1	0.90	1.10	1.00				
L	0.20	0.40	0.33				
а	0°		8°				
Al	All Dimensions in mm						

# **Suggested Pad Layout**

Please see http://www.diodes.com/package-outlines.html for the latest version.

### **SOT523**



Dimensions	Value (in mm)
С	1.29
Х	0.40
X1	0.70
Υ	0.51
Y1	1.80



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