



N-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

BV _{DSS}	R _{DS(ON)}	I _D T _A = +25°C
20V	0.45Ω @ $V_{GS} = 4.5V$	1.3A
	0.6Ω @ V _{GS} = 2.5V	1.2A

Features and Benefits

- Footprint of Just 0.6mm²—13 Times Smaller Than SOT23
- Low Gate Threshold Voltage
- Fast Switching Speed
- ESD Protected Gate
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please <u>contact us</u> or your local Diodes representative.

https://www.diodes.com/quality/product-definitions/

Description and Applications

This MOSFET has been designed to minimize the on-state resistance (RDS(ON)) yet maintain superior switching performance, which makes it ideal for high-efficiency power management applications.

Portable Electronics

Mechanical Data

- Case: X1-DFN1006-3
- Case Material: Molded Plastic, "Green" Molding Compound.
 UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish—NiPdAu Over Copper Leadframe.
 Solderable per MIL-STD-202, Method 208 @4
- Weight: 0.001 grams (Approximate)

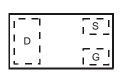


ESD PROTECTED

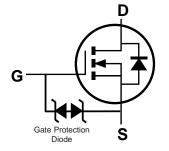


X1-DFN1006-3

Bottom View



Top View Internal Schematic



Equivalent Circuit

Ordering Information (Note 4)

Part Number	Case	Packaging
DMN2710UFB-7	X1-DFN1006-3	3,000/Tape & Reel
DMN2710UFB-7B	X1-DFN1006-3	10,000/Tape & Reel

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information



Top View Bar Denotes Gate and Source Side

BD = Part Marking Code



Maximum Ratings (@ $T_A = +25$ °C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit		
Drain-Source Voltage			VDSS	20	V
Gate-Source Voltage			V_{GSS}	±6	V
Continuous Drain Current (Note 6) V _{GS} = 4.5V	Steady State	T _A = +25°C T _A = +70°C	lo	1.3 1.1	А
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)			I _{DM}	5	Α
Maximum Body Diode Forward Current (Note 6)			Is	1.3	Α

Thermal Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 5)		PD	0.72	W
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	$R_{\theta JA}$	174	°C/W
Total Power Dissipation (Note 6)		PD	1.3	W
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	RθJA	96	°C/W
Thermal Resistance, Junction to Case (Note 6)		R _θ JC	117	°C/W
Operating and Storage Temperature Range		T _{J,} T _{STG}	-55 to +150	°C

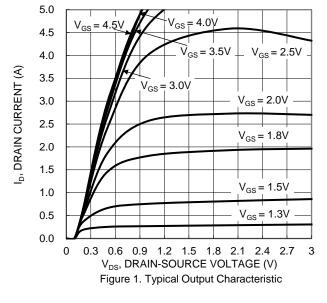
Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

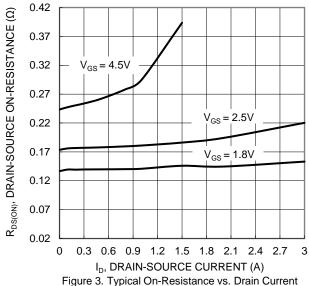
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)						
Drain-Source Breakdown Voltage		20		_	V	$V_{GS} = 0V, I_D = 250\mu A$
Zero Gate Voltage Drain Current T _J = +25°C			l	100	nA	V _{DS} = 20V, V _{GS} = 0V
Gate-Source Leakage			l	±1.0	μΑ	$V_{GS} = \pm 4.5V, V_{DS} = 0V$
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	Vgs(TH)	0.5	-	1.0	V	$V_{DS} = V_{GS}$, $I_D = 250\mu A$
		_	0.13	0.45	Ω	$V_{GS} = 4.5V, I_{D} = 600mA$
Static Drain-Source On-Resistance	RDS(ON)	_	0.17	0.6		$V_{GS} = 2.5V, I_{D} = 500mA$
			0.25	0.75		$V_{GS} = 1.8V, I_D = 350mA$
Diode Forward Voltage	VsD	_	0.7	1.2	V	V _G S = 0V, I _S = 150mA
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	Ciss	_	42	_	рF	101/11/00/
Output Capacitance	Coss	_	13	_	pF	VDS = 16V, VGS = 0V f = 1.0MHz
Reverse Transfer Capacitance	Crss		6.5	_	рF	T = T.OMHZ
Total Gate Charge	Qg	_	0.6	_	nC	V 45V V 40V
Gate-Source Charge	Qgs	_	0.1	_	nC	$V_{GS} = 4.5V, V_{DS} = 10V,$
Gate-Drain Charge	Q_{gd}	_	0.1	_	nC	$I_D = 250 \text{mA}$
Turn-On Delay Time	t _{D(ON)}		14	_	ns	\/ 40\/ \/ 45\/
Turn-On Rise Time	t _R	_	19	_	ns	V _{DD} = 10V, V _{GS} = 4.5V,
Turn-Off Delay Time	tD(OFF)	_	57	_	ns	$R_L = 47\Omega, R_g = 10\Omega$ $I_D = 200 \text{mA}$
Turn-Off Fall Time	tr	_	65	_	ns	ID = ZUUITIA
Reverse Recovery Time	trr		88	_	ns	$I_F = 1A$, $di/dt = 100A/\mu s$
Reverse Recovery Charge	Q _{RR}		29	_	nC	I _F = 1A, di/dt = 100A/μs

Notes:

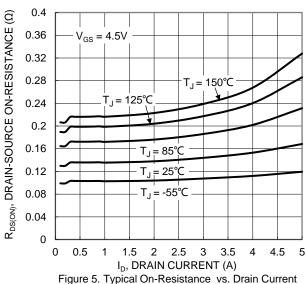
- 5. Device mounted on FR-4 PC board, with minimum recommended pad layout, single sided.
- 6. Device mounted on FR-4 substrate PC board, 2oz copper, with thermal vias to bottom layer 1-inch square copper plate.
- 7. Short duration pulse test used to minimize self-heating effect.
 8. Guaranteed by design. Not subject to production testing.







and Gate Voltage



and Junction Temperature

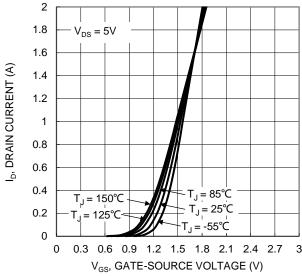
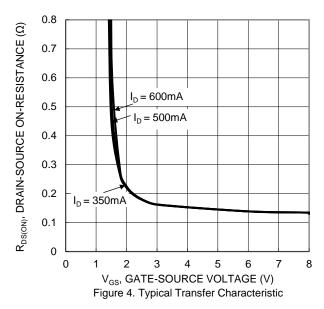


Figure 2. Typical Transfer Characteristic



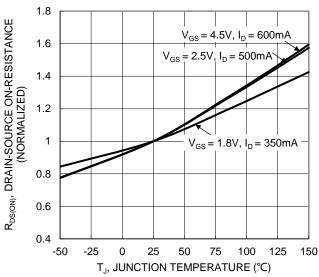


Figure 6. On-Resistance Variation with Temperature



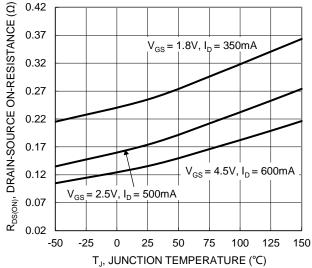
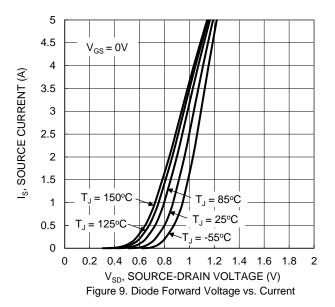
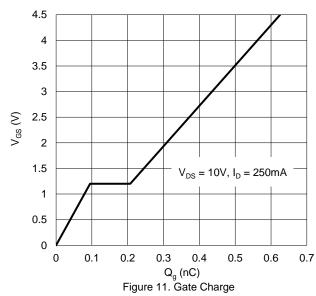


Figure 7. On-Resistance Variation with Temperature





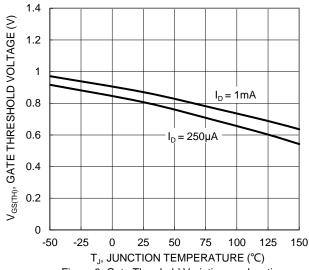
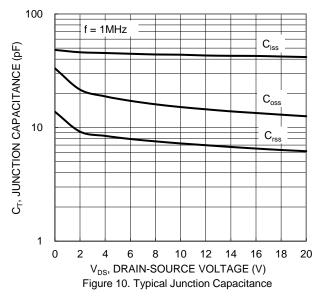
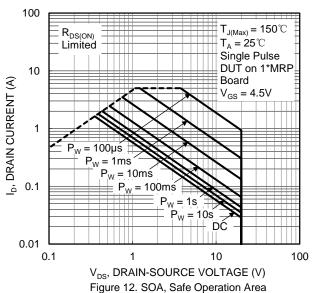
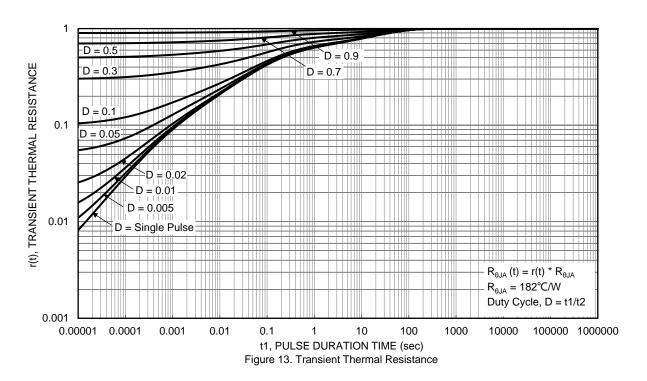


Figure 8. Gate Threshold Variation vs. Junction Temperature







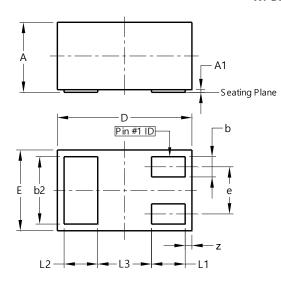




Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

X1-DFN1006-3

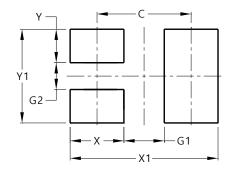


X1-DFN1006-3					
Dim	Min	Max	Тур		
Α	0.47	0.53	0.50		
A1	0.00	0.05	0.03		
b	0.10	0.20	0.15		
b2	0.45	0.55	0.50		
D	0.95	1.075	1.00		
Е	0.55	0.675	0.60		
е	-	-	0.35		
L1	0.20	0.30	0.25		
L2	0.20	0.30	0.25		
L3		-	0.40		
Z	0.02	0.08	0.05		
All Dimensions in mm					

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

X1-DFN1006-3



Dimensions	Value (in mm)			
С	0.70			
G1	0.30			
G2	0.20			
Х	0.40			
X1	1.10			
Y	0.25			
V1	0.70			



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