



N-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

BV _{DSS}	R _{DS(ON)} Max	I _D Max T _A = +25°C
	46mΩ @ Vgs = 4.5V	3.1A
20V	53mΩ @ V _{GS} = 2.5V	2.8A

Features and Benefits

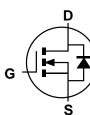
- Low On-Resistance
- Low Input Capacitance
- Fast Switching Speed
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e. parts gualified to AEC-Q100/101/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please contact us or your local Diodes representative. https://www.diodes.com/guality/product-definitions/

Description and Applications

This MOSFET is designed to minimize the on-state resistance (RDS(ON)) yet maintain superior switching performance, making it ideal for high efficiency power management applications.

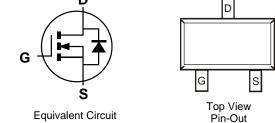
- General Purpose Interfacing Switch
- **Power Management Functions**
- **DC-DC Converters**
- Analog Switch





Mechanical Data

- Case: SOT323
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections Indicator: See Diagram
- Terminals: Finish Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 @3
- Weight: 0.027 grams (Approximate)



Ordering Information (Note 4)

Part Number	Case	Packaging
DMN2055UW-7	SOT323	3,000/Tape & Reel
DMN2055UW-13	SOT323	10,000/Tape & Reel

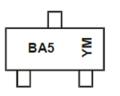
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. Notes:

2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information



BA5 = Product Type Marking Code YM or $\overline{Y}M$ = Date Code Marking Y or \overline{Y} = Year (ex: H = 2020) M = Month (ex: 9 = September)

Date Code Kev

Date Code Key												
Year	2019	2020	2021	2022	2023	2024	2025	2026	2027	2028	2029	2030
Code	G	Н		J	K	L	М	N	0	Р	R	S
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D



Maximum Ratings (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit		
Drain-Source Voltage	VDSS	V			
Gate-Source Voltage	V _{GSS}	±8	V		
	Steady	T _A = +25°C		3.1	
Continuous Drain Current (Note 6) $V_{GS} = 4.5V$	State	T _A = +70°C	ID	2.4	А
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%	Ідм	22	A		
Maximum Body Diode Forward Current (Note 5)	Is	0.8	A		

Thermal Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	
Total Power Dissipation (Note 5)		PD	0.52	W
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	RθJA	241	°C/W
Total Power Dissipation (Note 6)	·	PD	0.65	W
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	R _{θJA}	191	°C/W
Operating and Storage Temperature Range		TJ, TSTG	-55 to +150	°C

Electrical Characteristics (@TA = +25°C, unless otherwise specified.)

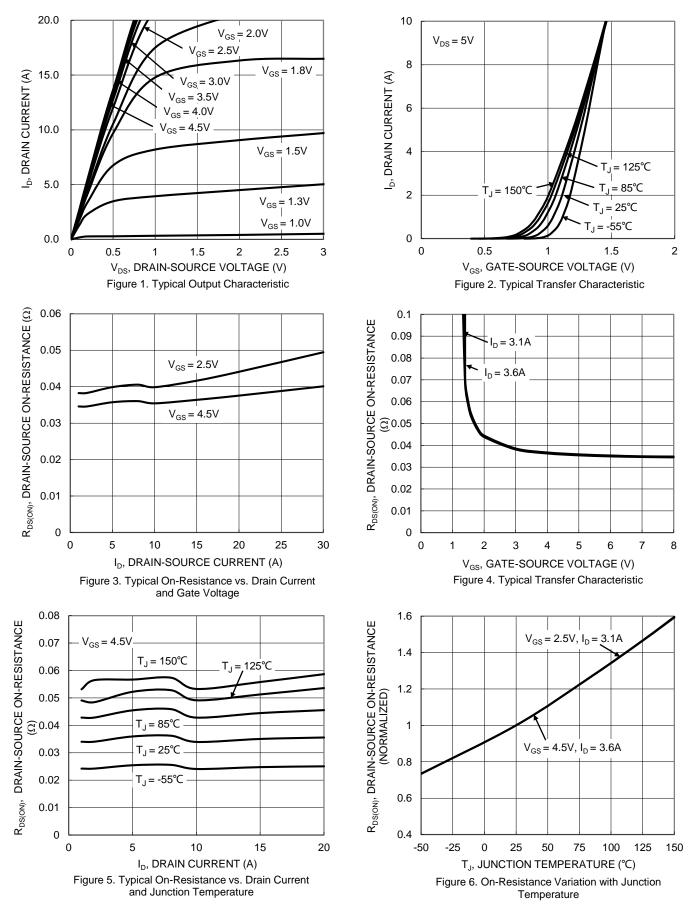
			-			T (0)
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)	1					
Drain-Source Breakdown Voltage	BVDSS	20	—	—	V	$V_{GS} = 0V, I_{D} = 250 \mu A$
Zero Gate Voltage Drain Current $@T_C = +25^{\circ}C$	IDSS			1.0	μA	$V_{DS} = 20V, V_{GS} = 0V$
Gate-Source Leakage	lgss	—	—	±100	nA	$V_{GS} = \pm 8V, V_{DS} = 0V$
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	VGS(TH)	0.4	—	1.0	V	$V_{DS} = V_{GS}, I_D = 250 \mu A$
Olatia Davia On Daviatana	-	_	35	46		V _{GS} = 4.5V, I _D = 3.6A
Static Drain-Source On-Resistance	RDS(ON)	_	39	53	mΩ	$V_{GS} = 2.5V, I_D = 3.1A$
Diode Forward Voltage	Vsd	—	0.7	1	V	$V_{GS} = 0V$, $I_{S} = 1A$
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance		—	400	—	pF	
Output Capacitance		—	55	—	pF	$V_{DS} = 10V, V_{GS} = 0V,$ f = 1.0MHz
Reverse Transfer Capacitance	Crss	—	37	—	pF	
Gate Resistance	Rg		3.7	—	Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1MHz$
Total Gate Charge		—	4.3	_	nC	
Gate-Source Charge		—	0.3	_	nC	$V_{GS} = 4.5V, V_{DS} = 10V,$ $I_D = 6A$
Gate-Drain Charge	Qgs Qgd	—	4.8	_	nC	ID = 6A
Turn-On Delay Time	t _{D(ON)}	_	2.8	_	ns	
Turn-On Rise Time	t _R		2.7	_	ns	$V_{DD} = 10V, V_{GS} = 5V,$
Turn-Off Delay Time	tD(OFF)	_	15.4	_	ns	$R_L = 1.7\Omega, R_g = 6\Omega$
Turn-Off Fall Time	tF	—	4.4	—	ns	

Notes:

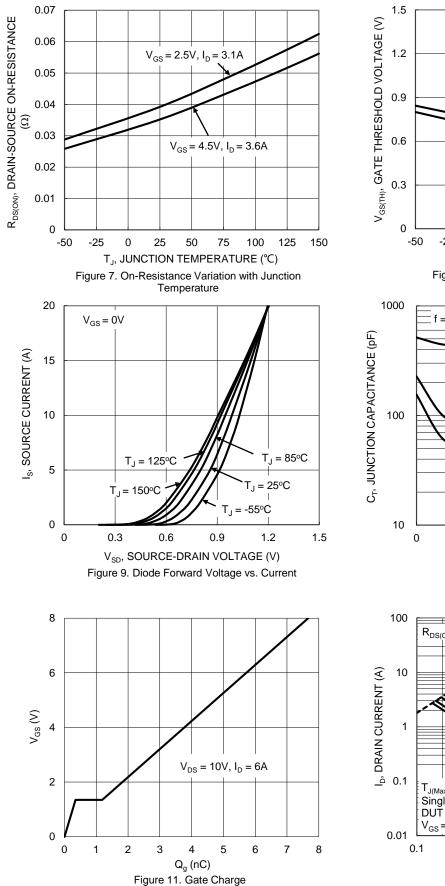
5. Device mounted on FR-4 substrate PC board, with minimum recommended pad layout.
6. Device mounted on FR-4 substrate PC board, 2oz copper, with thermal bias to bottom layer 1inch square copper plate.
7. Short duration pulse test used to minimize self-heating effect.
8. Guaranteed by design. Not subject to product testing.

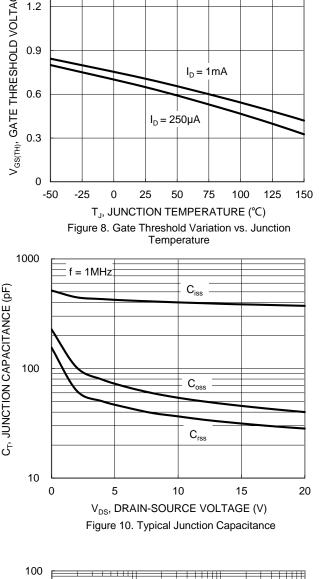


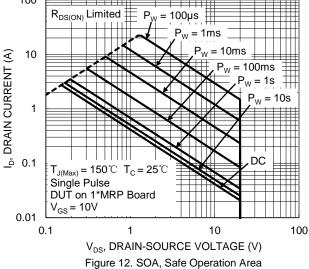
DMN2055UW



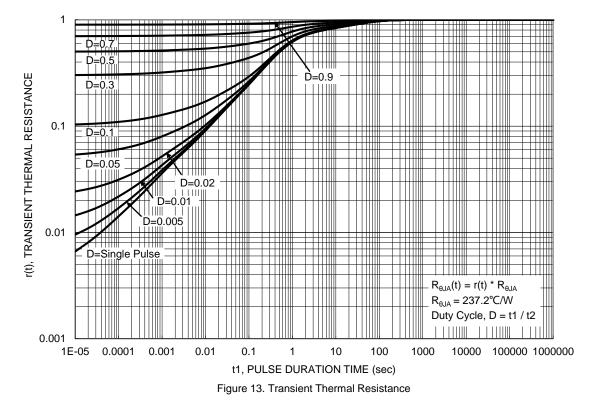










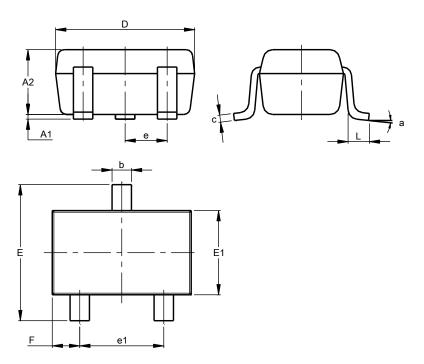




Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

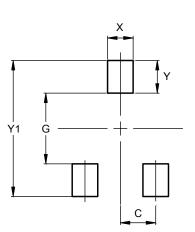




SOT323								
Dim	Min	Max	Тур					
A1	0.00	0.10	0.05					
A2	0.90	1.00	0.95					
b	0.25	0.40	0.30					
C	0.10	0.18	0.11					
D	1.80	2.20	2.15					
Е	2.00	2.20	2.10					
E1	1.15	1.35	1.30					
е	C).650 B	SC					
e1	1.20	1.40	1.30					
F	0.375	0.475	0.425					
L	0.25	0.40	0.30					
а	0°	8°						
All	Dimen	All Dimensions in mm						

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.



Dimensions	Value (in mm)
С	0.650
G	1.300
Х	0.470
Y	0.600
Y1	2.500

SOT323



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