



DMN2012UCA6

N-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

BVsss	Rss(on) Typ	Is мах Та = +25°С
24V	$7.4m\Omega @ V_{GS} = 3.8V$	13.0A

Description

This new generation MOSFET is designed to minimize the on-state resistance (Rss(ON)) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

Applications

- Battery Management
- Load Switch
- Battery Protection

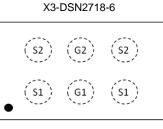
Features

- CSP with Footprint 2.70mm × 1.81mm
- Height = 0.21mm for Low Profile
- ESD Protection of Gate
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please <u>contact us</u> or your local Diodes representative. <u>https://www.diodes.com/quality/product-definitions/</u>

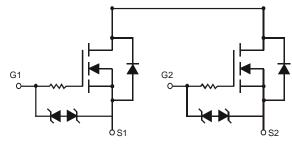
Mechanical Data

- Case: X3-DSN2718-6
- Terminal Connections: See Diagram Below
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish NiPdAu or NiAu. Solderable per MIL-STD-202, Method 208 ⁴
- Weight: 0.0026 grams (Approximate)





Top View



Equivalent Circuit

Ordering Information (Note 4)

Part Number	Case	Packaging
DMN2012UCA6-7	X3-DSN2718-6	3000/Tape & Reel

Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.

 See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information

M3	
ΥM	

 $\begin{array}{l} M3 = \mbox{Product Type Marking Code} \\ YM = \mbox{Date Code Marking} \\ Y \mbox{ or } \overline{Y} = \mbox{Year (ex: } G = 2019) \\ M \mbox{ or } \overline{M} = \mbox{Month (ex: } 9 = \mbox{September)} \end{array}$

Date Code Key

Year	2018	2019	20	020	2021	2022	2	2023	2024	202	25	2026
Code	F	G		H		J		K	L	N	1	Ν
Month	Jan	Feb	Mar	Apr	Мау	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	4	0	2	4	5	e	7	0	0	\circ	N	Р



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characterist	Symbol	Value	Unit			
Source-Source Voltage	V _{SSS}	24	V			
Gate-Source Voltage			Vgss	±12	V	
	Steady State $\frac{T_A = +25^{\circ}C}{T_A = +70^{\circ}C}$ Is	T _A = +25°C		13.0	٨	
Continuous Source Current (Note 5) $V_{GS} = 4.5V$		10.4	A			
	Stoody State	T _A = +25°C		10.8	A	
Continuous Source Current (Note 5) $V_{GS} = 2.5V$	Steady State	T _A = +70°C	IS	8.6		
Pulsed Source Current (Note 6)	•	I _{SM}	60	А		

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 7)	PD	0.82	W
Thermal Resistance, Junction to Ambient $@T_A = +25^{\circ}C$ (Note 7)	Reja	151.9	°C/W
Power Dissipation (Note 5)	PD	2.3	W
Thermal Resistance, Junction to Ambient $@T_A = +25^{\circ}C$ (Note 5)	Reja	55.0	°C/W
Operating and Storage Temperature Range	TJ, TSTG	-55 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 8)						1	
Source-Source Breakdown Voltage	BVsss	24	_		V	$V_{GS} = 0V$, $I_{S} = 1mA$	
Zero Gate Voltage Source Current T _J = +25°C	I _{SSS}		_	1	μA	$V_{SS} = 20V, V_{GS} = 0V$	
Gate-Source Leakage	lgss	—	—	±10	μA	$V_{GS} = \pm 12V$, $V_{SS} = 0V$	
ON CHARACTERISTICS (Note 8)							
Gate Threshold Voltage	Vgs(th)	0.5	0.9	1.3	V	$V_{SS} = 10V$, $I_{S} = 1mA$	
			7.0	9.0		VGS = 4.5V, IS = 5A	
			7.3	9.5		$V_{GS} = 4.0V, I_{S} = 5A$	
Static Source-Source On-Resistance	Rss(ON)		7.4	10.1	mΩ	V _{GS} = 3.8V, I _S = 5A V _{GS} = 3.1V, I _S = 5A	
			7.9	10.3			
			9.1	13.0		$V_{GS} = 2.5V, I_S = 5A$	
Diode Forward Voltage	Vss	—	0.7	_	V	VGS = 0V, IS = 10A	
DYNAMIC CHARACTERISTICS (Note 9)						·	
Input Capacitance	Ciss	_	2417	—			
Output Capacitance	Coss		270	_	pF	$V_{SS} = 10V, V_{GS} = 0V,$ f = 1.0MHz	
Reverse Transfer Capacitance	Crss	_	102				
Total Gate Charge	Qg	—	26.0	_			
Gate-Source Charge	Q _{gs}	—	5.2	—	nC	$V_{SS} = 19.2V, V_{GS} = 4.0V,$	
Gate-Drain Charge	Q _{gd}	—	9.5	—	nC	Is = 3A	
Gate Charge at VTH	Q _{g(TH)}		4.5		1		
Turn-On Delay Time	tD(ON)	—	543	—			
Turn-On Rise Time	tR		1183			Vss = 20V, Vgs = 4.0V,	
Turn-Off Delay Time	t _{D(OFF)}	—	1810	—	ns	$I_{S} = 10A, R_{g} = 6.0\Omega$	
Turn-Off Fall Time	tF	—	1602	—	1	-	

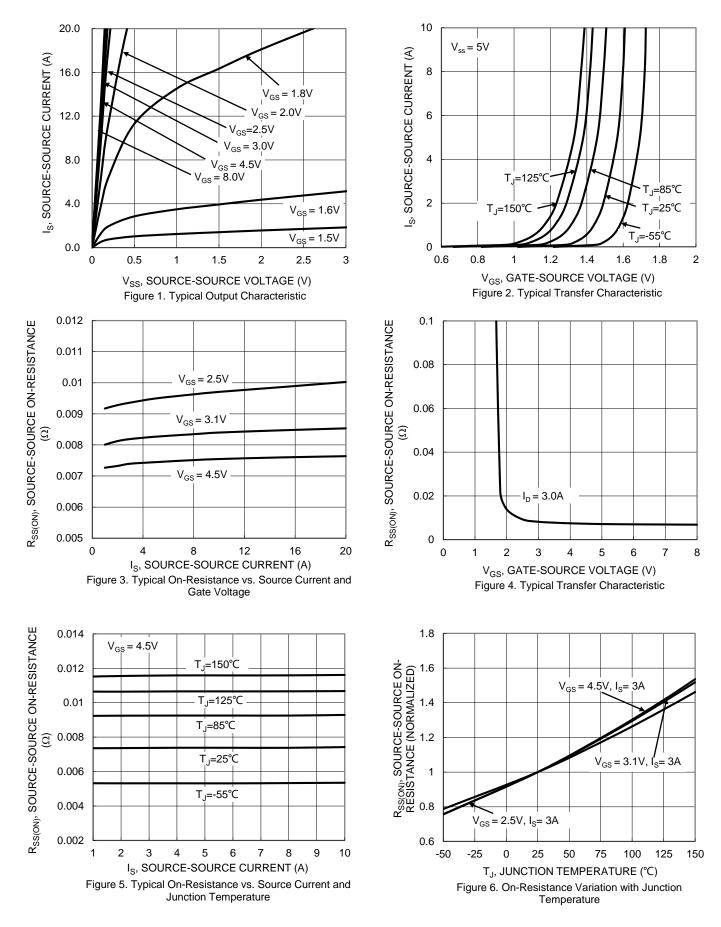
Notes: 5. Device mounted on FR-4 material with 1inch² (6.45cm²), 2oz. (0.071mm thick) Cu.

Repetitive rating, pulse width limited by junction temperature.
Device mounted on FR-4 PCB with minimum recommended pad layout, single sided.

Short duration pulse test used to minimize self-heating effect.
Guaranteed by design. Not subject to production testing.

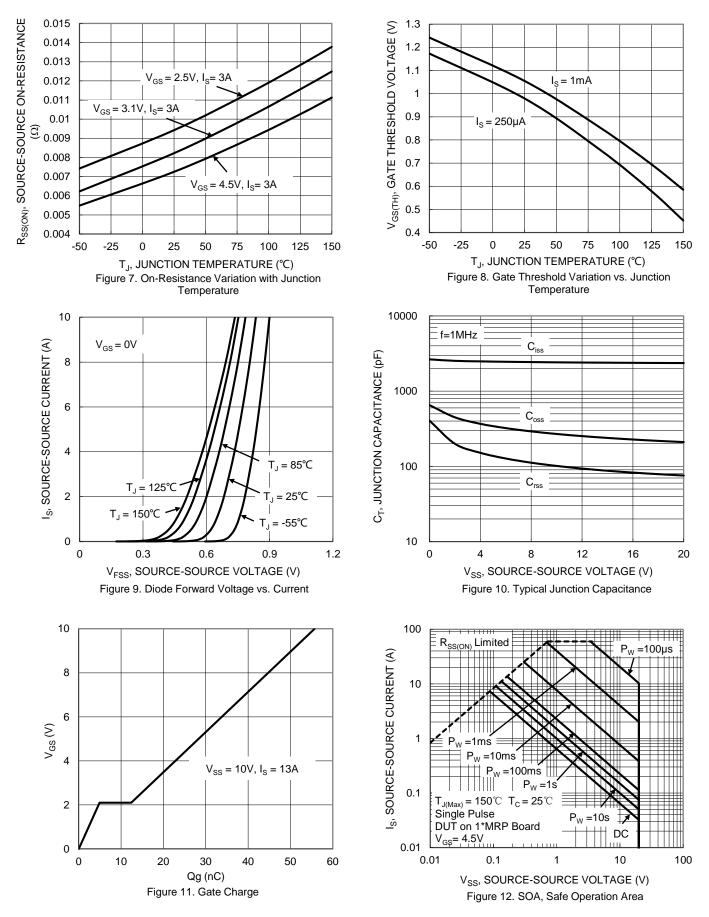


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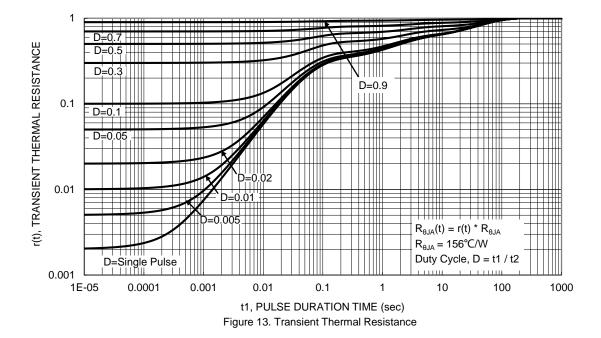
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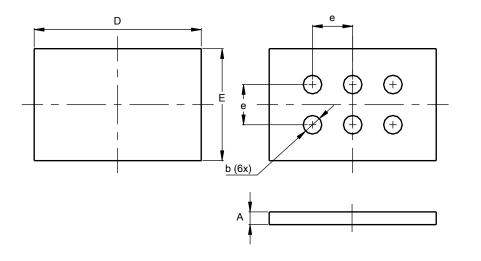




Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

X3-DSN2718-6

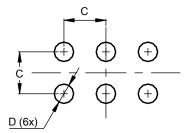


	X3-DSN2718-6						
Dim	Min	Max	Тур				
Α	0.16	0.26	0.21				
b	0.27	0.33	0.30				
D	2.65	2.75	2.70				
E	1.76	1.86	1.81				
е	0.62	0.68	0.65				
All	All Dimensions in mm						

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.





Dimensions	Value (in mm)
С	0.65
D	0.30



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