



N-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

BV _{SSS}	R _{SS(ON)} Typ	Is _{Max} T _A = +25°C
12V	4.8mΩ @ V _{GS} = 3.8V	17A

Description

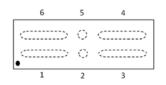
This new generation MOSFET is designed to minimize the on-state resistance (Rss(on)) yet maintain superior switching performance, making it ideal for high-efficiency power management applications.

Applications

- Battery Management
- Load Switch
- Battery Protection

X4-DSN2112-6





- 1. Source 1
- 2. Gate 1
- 3. Source 1
- 4. Source 2
- 5. Gate 2
- 6. Source 2

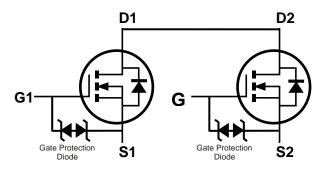
Features

- CSP with Footprint 2.11mm x 1.18mm
- Height = 0.11mm for Low Profile
- ESD Protection of Gate
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please contact us or your local Diodes representative.

 https://www.diodes.com/quality/product-definitions/

Mechanical Data

- Case: X4-DSN2112-6
- Terminal Connections: See Diagram Below
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish NiPdAu or NiAu. Solderable per MIL-STD-202, Method 208 4
- Weight: 0.0012 grams (Approximate)



Equivalent Circuit

Ordering Information (Note 4)

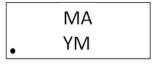
Part Number	Case	Packaging
DMN16M0UCA6-7	X4-DSN2112-6	3000/Tape & Reel

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/

Top View

Marking Information



MA = Product Type Marking Code YM = Date Code Marking Y or \overline{Y} = Year (ex: G = 2019) M or \overline{M} = Month (ex: 9 = September)

Date Code Key

Year	2018	2019	20	020	2021	2022	2	2023	2024	202	25	2026
Code	F	G		Н	I	J		K	L	N	1	N
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D



Maximum Ratings (@ $T_A = +25^{\circ}C$, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Source-Source Voltage	Vsss	12	V
Gate-Source Voltage	Vgss	±8	V
Continuous Source Current (Note 5) V _{GS} = 4.5V	Is	17 13.5	Α
Continuous Source Current (Note 5) V _{GS} = 2.5V	Is	12.5 10	Α
Pulsed Source Current (Note 6)	Ism	110	Α

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 7)	P _D	0.9	W
Thermal Resistance, Junction to Ambient @T _A = +25°C (Note 7)	R _{OJA}	135	°C/W
Power Dissipation (Note 5)	PD	2.6	W
Thermal Resistance, Junction to Ambient @T _A = +25°C (Note 5)	R _{OJA}	48	°C/W
Operating and Storage Temperature Range	TJ, TSTG	-55 to +150	°C

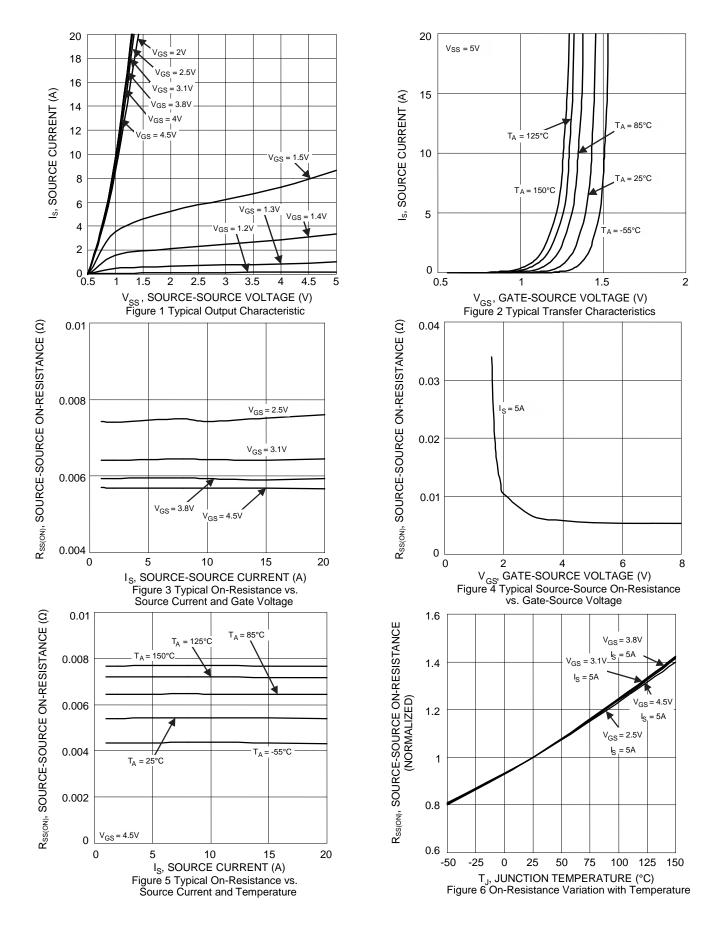
Electrical Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition			
OFF CHARACTERISTICS (Note 8)									
Source-Source Breakdown Voltage	BVsss	12	_	_	V	$V_{GS} = 0V$, $I_{S} = 1mA$			
Zero Gate Voltage Source Current T _J = +25°C	Isss	_	l	1	μA	Vss = 10V, Vgs = 0V			
Gate-Source Leakage	I _{GSS}	_	l	±10	μΑ	$V_{GS} = \pm 8V$, $V_{SS} = 0V$			
ON CHARACTERISTICS (Note 8)	ON CHARACTERISTICS (Note 8)								
Gate Threshold Voltage	V _{GS(TH)}	0.4	0.8	1.3	V	$V_{SS} = 6V$, $I_S = 1mA$			
		3.1	4.6	5.9	mΩ	$V_{GS} = 4.5V$, $I_{S} = 5A$			
Static Source-Source On-Resistance	Rss(on)	3.5	4.8	6.5		$V_{GS} = 3.8V$, $I_{S} = 5A$			
Static Source On-Nesistance		4.0	5.2	8.2		$V_{GS} = 3.1V$, $I_{S} = 5A$			
		5.2	6.1	11.0		Vgs = 2.5V, Is = 5A			
Diode Forward Voltage	Vss	_	0.7	_	V	$V_{GS} = 0V$, $I_{S} = 3A$			
DYNAMIC CHARACTERISTICS (Note 9)									
Total Gate Charge	Q_g	_	24		nC	Vss = 6V, Vgs = 4.5V, Is = 15A			
Turn-On Delay Time	t _D (ON)	_	123						
Turn-On Rise Time	t _R	_	207	-		$V_{SS} = 6V, V_{GS} = 4.5V,$			
Turn-Off Delay Time	tD(OFF)	_	547	_	ns	Is = 3A			
Turn-Off Fall Time	t _F	_	762	_					

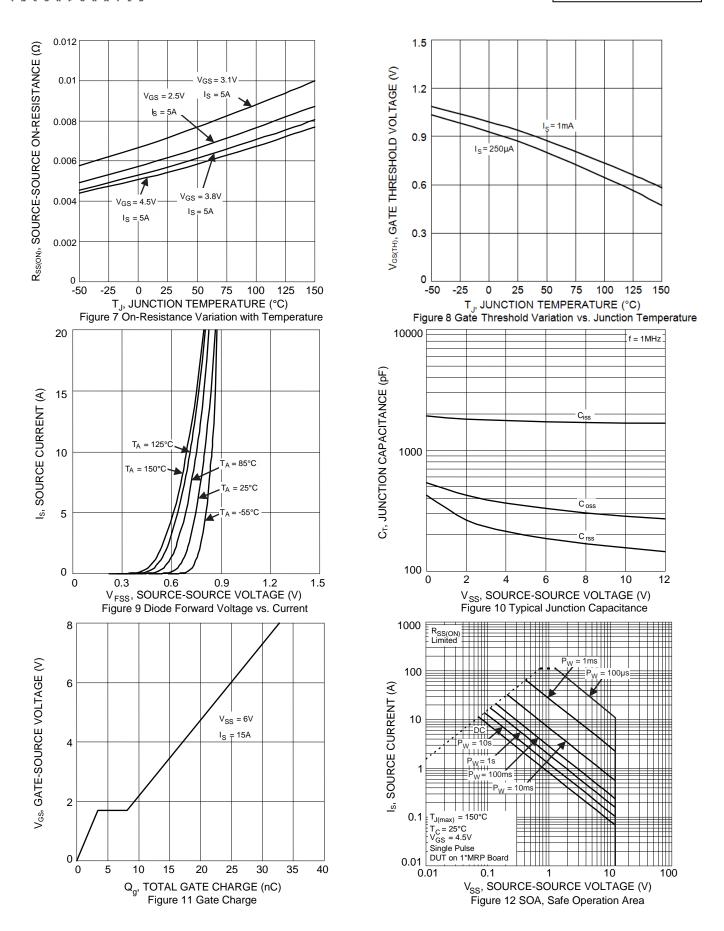
Notes:

- 5. Device mounted on FR-4 material with 1inch² (6.45cm²), 2oz. (0.071mm thick) Cu.
- 6. Repetitive rating, pulse width limited by junction temperature.
- 7. Device mounted on FR-4 PCB with minimum recommended pad layout, single sided.
 8. Short duration pulse test used to minimize self-heating effect.
- 9. Guaranteed by design. Not subject to production testing.

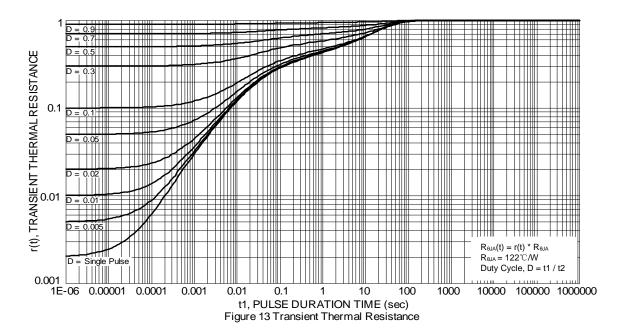










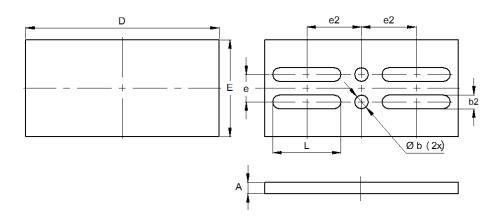




Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

X4-DSN2112-6

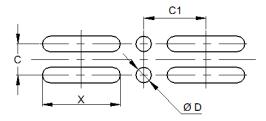


	V4 D01	10440.0						
X4-DSN2112-6								
Dim	Min	Max	Тур					
Α	0.09	0.16	0.11					
b	0.22	0.28	0.25					
b2	0.27	0.33	0.30					
D	2.06	2.17	2.11					
Е	1.13	1.24	1.18					
е			0.55					
e2			0.678					
L	0.575	0.635	0.600					
All Dimensions in mm								

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

X4-DSN2112-6



Dimensions	Value			
פווטופוושוווע	(in mm)			
С	0.55			
C1	0.678			
D	0.25			
Х	0.600			



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