



DMN15H310SK3

#### **Product Summary**

V <sub>(BR)DSS</sub>	R <sub>DS(ON)</sub> max	I <sub>D</sub> T <sub>C</sub> = +25°C
150V	310mΩ @ V <sub>GS</sub> = 10V	8.3A
	330mΩ @ V <sub>GS</sub> = 5.0V	8.0A

#### Description

This new generation MOSFET features low on-resistance and fast switching, making it ideal for high-efficiency power management applications.

# Applications

- Power Management Functions
- DC-DC Converters

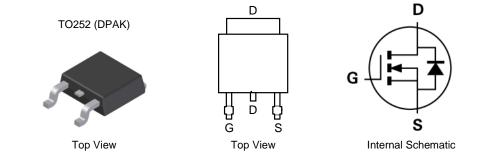
#### **150V N-CHANNEL ENHANCEMENT MODE MOSFET**

#### **Features**

- Low R<sub>DS(ON)</sub> ensures on state losses are minimized
- Small form factor thermally efficient package enables higher density end products
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

#### **Mechanical Data**

- Case: TO252 (DPAK)
- Case Material: Molded Plastic, "Green" Molding Compound; UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Diagram
- Terminals: Finish Matte Tin Annealed over Copper Leadframe Solderable per MIL-STD-202, Method 208 (3)
- Weight: 0.33 grams (Approximate)



#### Ordering Information (Note 4)

Part Number	Case	Packaging
DMN15H310SK3-13	TO252 (DPAK)	2,500/Tape & Reel

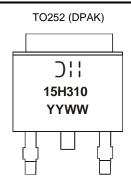
Notes: 1. EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant. All applicable RoHS exemptions applied.

 See http://www.diodes.com/quality/lead\_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

### **Marking Information**



) | | =Manufacturer's Marking 15H310= Product Type Marking Code YYWW = Date Code Marking YY = Last Digit of Year (ex: 15 = 2015) WW = Week Code (01 to 53)



#### Maximum Ratings (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	
Drain-Source Voltage	V <sub>DSS</sub>	150	V	
Gate-Source Voltage	V <sub>GSS</sub>	±20	V	
Continuous Drain Current, V <sub>GS</sub> = 10V	$T_{C} = +25^{\circ}C$ $T_{C} = +100^{\circ}C$	Ι <sub>D</sub>	8.3 5.2	A
Pulsed Drain Current (380µs Pulse, Duty Cycle = 1%)		I <sub>DM</sub>	10	А
Maximum Body Diode Continuous Current (note 5)		ls	2.6	A
Avalanche Current, L = 3mH (Note 6)		I <sub>AS</sub>	0.5	A
Avalanche Energy, L = 3mH (Note 6)	E <sub>AS</sub>	0.36	mJ	

#### Thermal Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit	
Total Bower Dissinction	$\frac{T_{C} = +25^{\circ}C}{T_{C} = +100^{\circ}C} P_{D}$		32	10/	
Total Power Dissipation			12	W	
Thermal Resistance, Junction to Ambient (Note 5)		R <sub>0JA</sub>	49	°CW	
Thermal Resistance, Junction to Case		R <sub>0JC</sub>	3.9	-C/VV	
Operating and Storage Temperature Range	TJ, TSTG	-55 to +150	°C		

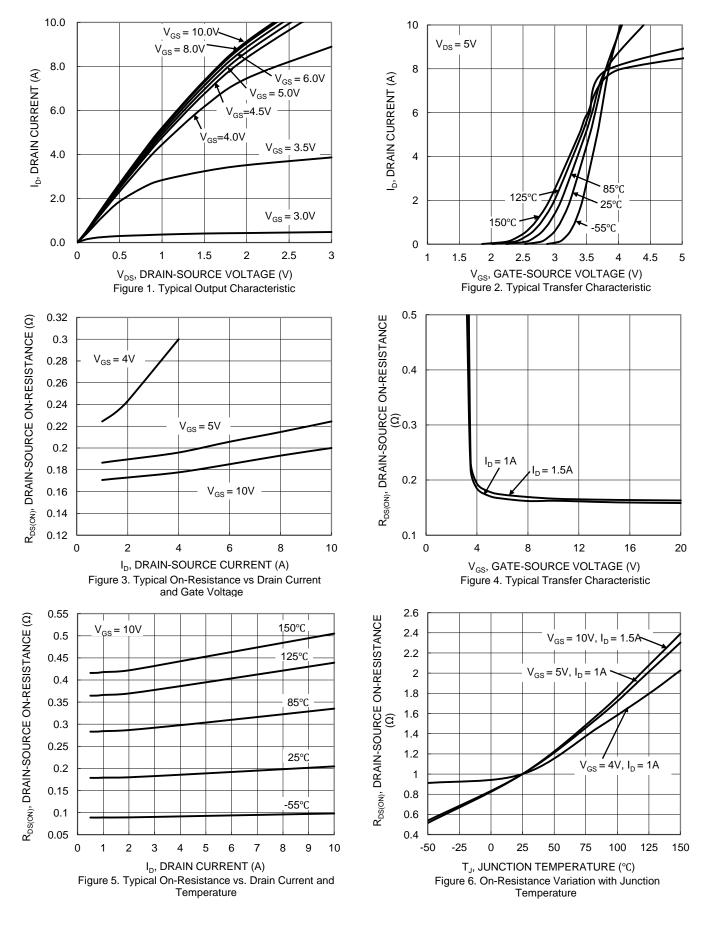
# Electrical Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)							
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	150	—		V	$V_{GS} = 0V, I_D = 250 \mu A$	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>		_	1	μA	$V_{DS} = 120V, V_{GS} = 0V$	
Gate-Source Leakage	Igss		_	±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 7)						-	
Gate Threshold Voltage	V <sub>GS(TH)</sub>	1	2.6	3	V	$V_{DS} = V_{GS}$ , $I_D = 250 \mu A$	
		_	180	310		V <sub>GS</sub> = 10V, I <sub>D</sub> = 1.5A	
Static Drain-Source On-Resistance	R <sub>DS(ON)</sub>	_	195 330		mΩ	$V_{GS} = 5.0V, I_D = 1.0A$	
			242	350		$V_{GS} = 4.0V, I_D = 1.0A$	
Diode Forward Voltage	V <sub>SD</sub>		0.8	1.2	V	$V_{GS} = 0V, I_{S} = 1.7A$	
DYNAMIC CHARACTERISTICS (Note 6)	•		•	•	•	•	
Input Capacitance	C <sub>iss</sub>	_	405	_		$V_{DS} = 25V, V_{GS} = 0V$ f = 1.0MHz	
Output Capacitance	C <sub>oss</sub>		40	_	pF		
Reverse Transfer Capacitance	C <sub>rss</sub>		20				
Gate Resistance	R <sub>G</sub>	_	2.88		Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1.0MHz$	
Total Gate Charge (V <sub>GS</sub> = 5.0V)	Qg		4.6			V <sub>DS</sub> = 80V, I <sub>D</sub> = 7.3A	
Total Gate Charge (V <sub>GS</sub> = 10V)	Qg		8.7		nC		
Gate-Source Charge	Q <sub>gs</sub>		1.7		nc		
Gate-Drain Charge	Q <sub>gd</sub>		1.8	_			
Turn-On Delay Time	t <sub>D(ON)</sub>		3.5			V <sub>DD</sub> = 50V, V <sub>GS</sub> = 10V,	
Turn-On Rise Time	t <sub>R</sub>		7.8	—			
Turn-Off Delay Time	t <sub>D(OFF)</sub>		22	—	ns	$R_{G} = 25\Omega, I_{D} = 7.3A$	
Turn-Off Fall Time	tF		11	_			
Reverse Recovery Time	t <sub>RR</sub>	_	38	_	ns	I <sub>F</sub> = 7.3A, di/dt = 100A/µs	
Reverse Recovery Charge	Q <sub>RR</sub>	_	53	_	nC	I <sub>F</sub> = 7.3A, di/dt = 100A/µs	

 Device mounted on FR-4 substrate PC board, 2oz copper, with 1-inch square copper plate.
Guaranteed by design. Not subject to product testing.
Short duration pulse test used to minimize self-heating effect. Notes:

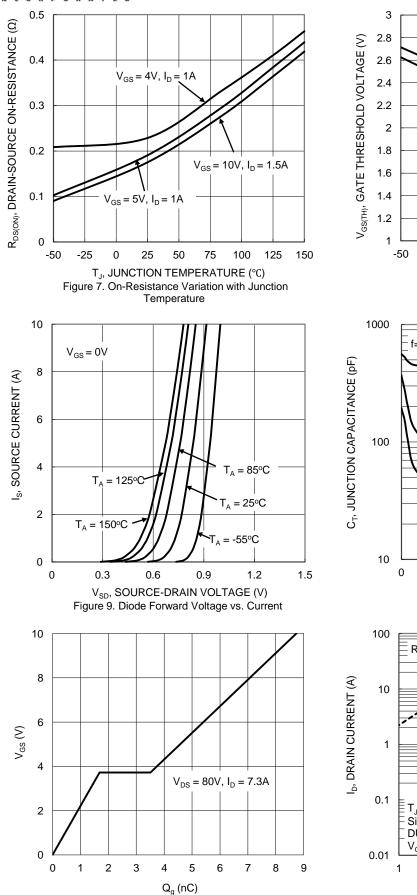


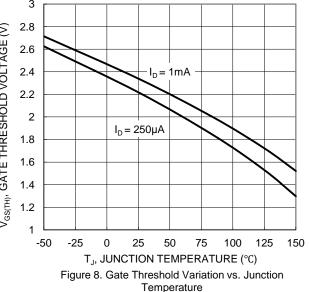
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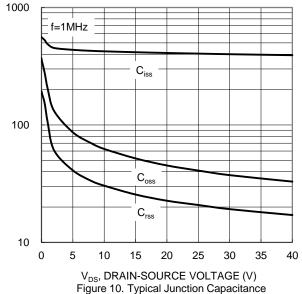




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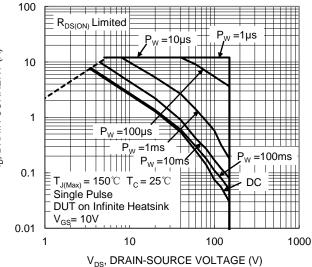
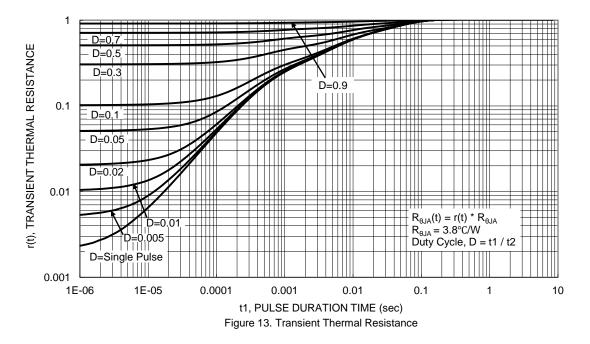


Figure 12. SOA, Safe Operation Area

Figure 11. Gate Charge

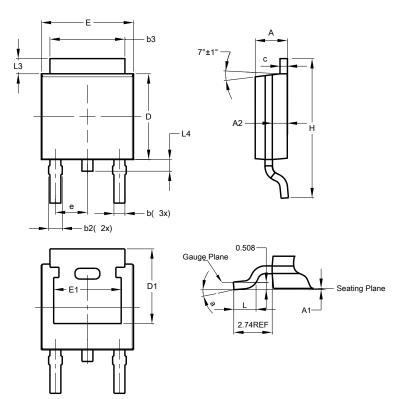






# **Package Outline Dimensions**

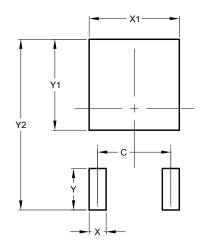
Please see AP02001 at http://www.diodes.com/\_files/datasheets/ap02001.pdf for the latest version.



TO252 (DPAK)						
Dim	Min	Max	Тур			
Α	2.19	2.39	2.29			
A1	0.00	0.13	0.08			
A2	0.97	1.17	1.07			
b	0.64	0.88	0.783			
b2	0.76	1.14	0.95			
b3	5.21	5.46	5.33			
c	0.45	0.58	0.531			
D	6.00	6.20	6.10			
D1	5.21	-	-			
е	-	-	2.286			
Е	6.45	6.70	6.58			
E1	4.32	-	-			
Н	9.40	10.41	9.91			
L	1.40	1.78	1.59			
L3	0.88	1.27	1.08			
L4	0.64	1.02	0.83			
а	0°	10°	-			
All Dimensions in mm						

# Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/\_files/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)		
С	4.572		
Х	1.060		
X1	5.632		
Y	2.600		
Y1	5.700		
Y2	10.700		



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