

## Product Summary

BV <sub>DSS</sub>	R <sub>DS(ON)</sub>	I <sub>D</sub> T <sub>A</sub> = +25°C
-20V	700mΩ @ V <sub>GS</sub> = -4.5V	-460mA
	900mΩ @ V <sub>GS</sub> = -2.5V	-420mA
	1300mΩ @ V <sub>GS</sub> = -1.8V	-350mA

## Description and Applications

This new generation MOSFET is designed to minimize the on-state resistance (R<sub>DS(ON)</sub>) yet maintain superior switching performance, making it ideal for high-efficiency power-management applications.

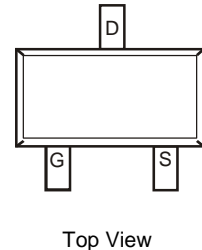
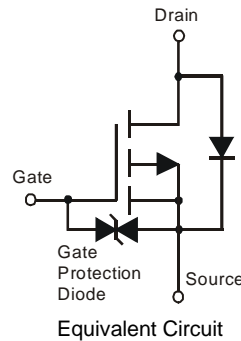
- DC-DC converters
- Load switches
- Power-management functions

## Features and Benefits

- Low On-Resistance
- Low Gate Threshold Voltage
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- ESD Protected Up to 3kV
- **Totally Lead-Free & Fully RoHS compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**
- **This part is qualified to JEDEC standards (as references in AEC-Q) for High Reliability.**  
<https://www.diodes.com/quality/product-definitions/>
- **An automotive-compliant part is available under separate datasheet ([DMG1013TQ](#))**

## Mechanical Data

- Package: SOT523
- Package Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish — Matte Tin Annealed over Alloy 42 Leadframe. Solderable per MIL-STD-202, Method 208 ③
- Terminal Connections: See Diagram
- Weight: 0.002 grams (Approximate)

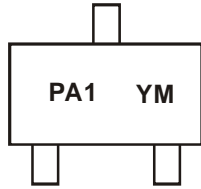


## Ordering Information (Note 4)

Part Number	Package	Packing	
		Qty.	Carrier
DMG1013T-7	SOT523	3000	Tape & Reel

- Notes:
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
  2. See <https://www.diodes.com/quality/lead-free/> for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
  3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
  4. For packaging details, go to our website at <https://www.diodes.com/design/support/packaging/diodes-packaging/>.

## Marking Information



PA1 = Product Type Marking Code  
 YM = Date Code Marking  
 Y = Year (ex: L = 2024)  
 M = Month (ex: 9 = September)

Date Code Key

<b>Year</b>	2009	...	2024	2025	2026	2027	2028	2029	2030	2031	2032	2033
<b>Code</b>	W	...	L	M	N	P	R	S	T	U	V	W

<b>Month</b>	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
<b>Code</b>	1	2	3	4	5	6	7	8	9	O	N	D

## Maximum Ratings (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit	
Drain-Source Voltage		V <sub>DSS</sub>	-20	V	
Gate-Source Voltage		V <sub>GSS</sub>	±6	V	
Drain Current (Note 5)	Steady State	I <sub>D</sub>	T <sub>A</sub> = +25°C	-0.46	A
			T <sub>A</sub> = +85°C	-0.33	
Pulsed Drain Current (Note 6)		I <sub>DM</sub>	-6	A	

## Thermal Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Total Power Dissipation (Note 5)	P <sub>D</sub>	0.27	W
Thermal Resistance, Junction to Ambient (Note 5)	R <sub>θJA</sub>	461	°C/W
Operating and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-55 to +150	°C

- Notes:
5. For a device surface mounted on a minimum recommended pad layout of an FR4 PCB, in still air conditions; the device is measured when operating in steady-state condition.
  6. Same as note 5, except the device is pulsed at duty cycle of 1% for a pulse width of 10µs.

**Electrical Characteristics** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
<b>OFF CHARACTERISTICS (Note 7)</b>						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	-20	—	—	V	V <sub>GS</sub> = 0V, I <sub>D</sub> = -250μA
Zero Gate Voltage Drain Current, T <sub>J</sub> = +25°C	I <sub>DSS</sub>	—	—	-100	nA	V <sub>DS</sub> = -20V, V <sub>GS</sub> = 0V
Gate-Source Leakage	I <sub>GSS</sub>	—	—	±2.0	μA	V <sub>GS</sub> = ±4.5V, V <sub>DS</sub> = 0V
<b>ON CHARACTERISTICS (Note 7)</b>						
Gate Threshold Voltage	V <sub>GS(TH)</sub>	-0.5	—	-1.0	V	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = -250μA
Static Drain-Source On-Resistance	R <sub>DS(ON)</sub>	—	0.5	0.7	Ω	V <sub>GS</sub> = -4.5V, I <sub>D</sub> = -350mA
			0.7	0.9		V <sub>GS</sub> = -2.5V, I <sub>D</sub> = -300mA
			1.0	1.3		V <sub>GS</sub> = -1.8V, I <sub>D</sub> = -150mA
Forward Transfer Admittance	Y <sub>fs</sub>	—	0.9	—	S	V <sub>DS</sub> = -10V, I <sub>D</sub> = -250mA
Diode Forward Voltage	V <sub>SD</sub>	—	-0.8	-1.2	V	V <sub>GS</sub> = 0V, I <sub>S</sub> = -150mA
<b>DYNAMIC CHARACTERISTICS (Note 8)</b>						
Input Capacitance	C <sub>iss</sub>	—	59.76	—	pF	V <sub>DS</sub> = -16V, V <sub>GS</sub> = 0V, f = 1.0MHz
Output Capacitance	C <sub>oss</sub>	—	12.07	—	pF	
Reverse Transfer Capacitance	C <sub>rss</sub>	—	6.36	—	pF	
Total Gate Charge	Q <sub>g</sub>	—	580	—	pC	V <sub>GS</sub> = -4.5V, V <sub>DS</sub> = -10V, I <sub>D</sub> = -250mA
Gate-Source Charge	Q <sub>gs</sub>	—	104	—	pC	
Gate-Drain Charge	Q <sub>gd</sub>	—	125	—	pC	
Turn-On Delay Time	t <sub>D(ON)</sub>	—	5.1	—	ns	V <sub>DD</sub> = -10V, V <sub>GS</sub> = -4.5V, R <sub>L</sub> = 47Ω, R <sub>G</sub> = 10Ω, I <sub>D</sub> = -200mA
Turn-On Rise Time	t <sub>R</sub>	—	8.1	—	ns	
Turn-Off Delay Time	t <sub>D(OFF)</sub>	—	28.4	—	ns	
Turn-Off Fall Time	t <sub>F</sub>	—	20.7	—	ns	

Notes: 7. Measured under pulsed conditions to minimize self-heating effect. Pulse width ≤ 300μs; duty cycle ≤ 2%.  
8. For design aid only, not subject to production testing.

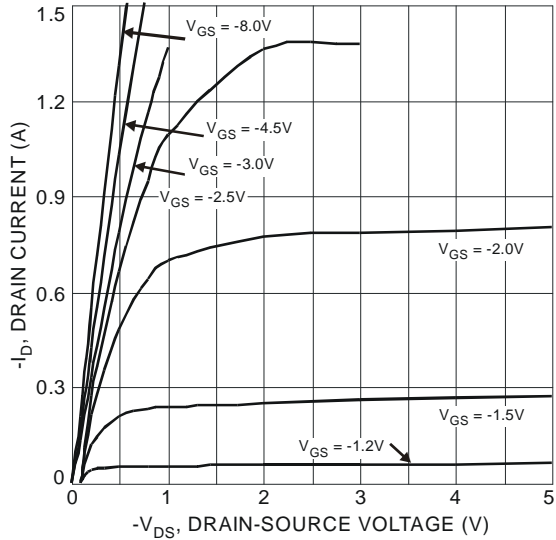


Fig. 1 Typical Output Characteristic

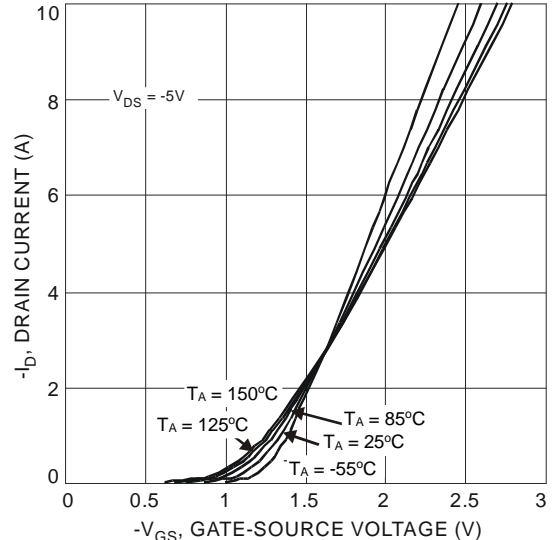


Fig. 2 Typical Transfer Characteristic

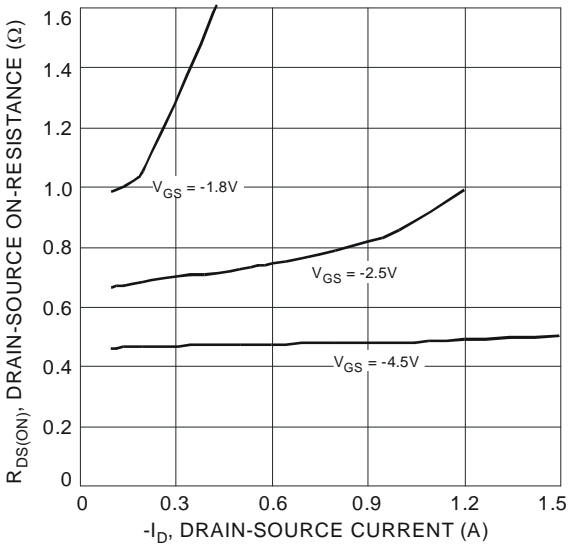


Fig. 3 Typical On-Resistance vs. Drain Current and Gate Voltage

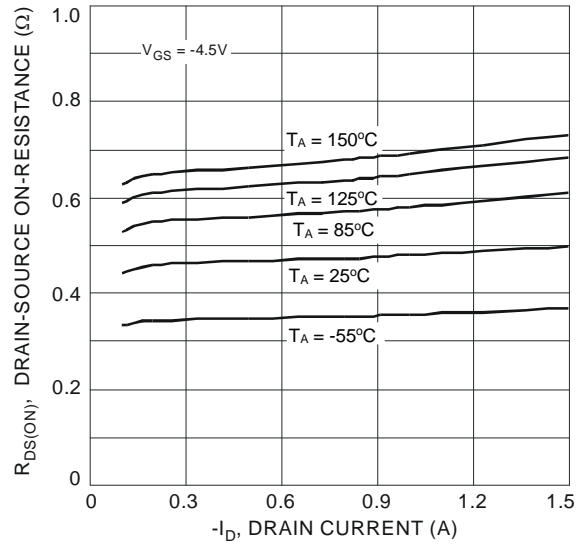


Fig. 4 Typical On-Resistance vs. Drain Current and Temperature

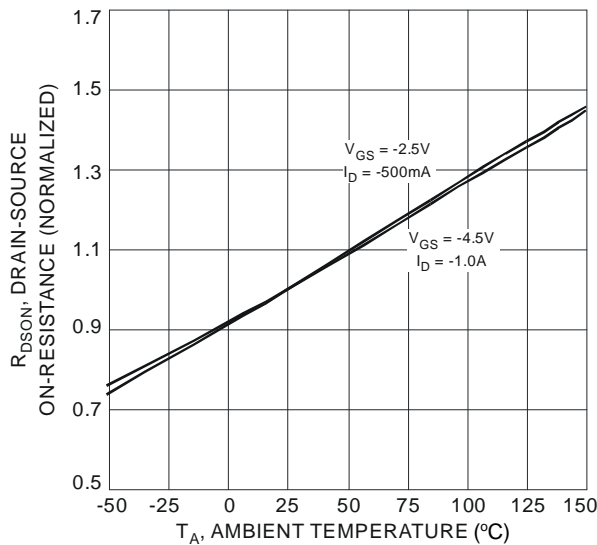


Fig. 5 On-Resistance Variation with Temperature

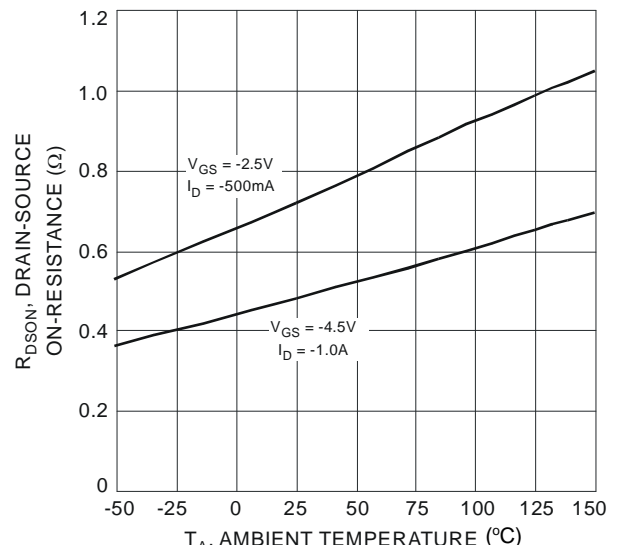


Fig. 6 On-Resistance Variation with Temperature

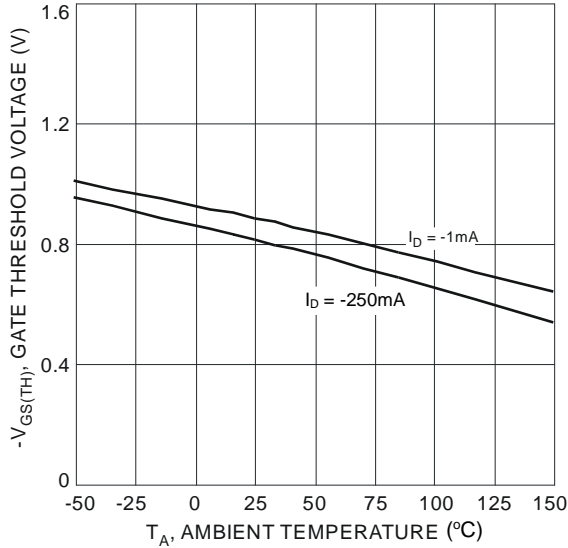


Fig. 7 Gate Threshold Variation vs. Ambient Temperature

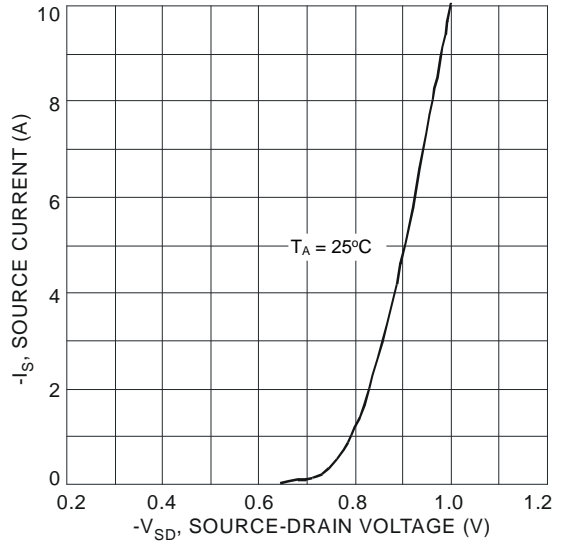


Fig. 8 Diode Forward Voltage vs. Current

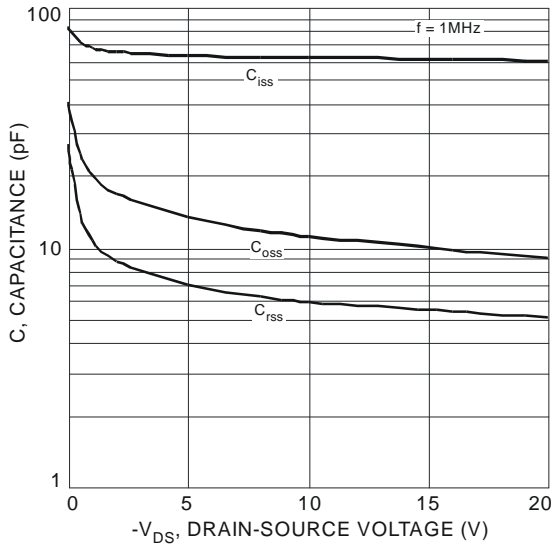


Fig. 9 Typical Total Capacitance

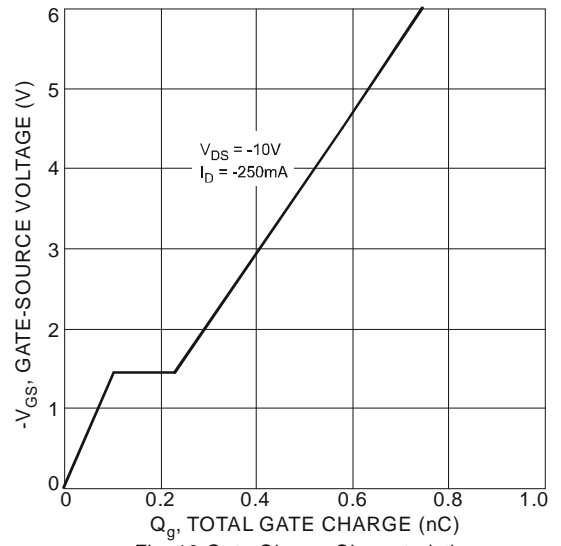


Fig. 10 Gate-Charge Characteristics

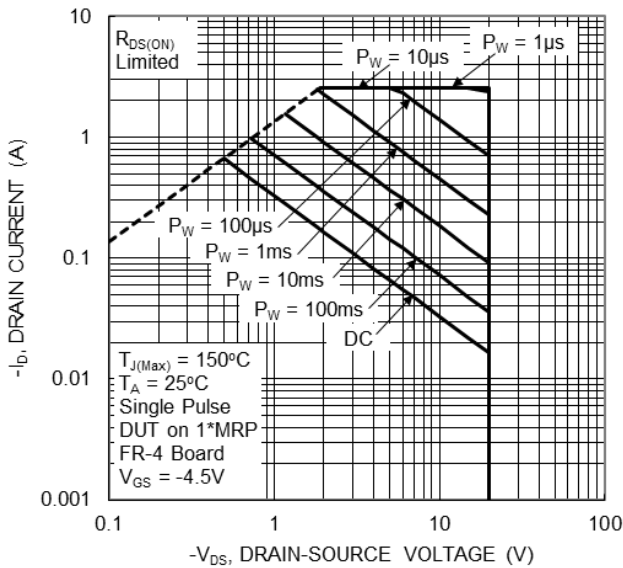


Fig. 11 SOA, Safe Operation Area

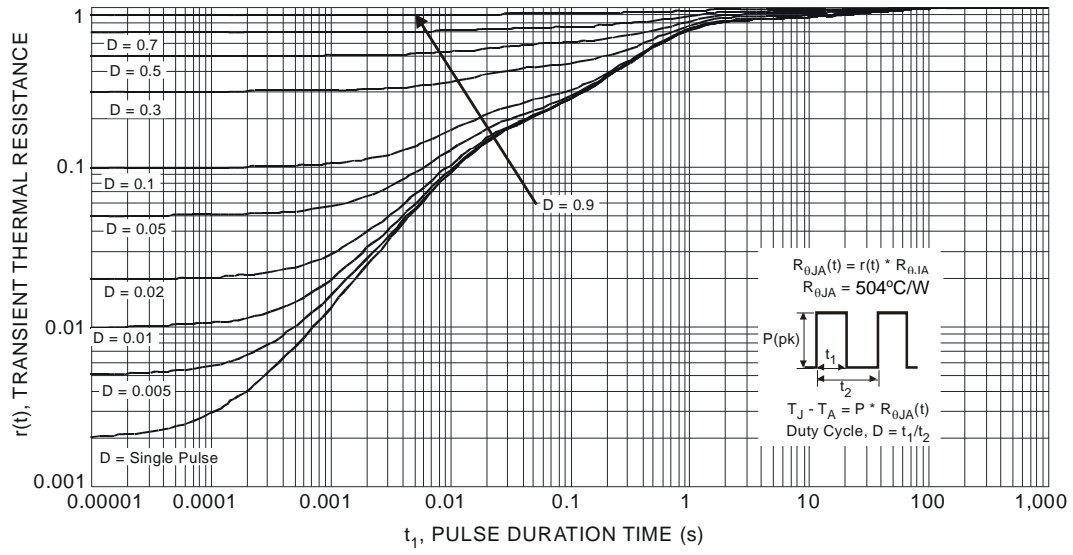
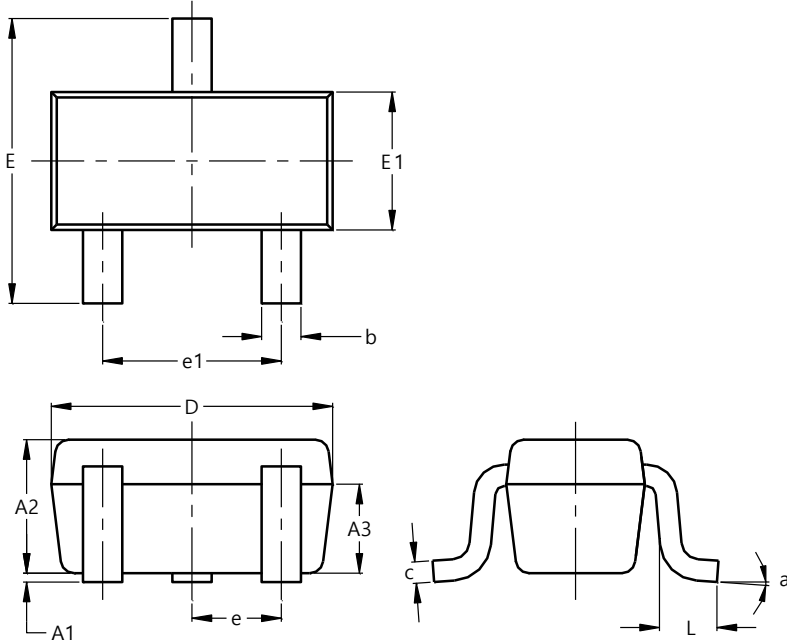


Fig. 12 Transient Thermal Response

**Package Outline Dimensions**

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

**SOT523**

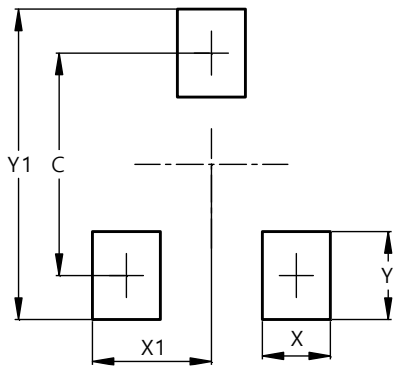


SOT523			
Dim	Min	Max	Typ
A1	0.00	0.10	0.05
A2	0.60	0.80	0.75
A3	0.45	0.65	0.50
b	0.15	0.30	0.22
c	0.10	0.20	0.12
D	1.50	1.70	1.60
E	1.45	1.75	1.60
E1	0.75	0.85	0.80
e	0.50 BSC		
e1	0.90	1.10	1.00
L	0.20	0.40	0.33
a	0°	--	8°
All Dimensions in mm			

**Suggested Pad Layout**

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

**SOT523**



Dimensions	Value (in mm)
C	1.29
X	0.40
X1	0.70
Y	0.51
Y1	1.80

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