DM5W10A-DM5W43A
3600W SURFACE MOUNT TRANSIENT VOLTAGE SUPPRESSOR

Product Summary (@TA=+25$\left.{ }^{\circ} \mathrm{C}\right)$

| PPK | IFSM (A) | V $_{\text {RWM (V) }}$ | PM $_{\text {(AV) }}$ |
| :---: | :---: | :---: | :---: |
| 3600 W | 500 | 10 to 43 | 5 W |

## Description and Applications

Suitable to protect sensitive automotive circuits against surges defined in ISO7637-2 and against load dump surge according to ISO16750-2.

Compliance with the following standards:

- ISO 10605, Pulse A and Pulse B
- ISO 7637-2 (Note 5)

Pulse 1, Pulse 2a, Pulse 3a, Pulse 3b

## Features and Benefits

- 3600W Peak Pulse Power Dissipation
- High Current Capability
- Low Reverse Current
- Low Thermal Resistance
- Low Power Loss and High Efficiency
- Excellent High Temperature Stability
- Meets ISO7637-2 Surge Capability
- Meets ISO16750-2 Surge Specification
- Lead-Free Finish; RoHS Compliant (Notes 1 \& 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e.: parts qualified to AEC-Q100/101/104/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please refer to the related automotive grade ( $Q$-suffix) part. A listing can be found at https://www.diodes.com/products/automotive/automotiveproducts/.
- This part is qualified to JEDEC standards (as references in AEC-Q) for High Reliability.
https://www.diodes.com/quality/product-definitions/


## Mechanical Data

- Package: DO-218
- Package Material: Molded Plastic

> UL Flammability Classification Rating 94V-0

- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Lead-Free Plating (Matte Tin Finish).

Solderable per MIL-STD-202, Method 208 © ©3)

- Polarity Indicator: Heatsink is Anode
- Weight: 2.74 grams (Approximate)

DO-218 (Type E)
Polarity: Heatsink is anode


Top View


Pin Information

## Ordering Information (Note 4)

| Part Number | Qualification | Package | Carrier |  |
| :---: | :---: | :---: | :---: | :---: |
|  |  |  |  |  |  |  |
| DM5WxxA-13 | 750 | Tape \& Reel |

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## Marking Information



Date Code Key

| Year | 2018 | ... | 2022 | 2023 | 2024 | 2025 | 2026 | 2027 | 2028 | 2029 | 2030 | 2031 |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Code | 1 | $\ldots$ | M | N | 0 | P | Q | R | S | T | U | V |
| Month | Jan | Feb | Mar | Apr | May | Jun | Jul | Aug | Sep | Oct | Nov | Dec |
| Code | 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 | 9 | A | B | C |
| Date | 1 | 2 | 3 | $\ldots$ | 9 | 10 | 11 | 12 | $\ldots$ | 29 | 30 | 31 |
| Code | 1 | 2 | 3 | $\ldots$ | 9 | A | B | C | $\ldots$ | T | U | V |

## Maximum Ratings (@TA $=+25^{\circ} \mathrm{C}$, unless otherwise specified.)

| Characteristic |  | Symbol | Value | Unit |
| :---: | :---: | :---: | :---: | :---: |
| Peak Pulse Power Dissipation <br> (Non Repetitive Current Pulse Derated above $\mathrm{T}_{\mathrm{A}}=+25^{\circ} \mathrm{C}$ ) <br> (Note 6) | 10/1000 $\mu \mathrm{s}$ Waveform | PPK | $\begin{aligned} & 3600 \\ & 2800 \end{aligned}$ | W |
|  | 10/10000 s W Waveform |  |  |  |
| Peak Forward Surge Current, <br> 8.3ms Single Half Sine-Wave Superimposed on Rated Load (Notes 6 \& 7) |  | IFSM | 500 | A |
| Steady State Power Dissipation @Tc $=+25^{\circ} \mathrm{C}$ |  | $\mathrm{PM}(\mathrm{AV})$ | 5.0 | W |

## Thermal Characteristics

| Characteristic | Symbol | Value | Unit |
| :--- | :---: | :---: | :---: |
| Typical Thermal Resistance, Junction to Case | RөJc | 1.1 | ${ }^{\circ} \mathrm{C} / \mathrm{W}$ |
| Operating Temperature Range | $\mathrm{TJ}_{\mathrm{J}}$ | -55 to +175 | ${ }^{\circ} \mathrm{C}$ |
| Storage Temperature Range | $\mathrm{TSTG}^{\circ}$ | -55 to +175 | ${ }^{\circ} \mathrm{C}$ |

Notes: $\quad$ 6. Valid provided that terminals are kept at ambient temperature.
7. Measured on 8.3 ms single half sine-wave or equivalent square wave. Duty cycle $=4$ pulses per minute maximum.

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Electrical Characteristics (@TA $=+25^{\circ} \mathrm{C}$, unless otherwise specified.)

| Part Number | Reverse Standoff Voltage | $\begin{gathered} \text { Breakdown } \\ \text { Voltage } \\ \text { VBR @ IT (Note 8) } \end{gathered}$ |  | Test Current | Max. Reverse Leakage @ Vrwm | Max. Clamping Voltage @ lpp | Max. Peak Pulse Current lpp at 10/1000 $\mu \mathrm{s}$ (Note 9) | $\begin{gathered} \text { Maximum } \\ \text { Leakage } \\ \text { at } \mathrm{V} w \mathrm{~m} \\ \mathrm{~T}_{\mathrm{J}}=+175^{\circ} \mathrm{C} \end{gathered}$ |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | VRWM (V) | Min (V) | Max (V) | $\mathrm{IT}^{(\mathrm{mA}}$ ) | $\mathrm{IR}(\mu \mathrm{A})$ | Vc (V) | (A) | ld ( $\mu \mathrm{A}$ ) |
| DM5W10A | 10 | 11.1 | 12.3 | 5 | 15 | 17.0 | 211 | 250 |
| DM5W11A | 11 | 12.2 | 13.5 | 5 | 10 | 18.2 | 198 | 150 |
| DM5W12A | 12 | 13.3 | 14.7 | 5 | 10 | 19.9 | 181 | 150 |
| DM5W13A | 13 | 14.4 | 15.9 | 5 | 10 | 21.5 | 167 | 150 |
| DM5W14A | 14 | 15.6 | 17.2 | 5 | 10 | 23.2 | 155 | 150 |
| DM5W15A | 15 | 16.7 | 18.5 | 5 | 10 | 24.2 | 148 | 150 |
| DM5W16A | 16 | 17.8 | 19.7 | 5 | 10 | 26.0 | 138 | 150 |
| DM5W17A | 17 | 18.9 | 20.9 | 5 | 10 | 27.6 | 130 | 150 |
| DM5W18A | 18 | 20.0 | 22.1 | 5 | 10 | 29.2 | 123 | 150 |
| DM5W20A | 20 | 22.2 | 24.5 | 5 | 10 | 32.4 | 111 | 150 |
| DM5W22A | 22 | 24.4 | 26.9 | 5 | 10 | 35.5 | 101 | 150 |
| DM5W24A | 24 | 26.7 | 29.5 | 5 | 10 | 38.9 | 93 | 150 |
| DM5W26A | 26 | 28.9 | 31.9 | 5 | 10 | 42.1 | 86 | 150 |
| DM5W28A | 28 | 31.1 | 34.4 | 5 | 10 | 45.4 | 79 | 150 |
| DM5W30A | 30 | 33.3 | 36.8 | 5 | 10 | 48.4 | 74 | 150 |
| DM5W33A | 33 | 36.7 | 40.6 | 5 | 10 | 53.3 | 68 | 150 |
| DM5W36A | 36 | 40.0 | 44.2 | 5 | 10 | 58.1 | 62 | 150 |
| DM5W40A | 40 | 44.4 | 49.1 | 5 | 10 | 64.5 | 56 | 150 |
| DM5W43A | 43 | 47.8 | 52.8 | 5 | 10 | 69.4 | 52 | 150 |

Notes: $\quad$ 8. $V_{B R}$ measured with $I_{T}$ current pulse $=10 \mathrm{~ms}$ to 15 ms .
9. Refer to Figure 3 for the waveform.



Fig. 3 - Pulse Waveform


Fig. 5 Typical Transient Thermal Impedance


Fig. 2 Load Dump Power Characteristics (10ms Exponential Waveform)


Fig. 4 Reverse Power Capability


Fig. 6 Typical Total Capacitance

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## Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.
DO-218 (Type E)


| DO-218 <br> (Type E) |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: |
| Dim | Min | Max | Typ |  |
| A | 4.70 | 5.70 | -- |  |
| A1 | 4.70 | 5.25 | 5.00 |  |
| A2 | 3.45 | 4.26 | 3.95 |  |
| A3 | 1.70 | 2.50 | 2.00 |  |
| A4 | 2.58 | 3.55 | 3.10 |  |
| b | 2.30 | 3.00 | -- |  |
| C | 0.45 | 0.90 | -- |  |
| D | 13.20 | 13.80 | 13.50 |  |
| D1 | 8.70 | 9.30 | 9.00 |  |
| D2 | 9.70 | 10.30 | 10.00 |  |
| E | 8.20 | 8.80 | 8.50 |  |
| E1 | 9.50 | 10.50 | -- |  |
| H | 15.00 | 16.00 | 15.50 |  |
| L | 1.50 | 2.50 | 2.00 |  |
| All Dimensions in $\mathbf{~ m m}$ |  |  |  |  |
|  |  |  |  |  |



## Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

## DO-218 (Type E)



| Dimensions | Value <br> (in $\mathbf{~ m m}$ ) |
| :---: | :---: |
| $\mathbf{G}$ | 3.30 |
| $\mathbf{X}$ | 3.50 |
| $\mathbf{X 1}$ | 11.00 |
| $\mathbf{Y}$ | 3.00 |
| $\mathbf{Y 1}$ | 9.50 |
| $\mathbf{Y} 2$ | 15.80 |

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[^0]:    *x = Device Voltage, e.g., DM5W10A-13
    Notes: 1. EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) \& 2015/863/EU (RoHS 3) compliant. All applicable RoHS exemptions applied.
    2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
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    4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.
    5. Not applicable to parts with stand-off voltage lower than the average battery voltage (13.5V).

