## Product Summary

| $\mathbf{V}_{\mathbf{B R}(\min )}$ | $\mathrm{I}_{\mathrm{PP}(\max )}$ | $\mathrm{I}_{\mathrm{R}(\max )}$ |
| :---: | :---: | :---: |
| 5.8 V and 11 V | 9 A | 25 nA |

## Description

This new generation TVS is designed to protect sensitive electronics from the damage due to ESD. The combination of small size and high ESD surge capability makes it ideal for use in portable applications such as cellular phones, digital cameras, and MP3 players.

## Applications

- Cellular handsets
- Portable electronics
- Computers and peripherals


## Features

- Low-Profile Package ( 0.53 mm max) and Ultra-Small PCB Footprint Area ( 1.08 * 0.68mm max) Suitable for Compact Portable Electronics
- Provides ESD Protection per IEC 61000-4-2 Standard: Air $\pm 30 \mathrm{kV}$, Contact $\pm 30 \mathrm{kV}$
- 1 Channel of ESD Protection
- Low Channel Input Capacitance
- Totally Lead-Free \& Fully RoHS Compliant (Notes 1 \& 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/104/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please contact us or your local Diodes representative. https://www.diodes.com/quality/product-definitions/


## Mechanical Data

- Package: X1-DFN1006-2
- Package Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: NiPdAu over Copper leadframe. Solderable per MIL-STD-202, Method 208 (e4)
- Weight: 0.001 grams (approximate)


Bottom View


Device Schematic

## Ordering Information (Note 4)

| Product | Marking | Reel Size (inches) | Tape Width (mm) | Packing |  |
| :---: | :---: | :---: | :---: | :---: | :---: |
|  | Qarrier |  |  |  |  |
| DESDALC5ALP-7B | QR | 7 | 8 | 10,000 | Tape \& Reel |

Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) \& 2015/863/EU (RoHS 3) compliant.
2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen-and Antimony-free, "Green" and Lead-free.
3. Halogen- and Antimony-free "Green" products are defined as those which contain $<900 \mathrm{ppm}$ bromine, $<900 \mathrm{ppm}$ chlorine ( $<1500 \mathrm{ppm}$ total $\mathrm{Br}+\mathrm{Cl}$ ) and <1000ppm antimony compounds.
4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

## Marking Information



QR = Product Type Marking Code Line Denotes Pin 1

DESDALC5ALP

Maximum Ratings (@ $\mathrm{T}_{\mathrm{A}}=+25^{\circ} \mathrm{C}$, unless otherwise specified.)

| Characteristic | Symbol | Value | Unit | Conditions |
| :--- | :---: | :---: | :---: | :---: |
| Peak Pulse Power Dissipation | PPP $^{2}$ | 150 | W | $8 / 20 \mu \mathrm{~s}$ |
| Peak Pulse Current | IPP | 9 | A | $8 / 20 \mu \mathrm{~s}$ |
| ESD Protection - Contact Discharge | V ESD_Contact | $\pm 30$ | kV | IEC 61000-4-2 Standard |
| ESD Protection - Air Discharge | V ESD_Air $^{2}$ | $\pm 30$ | kV | IEC 61000-4-2 Standard |

## Thermal Characteristics

| Characteristic | Symbol | Value | Unit |
| :--- | :---: | :---: | :---: |
| Package Power Dissipation (Note 5) | $\mathrm{PD}_{\mathrm{D}}$ | 250 | mW |
| Thermal Resistance, Junction to Ambient (Note 5) | $\mathrm{R}_{\theta J A}$ | 500 | ${ }^{\circ} \mathrm{C} / \mathrm{W}$ |
| Operating and Storage Temperature Range | $\mathrm{T}_{J}, \mathrm{~T}_{\text {STG }}$ | -65 to +150 | ${ }^{\circ} \mathrm{C}$ |

Electrical Characteristics (@T $A=+25^{\circ} \mathrm{C}$ unless otherwise specified)

| Characteristic | Symbol | Min | Typ | Max | Unit | Test Conditions |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Reverse Breakdown Voltage | $V_{B R}$ | 11 | 13 | 17 | V | $\mathrm{I}_{\mathrm{R}}=1 \mathrm{~mA}$, pin1 to pin2 |
|  |  | 5.8 | 8 | 11 |  | $\mathrm{I}_{\mathrm{R}}=1 \mathrm{~mA}, \mathrm{pin} 2$ to pin1 |
| Reverse Current (Note 6) | $\mathrm{I}_{\mathrm{R}}$ | - | - | 25 | nA | $\mathrm{V}_{\mathrm{R}}=5 \mathrm{~V}$ |
| Dynamic Resistance, from Pin 1 to Pin 2 | R ${ }_{\text {DYN }}$ | - | 0.19 | - | $\Omega$ | $\mathrm{I}_{\text {TLP }}=1 \mathrm{~A}$ to 20A, $\mathrm{tp}=100 \mathrm{~ns}$ |
| Dynamic Resistance, from Pin 2 to Pin 1 | R ${ }_{\text {DYN }}$ | - | 0.19 | - | $\Omega$ |  |
| Capacitance | $\mathrm{C}_{\text {T }}$ | - | 26 | 30 | pF | $\mathrm{V}_{\mathrm{R}}=0 \mathrm{~V}, \mathrm{f}=1 \mathrm{MHz}$ |
| Clamping Voltage, from Pin 1 to Pin 2 | $\mathrm{V}_{\mathrm{CL}}$ | - | 21 | - | V | 8 kV contact discharge after 30ns IEC61000-4-2 |
| Clamping Voltage, from Pin 2 to Pin 1 | $\mathrm{V}_{\mathrm{CL}}$ | - | 12 | - | V | 8 kV contact discharge after 30ns IEC61000-4-2 |

Notes: 5. Device mounted on FR-4 PCB pad layout (2oz copper) as shown on on our website at www.diodes.com/package-outlines.html.
6. Short duration pulse test used to minimize self-heating effect.


Figure 1 Normalized Peak Pulse Power vs. Initial Junction Temperature


Figure 2 Leakage Current vs. Junction Temperature (Typical Values)

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Figure 3 Peak Pulse Power vs. Pulse Duration


Figure 5 Clamping Voltage vs. Peak Pulse Current
(Typical Values)


Figure 7 Junction Capacitance vs. Reverse Voltage (Typical Values)


Figure 4 Peak Pulse Power vs. Pulse Duration


Figure 6 Clamping Voltage vs. Peak Pulse Current (Typical Values)


Figure 8 Junction Capacitance vs. Reverse Voltage (Typical Values)

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$\left.\begin{array}{l}\begin{array}{l}\text { 50ns/div } \\ 50 \mathrm{~V} / \mathrm{div}\end{array} \\ \begin{array}{l}\text { (1) ESD Peak } \\ \text { (2) Clamp Voltage @ 30ns }\end{array} \\ \text { (3) Clamp Voltage @ 60ns } \\ \text { (4) Clamp Voltage @ 100ns }\end{array}\right\}$

Figure 9 ESD Response to IEC 6100-4-2 (+8kV Contact Discharge)


Figure 11 ESD Response to IEC 6100-4-2 (+15kV Contact Discharge)


Figure 13 S21 Attenuation Measurement Result


Figure 10 ESD Response to IEC 6100-4-2 (-8kV Contact Discharge)


Figure 12 ESD Response to IEC 6100-4-2 (-15kV Contact Discharge)


Figure 14 TLP Measurement

## Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.


## Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.
X1-DFN1006-2


| Dimensions | Value <br> (in $\mathbf{~ m m}$ ) |
| :---: | :---: |
| $\mathbf{C}$ | 0.70 |
| $\mathbf{G}$ | 0.30 |
| $\mathbf{X}$ | 0.40 |
| $\mathbf{X 1}$ | 1.10 |
| $\mathbf{Y}$ | 0.70 |

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