



ONE CHANNEL HIGH SURGE TVS DIODE

Product Summary

VBR (Min)	IPP (Max)	Ст (Тур)
26.0V	31A	210pF

Description

This new generation TVS is designed to protect sensitive electronics from the damage due to ESD. The combination of small size and high ESD surge capability makes it ideal for protecting one line against high surge current and other transients.

Applications

- Power line protections
- Mobile device applications
- Touch panels
- Small panel modules
- Type-C "CC"
- V_{DD} protections

Features

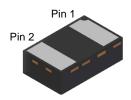
- Provides ESD Protection per IEC 61000-4-2 Standard: Air ±30kV, Contact ±30kV
- One Channel of ESD Protection
- Low Channel Input Capacitance
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- The DIODES™ D24V0S1U2LP1610Q is suitable for automotive applications requiring specific change control; this part is AEC-Q101 qualified, PPAP capable, and manufactured in IATF 16949 certified facilities.

 https://www.diodes.com/quality/product-definitions/

Mechanical Data

- Package: U-DFN1610-2
- Package Material: Molded Plastic, "Green" Molding Compound.
 UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: NiPdAu. Solderable per MIL-STD-202, Method 208 (4)
- Weight: 0.003 grams (Approximate)

U-DFN1610-2 (Type B)



Bottom View



Device Schematic

Ordering Information (Note 4)

Part Number	Dookogo	Marking Code	Deal Size (inches)	Tape Width (mm)	Packing		
Part Number	Package	warking Code	Reel Size (inches)	rape width (mm)	Qty.	Carrier	
D24V0S1U2LP1610Q-7	U-DFN1610-2 (Type B)	24T	7	8	10,000	Tape & Reel	

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.



Marking Information

Option A:



24T = Product Type Marking Code YM = Date Code Marking Y = Year (ex: J = 2022) M = Month (ex: 9 = September) Dot Denotes Cathode Side

Date Code Key

Year	2022	2023	2024	2025	2026	2027	2028	2029	2030	2031	2032	2033
Code	J	K	L	М	N	0	Р	R	S	Т	U	V
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec

Option B:



24T = Product Type Marking Code YWX = Date Code Marking Y = Year (ex: 0 to 9) W = Week (ex: a = Week 27; z Represents Week 52 and 53) X = Internal Code Dot Denotes Cathode Side

Date Code Key

Year	2022	2023	2024	2025	2026	2027	2028	2029	2030	2031	2032	2033
Code	2	3	4	5	6	7	8	9	0	1	2	3

[Week	1-26	27-52	53
	Code	A-Z	a-z	Z

Internal Code	Sun	Mon	Tue	Wed	Thu	Fri	Sat
Code	Т	U	V	W	Χ	Υ	Z



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	Conditions
ESD Protection – Contact Discharge	Vesd_contact	±30	kV	Standard IEC 61000-4-2
ESD Protection – Air Discharge	Vesd_air	±30	kV	Standard IEC 61000-4-2

Thermal Characteristics

Characteristic		Symbol	Value	Unit
Power Dissipation (Note 5)		P _D	300	mW
Thermal Resistance, Junction to Ambient T _A :	= +25°C	$R_{\theta JA}$	417	°C/W
Operating and Storage Temperature Range		TJ, TSTG	-55 to +150	°C

Electrical Characteristics (@TA = +25°C, unless otherwise specified.)

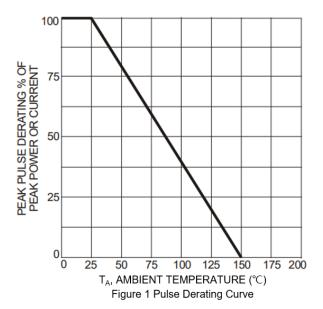
Part Number	Reverse Standoff Voltage	Breakdown Voltage		Test Current	Max. Reverse Leakage Current @ V _{RWM} (Note 6)	Max. Clamping Voltage @ IPP (Note 7)	Max. Peak Pulse Current	Channel Input Capacitance (Note 8) VR = 0V, f = 1MHz, Any I/O to GND	Marking Code
		V BR	@ I _T		()			,	
	V _{RWM} (V)	Min (V)	Max (V)	l⊤ (mA)	I _R (nA)	Vc (V)	IPP (A)	Ст (рF)	
D24V0S1U2LP1610Q-7	24	26	30	1	200	42	31	210	24T

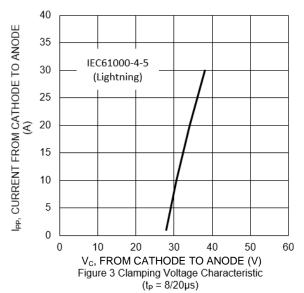
Notes:

^{5.} Device mounted on FR-4 PCB pad layout (2oz copper) as shown on Diodes Incorporated's suggested pad layout, which can be found on our website at http://www.diodes.com/package-outlines.html.
Short duration pulse test used to minimize self-heating effect.
Clamping voltage value is based on an 8x20μs peak pulse current (IPP) waveform.
Measured from any I/O to GND.









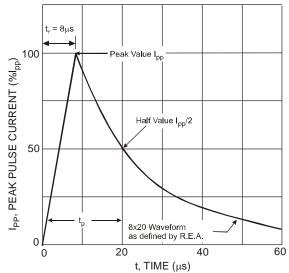


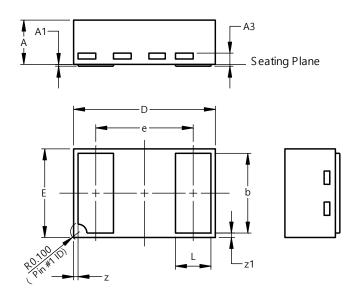
Figure 2 Typical 8 x 20µs Pulse Waveform



Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

U-DFN1610-2 (Type B)

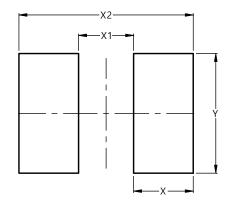


U-DFN1610-2 (Type B)								
Dim	Min	Max	Тур					
Α	0.45	0.55	0.50					
A1	0.00	0.05	0.015					
A3	-	-	0.127					
b	0.85	0.95	0.90					
D	1.55	1.65	1.60					
Е	0.95	1.05	1.00					
е	1	-	1.10					
L	0.35 0.45 0.40							
Z	z 0.050 REF							
z1	z1 0.050 REF							
All D	imens	ions in	mm					

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

U-DFN1610-2 (Type B)



Dimensions	Value (in mm)
Х	0.650
X1	0.600
X2	1.900
Υ	1.300



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