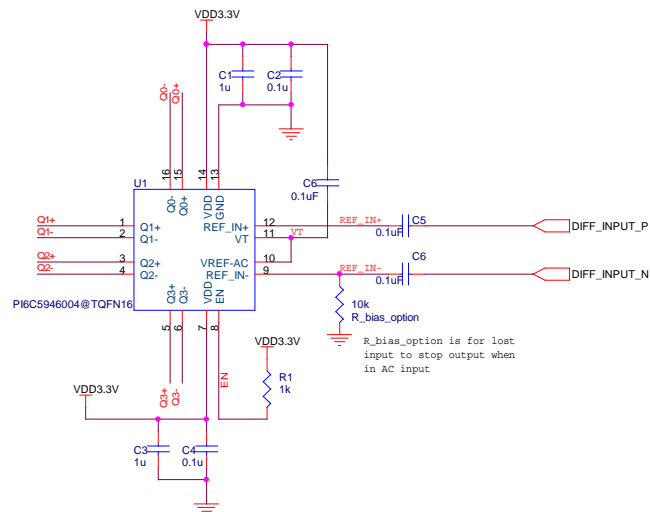
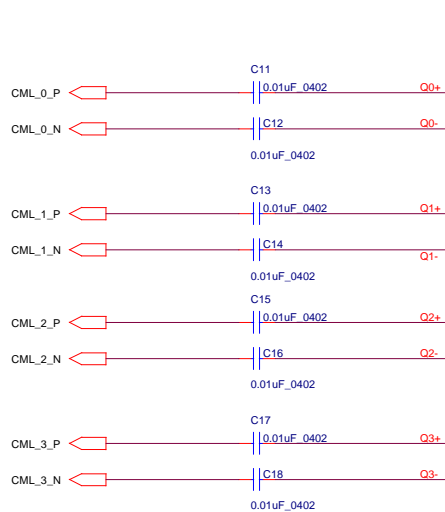


CML OUTPUT_0/3

To AC
CML/LVPECL/LVDS
input of 100ohm
diff. load



App Note:

1. This IC has internal 100ohm diff. termination already
2. If use AC input, needs connect VREF-AC to VT
3. If use DC diff. input, leave VREF-AC and VT open
4. Each VDD pin needs 0.1uF +1uF decoupling cap.
5. Make =>4 vias on device thermal pad to GND plane
6. This is CML output device does not need 150ohm pull-down;
7. CML app. uses AC coupling drive, if need DC coupling drive check datasheet of output DC level

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