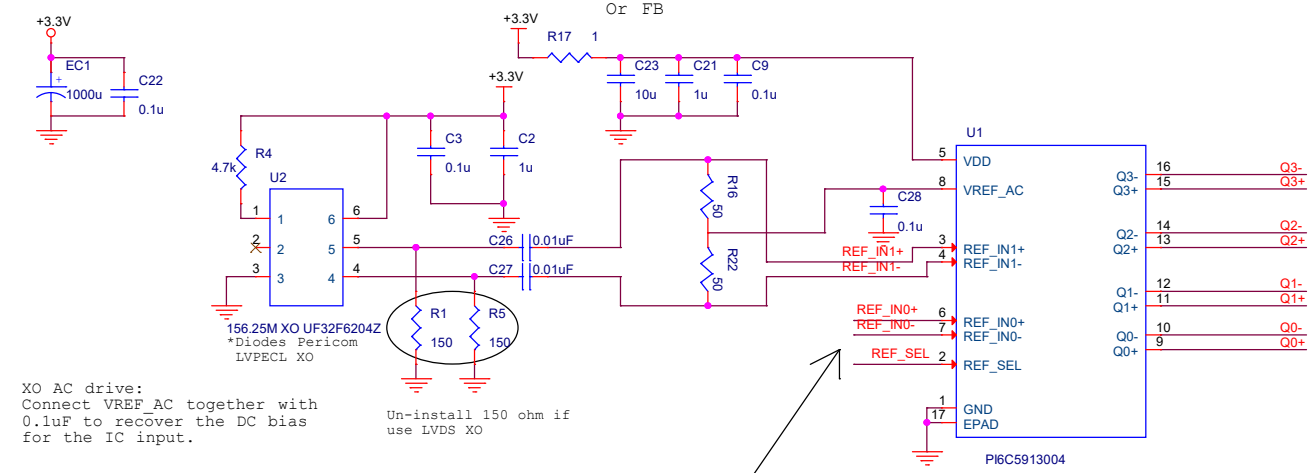
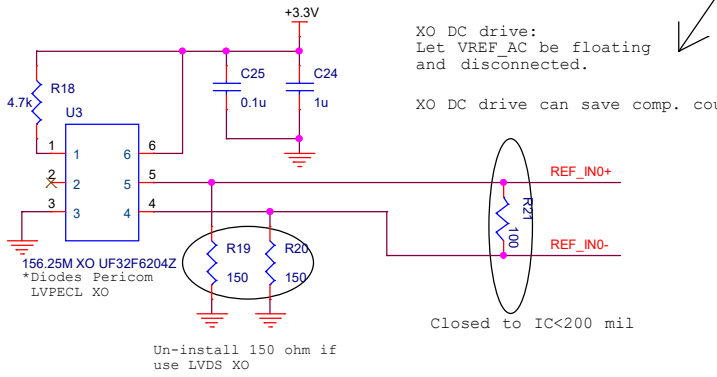
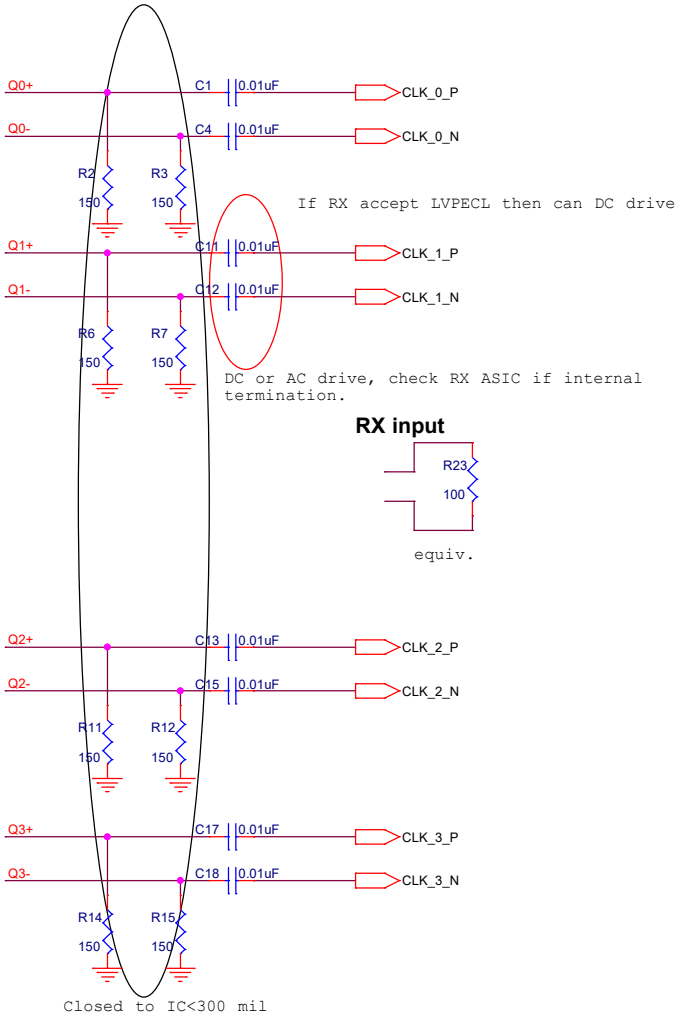
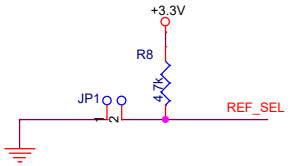




XO DC drive:
Let VREF_AC be floating
and disconnected.

XO DC drive can save comp. count





Set REF_SEL logic in R8/JP1

CLK_SEL	Function
0	REF_IN0 is Input
1	REF_IN1 is Input

App Note:

1. Select REF_IN0 or REF_IN1 as input ;
2. 1 Each VDD pin needs 0.1u +1uF decoupling close to pin. (e.g.: VDD,...etc)
2. 2 VDD uses small R=1~2 ohm or FB(ferrite bead)+C=10uF filtering for better DC/DC ripple noise rejection
3. Place 150ohm pull-down in comp. side close to pin <=300mil.
4. Suggest to use DC coupling in LVPECL/LVDS drive input clock with 100ohm corss at input pins <200mil;
5. When in AC input drive ,VREF_AC is used.
6. If use CMOS XO drive, needs 1k pull-up/down at REF_IN_ pin, refer to datasheet app. page

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