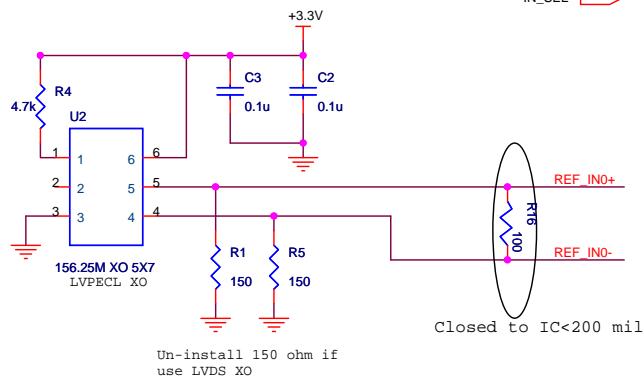


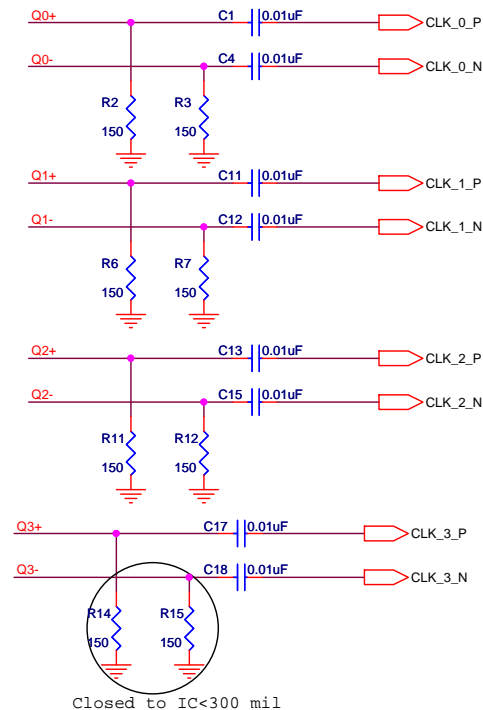
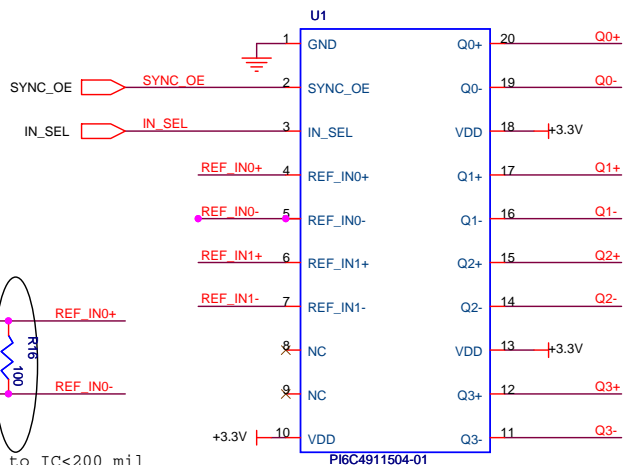
SYNC_OE	Function
0	All output Disable
1	All output Enable

IN_SEL	Function
0	Ref_IN0 is Input
1	Ref_IN1 is Input



App Note:

1. Select Ref_IN0 or Ref_IN1 as input ;
2. Each VCC pin must have 0.1uF decoupling cap.; better has 1u more;
3. Place 150ohm pull-down in comp. side close to pin <=300mil.
4. Suggest to use DC coupling in LVPECL/LVDS drive input clock with 100ohm corss at input pins <200mil;
5. when in AC input drive either just use 100ohm cross to bias balance
6. If use CMOS XO drive, needs 1k pull-up/down at REF_IN_ pin, refer to datasheet app. page



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