

AN74 Solar Street Lighting application with ZXLD1374

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Introduction

Energy efficiency and sustainability are more and more at the top of policymakers' agenda. A way to reduce power consumption and CO_2 impact while maintaining the safety standards of the roads is to use solar powered solar street lighting systems.

Interest in solar lighting systems, and particularly street lighting, is rising also for the possibility to install them in remote areas such as rural areas in less developed countries.

Solar street lighting systems generally consist of a solar panel able to deliver about 90W, a battery charge controller, a set of batteries at 12V and a Boost driver to control the current in the LEDs, as shown in figure 1.



BATTERIES

Figure 1: Solar street lighting system

ZXLD1374 Device description

The ZXLD1374 is an LED driver IC with an internal MOSFETs to drive high current LEDs. It is a multi-topology driver enabling it to efficiently control the current through series connected LED. The multi-topology enables it to operate in Buck, Boost and Buck-boost configurations.

The 60V capability coupled with its multi-topology flexibility enables it to be used in a wide range of applications and drive up to 15 LEDs in series.

The ZXLD1374 is a modified hysteretic controller using a patent pending control scheme providing high output current accuracy in all three modes of operation. High accuracy dimming is achieved through dc dimming and high frequency PWM dimming.

The ZXLD1374 uses two pins for fault diagnosis. A flag output highlights a fault presence, while the multi-level status pin gives further information on the specific fault.

ZXLD1374 for solar street lighting

Street lighting applications are modular since they need to adapt to different light output requirements.

The circuit using the ZXLD1374 is designed to drive a single string of 12 series connected LEDs from a 12V battery for solar street lighting applications.

In particular, in this note we are going to focus on a design of a lighting system having 4200lm on target from a single fitting.

To this end, the procedure to calculate the number of strings (modules) required for our target applications are as in the following paragraphs.

Assuming to use 90lm/W LEDs and lenses with 90% optical efficiency and a further 20% of lumen losses due to thermal derating of the LEDs at high temperature, a string of 12LEDs @ 350mA is able to deliver approximately 898lm.

Lumen output per string = 90lm/W * 0.9 * (1- 0.2) * (12 * 3.3V * 350mA) = 898lm

Since the requirement for solar street lighting systems is to have 4200lm on target from a single fitting, the number of strings required is 5.

The total power dissipation will depend also on the type of LEDs being used:

If VF = 3.3V → PLED = 3.3V * 0.35A = 1.15W

The total output power for 5 strings of 12 series LEDs is:

P_{LED-TOT} = 5 * 12 * PLED = 69W

Since the ZXLD1374 can achieve 91% of efficiency in this application the total power consumption is:

 $P_{TOT-MAX} = P_{LED-TOT} / Min - Efficiency = 69 / 0.91 = 75.8W$

Therefore to total power consumption of a system of 5 strings of 12 LEDs in series is about 75W, compatible with the power that a standard solar panel used in street lighting applications can currently deliver.

Solar Street Lighting - System design



In this section the system design procedure is outlined according to the schematic shown in figure 2.

Figure 2: Circuit schematic

The specifications for each string of the module are: VIN =12V (Nominal)

VINMIN = 9V (minimum) VINMAX = 15V (maximum) ILED = 350mA VLED = 3.3V VLEDs = 12 * VLEDs = 40V

The LED current have been set according to the following considerations:

Calculate the max duty cycle: $D_{MAX} = \frac{V_{LEDS} - V_{IN MIN}}{V_{LEDS}} = \frac{40 - 9}{40} \approx 0.77$ Calculate the GI pin ratio as $\frac{R_{10}}{(R_9 + R_{10})} \approx 1 - D_{MAX} \approx 0.23$ Calculate the sense resistor as: $R_1 = \frac{R_{10}}{(R_9 + R_{10})} \frac{225mV}{I_{LED}} \approx 150m\Omega$

This component dimensioning with allow the device to operate safely in worst case condition since: $I_{LED} = 350 \text{mA}$

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$$I_{COIL} = \frac{I_{LED}}{1 - D_{MAX}} = 1.52A$$

 $V_{RS} = I_{COIL} * R_1 = \frac{I_{LED} * R_1}{1 - D} = 228mV$

While in nominal condition this will result in:

$$I_{LED} = 350 \text{mA}$$

$$I_{COIL} = \frac{I_{LED}}{1 - D} = 1.17 \text{A}$$

$$V_{RS} = I_{COIL} * \text{R}_{1} = \frac{I_{LED} * \text{R}_{1}}{1 - D} = 175 \text{mV}$$

Input capacitor

The input capacitor can be selected using the following formula:

$$C_{IN} = \frac{\Delta I_{COIL-PP}}{8^* f_{SW}^* \Delta V_{IN-PP}}$$

Where:

- $\Delta I_{COIL-PP}$, the ripple on the coil current, is 40% of worst case current I_{COIL} =1.52A
- ΔV_{IN-PP} , the ripple on the input voltage, is set at 100mV
- f_{sw}, the switching frequency, is 350kHz in nominal conditions

$$C_{IN} = C_3 = \frac{\Delta I_{COIL-PP}}{8^* f_{SW}^* \Delta V_{IN-PP}} = \frac{2^* 0.2^* 1.52}{8^* 350 k^* 100 m} \approx 2.2 uF$$

To minimize power supply interaction a 200% larger capacitance C₃ of approximately 4.7 μF is used, therefore the actual ΔV_{IN-PP} is much lower.

In addition to this, a capacitor C_2 of 1uF placed close to the VIN pin is used to filter the supply signal on VIN pin in order to guarantee high level of accuracy in the sensed current.

Output capacitor

An output capacitor is required to limit interference and provide energy to the load when the freewheeling diode is reverse biased during the first switching subinterval. The output capacitor is chosen to provide a desired current ripple of the LED current (usually recommended to be less than 40% of the average LED current).

$$C_{OUTPUT} = \frac{D * I_{LED}}{f_{SW} * r_{LED} * \Delta I_{LED-PP}}$$

where:

- ΔI_{LED} is the ripple of the LED current, set at 10% \rightarrow 35mA
- f_{sw} is the switching frequency (From graphs and calculator)

• r_{LED} is the dynamic resistance of the LEDs string (N times the dynamic resistance of the single LED - from the datasheet of the LED manufacturer).

 $C_{\text{OUTPUT}} = C_{10} = \frac{D^* I_{\text{LED}}}{f_{\text{SW}} * r_{\text{LED}} * \Delta I_{\text{LED-PP}}} = \frac{0.7 * 350 \text{mA}}{350 \text{kHz} * 12 * 350 \text{m}\Omega * 35 \text{mA}} \approx 4.7 \mu \text{F}$

The output capacitor should be chosen to account for derating due to temperature and operating voltage. It must also have the necessary RMS current rating. The minimum RMS current for the output capacitor is calculated as follows:

$$I_{\text{COUTPUT-RMS}} = I_{\text{C}_{10}-\text{RMS}} = I_{\text{LED}} \sqrt{\frac{D_{\text{MAX}}}{1 - D_{\text{MAX}}}} = 0.35 \sqrt{\frac{0.77}{1 - 0.77}} = 0.64 \text{A}$$

Ceramic capacitors with X7R dielectric are the best choice due to their high ripple current rating, long lifetime, and performance over the voltage and temperature ranges.

Thermal Feedback

In order to improve the system reliability and lifetime, it is advisable to reduce the LED current at high temperatures, for example starting to reduce the current from 80°C.

The ZXLD1374 offers a simple way to realize a LED temperature thermal feedback using a resistor and NTC connected to the TADJ pin, as shown in the schematic below.

Here a simple procedure to design the thermal feedback circuit in this example:

- 1) Temperature threshold $T_{threshold} = 80^{\circ}C$
- 2) TH1 = 10k Ω @ 25°C and β eta= 3500 \rightarrow TH1 = 1.5k Ω @ 80°C
- 3) $R_6 = TH1 @ T_{threshold} = 1.5k\Omega$

System Diagnostic

The ZXLD1374 has two pins, FLAG and STATUS, which provide the diagnostic function if this is required by the application. In this way, the system can be used in conjunction with a controlling section that can receive the diagnostic signals to protect the system and highlight any unwanted behavior.

The FLAG/STATUS outputs provide a warning of extreme operating or fault conditions. FLAG is an open-drain logic output, which is normally high resistance, but switches low resistance to indicate that a warning, or fault condition exists. STATUS is a DAC output, which is normally high (4.5V), but switches to a lower voltage to indicate the nature of the warning/fault.

For more information about the conditions monitored, the method of detection and the nominal STATUS output voltage it is recommended to refer to the datasheet of the ZXLD1374.

In figure 3, it can be found the true table of the diagnostic section in terms of FLAG voltage and STATUS voltage levels in relation to the different warning/fault conditions.



Fig 3: Status levels and FLAG signal

In the event of more than one fault/warning condition occurring, the higher severity condition will take precedence. E.g. 'Excessive coil current' and 'Out of regulation' occurring together will produce an output of 0.9V on the STATUS pin.

DIMMING

Dimming function, both DC and PWM dimming, can be used to reduce the light output and energy consumption during night hours when the need to have illuminated roads is lower. For example dimming the light output from 100% to 50% from 1am to 6am can be accomplishing using a simple microcontroller.

PWM dimming

The ZXLD1374 has a dedicated PWM dimming input that allows a wide dimming frequency range from 100Hz to 1kHz with 1000:1 resolution; however higher dimming frequencies can be used, at the expense of dimming dynamic range and accuracy.

Typically, for a PWM frequency of 1kHz, the error on the current linearity is lower than 4%; in particular the accuracy is better than 1% for PWM from 5% to 100% (figure 4).



Figure 4: PWM dimming

DC dimming

DC dimming is performed modifying the voltage on the adjust input. When the ADJ pin is connected to REF, it sets the output current to its 100% nominal value.

Driving the ADJ pin with dc voltage (125mV<VADJ< 2.5V) it is possible to adjust output current from 10% to 200% of set value. The ADJ pin has an internal clamp that limits the internal node to less than 3V. This provides some failsafe should they get overdriven.

The following section will graphically display the LED current as a function of the voltage on the ADJ pin.





The dimming function can be also used to compensate for the lost of lumen output of the LEDs with time; this can be realized for example increasing the LED current of 1% every 2 years to compensate of a 10% lost of lumen efficiency over 20 years. It is worth noting that in this case the circuit components need to be designed taking into account the progressive increase in LED current over time.

Bootstrap circuit

The solar street lighting module proposed in this document requires the use of a bootstrap circuit connected to Vaux. The bootstrap circuit will guarantee that the gate of the internal switch is fully enhanced in all conditions.



Figure 6: Bootstrap circuit

The bootstrap circuit is realized adding a reservoir capacitor, C8, able to supply an average 10mA current during the 'turn on' of the internal MOSFET.

In addition of the capacitor C8, a current limiting resistor R13=1k Ω and a blocking diode D2 (DFSL160) are required.

Using a capacitor of 1uF (C8) will guarantee the best trade-off between VAUX supply needs and LED current accuracy. In fact, using 1uF capacitor, VAUX pin supply will require few mA of current from the LED current and only during the first switching cycles. In normal operation the current sunk from the LED current to supply Vaux will be negligible. This will improve the overall current accuracy of the system.

Results

The performances of the systems are outlined in table 1 and in figure 7 and 8.

They display a level of efficiency generally higher than 91% and a current regulation of around 1% over the whole range of the input voltages.

Table 1: Test data

Parameter		Reading		
VIN	Load	Vout	ILED	Efficiency
12	12 LEDs	40V	350mA	91%





Figure 7: LED driver system efficiency



The BOM in table 2 and the PCB layout in figures 9 and 10, complete the tools needed to design a solar street lighting system using the ZXLD1374.

Table 2: BOM						
ltem	Designator	Description	Part N	Manufacturer		
1	U1	Multi – Topology LED driver	ZXLD1374	DIODES Inc		
2	D1	3A – 100V Schottky diode in PowerDI5 package	PSD3100	DIODES Inc		
3	D2	1A – 60V Schottky diode in PowerDI123	DFLS160	DIODES Inc		
4	L1	33uH 4.2A	744770933	Wurth Electronik		
5	C1	100pF COG 0805 capacitor	Generic			
6	C2, C8	1uF – 50V – 1206 Ceramic capacitors	C1206X105K5RAC	KEMET		
7	C3, C10	4.7uF – 50V – 1210 Ceramic capacitors	C1210X475K5RAC	KEMET		
8	R1	0R15 1210 500mW sense resistor	Generic			
9	R6	1.5k Ω 0805 resistor	Generic			
10	R9	120kΩ 0805 resistor	Generic			

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AN74

11	R10	36k Ω 0805 resistor	Generic
12	R13	1k Ω 1206 resistor	Generic
13	TH1	10k Ω NTC 1206 Beta = 3500	Generic

PCB layout



Figure 9: Top Layer



For more considerations on the layout and further support in the design phase please refer to the datasheet and the calculator available on the website www.diodes.com

Conclusion

This application note provides a simple tool to design a solar street lighting system using the ZXLD1374 LED driver. It provides a high level of efficiency as well as LED current control over the whole range of input voltages. Moreover the design note explains how to design a system with thermal feedback, allowing derating of the LED current at high temperature. This feature will allow the designer to get the maximum from a LED lighting system improving the life time of the system and reducing the total cost of ownership. Dimming and Diagnostic functions are also explained to improve system flexibility and reliability on all the conditions a solar street lighting application can encounter.

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