



200 MA SYNCHRONOUS RECTIFIER FEATURING N-MOSFET AND SCHOTTKY DIODE

Product Summary

V _{(BR)DSS}	R _{DS(ON)}	Package	I _D T _A = +25°C
60V	V _{GS} = 10V	SOT-363	200mA

Features

- N-MOSFET with ESD Gate Protection
- N-MOSFET with Low On-Resistance (R_{DS(ON)})
- Low V_f Schottky Diode
- Low Static, Switching, and Conduction Losses
- Good Dynamic Performance
- Surface Mount Package Suited for Automated Assembly
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen- and Antimony-Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please contact us or your local Diodes representative.

https://www.diodes.com/quality/product-definitions/

Description

The NMSD200B01 improves the efficiency and reliability of DC-DC controllers used in Voltage Regulator Modules (VRM) and can support a continuous maximum current of 200mA. It features an ESD protected discrete N-MOSFET with low on-resistance and a discrete Schottky diode with low forward drop.

The device reduces component count, consumes less space, and minimizes parasitic losses. The component devices can be used as a part of a circuit or as a stand-alone discrete device.

Mechanical Data

- Case: SOT-363
- Case Material: Molded Plastic. "Green Molding" Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020D
- Terminal Connections: See Diagram
- Terminals: Finish Matte Tin annealed over Alloy 42 Lead-Frame. Solderable per MIL-STD-202, Method 208 @3
- Weight: 0.006 grams (Approximate)

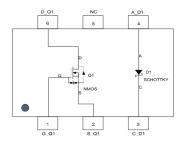
Applications

- Switching Voltage Regulators
- **Power Management Applications**

SOT-363



Top View



Pin Configuration

Ordering Information

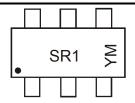
Part Number	Compliance	Case	Reel Size	Quantity per Reel
NMSD200B01-7	Standard	SOT-363	7 Inches	3,000/Tape & Reel

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.



Marking Information



SR1 = Product Type Marking Code, YM = Date Code Marking Y = Year, e.g., H = 2020 M = Month, e.g., 9 = September

Date Code Kev

Year	2010		2019	2020	2021	2022	2023	2024	2025	2026	2027	2028
Code	Х		G	Н	I	J	K	L	М	N	0	Р
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec

Maximum Ratings @ TA = 25°C unless otherwise specified

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 5)	P_d	200	mW
Output Current	l _{out}	200	mA

Maximum Ratings (@ $T_A = +25^{\circ}C$, unless otherwise specified.)

Sub-Component Device: ESD Protected N-Channel MOSFET (Q1)

Characteri	stic	Symbol	Value	Unit
Drain Source Voltage		V_{DSS}	60	V
Drain Gate Voltage (RGS <+ 1MOhm)		V_{DGR}	60	V
Gate Source Voltage	Continuous	V _{GSS}	+/-20	V
Drain Current (Note 5)	Continuous (V _{gs} =10V)	I _D	200	mA

Maximum Ratings @ T_A = 25°C unless otherwise specified

Sub-Component Device: Schottky Diode (D1)

Characteristic	Symbol	Value	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V _{RRM} V _{RWM} VR	40	٧
RMS Reverse Voltage	V _{R(RMS)}	28	>
Forward Continuous Current (Note 5)	I _{FM}	350	mA
Non-Repetitive Peak Forward Surge Current @ t<1.0 s	I _{FSM}	1.5	Α

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Junction Operation and Storage Temperature Range	T _j , T _{stg}	-55 to +150	°C
Thermal Resistance, Junction to Ambient Air (Note 5) (Equivalent to one heated junction of N-MOSFET)	$R_{ heta}$ JA	625	°C/W

Note: 5. Device mounted on FR-4 PCB, 1 inch square, 2oz copper pad area.



Electrical Characteristics @ T_A = 25°C unless otherwise specified

Sub-Component Device: ESD Protected N-Channel MOSFET (Q1)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 6)						
Drain-Source Breakdown Voltage, BV _{DSS}	V _{BR(DSS)}	60			V	$V_{GS} = 0V, I_D = 10\mu A$
Zero Gate Voltage Drain Current (Drain Leakage Current)	I _{DSS}	_	_	1	μA	$V_{GS} = 0V, V_{DS} = 60V$
Gate Body Leakage Current, Forward	I _{GSSF}	_	_	10	μA	V _{GS} = 20V, V _{DS} = 0V
Gate Body Leakage Current, Reverse	I _{GSSR}	_	_	-10	μΑ	$V_{GS} = -20 \text{ V}, V_{DS} = 0 \text{V}$
ON CHARACTERISTICS (Note 6)						
Cata Cauraa Throphald Valtaga (Cantral Cupply Valtaga)		1	1.6	2.5	V	$V_{DS} = V_{GS} = 10V, I_{D} = 0.25mA$
Gate Source Threshold Voltage (Control Supply Voltage)	V _{GS(th)}	1.1	1.8	3	V	$V_{DS} = V_{GS} = 10V$, $I_{D} = 1mA$
Static Drain Source On State Voltage		_	0.09	1.5	V	V_{GS} = 5V, I_D = 50mA
Static Drain-Source On-State Voltage	V _{DS(on)}	_	0.62	1.25	V	V _{GS} = 10V, I _D = 500mA
Static Drain-Source On Resistance	R _{DS (on)}	_	1.6	3	Ω	V_{GS} = 5V, I_D = 50mA
Static Dialii-Source Off Resistance		_	1.5	2	12	V _{GS} = 10V, I _D = 500mA
Forward Transconductance	g FS	80	420	_	mS	$V_{DS} >= 2*V_{DS(ON)}$, $I_D = 200 \text{mA}$
Dynamic Characteristics						
Input Capacitance	C _{iss}			50	pF	
Output Capacitance	Coss		_	25	pF	V _{DS} = 25V, V _{GS} = 0V, f = 1MHz
Reverse Transfer Capacitance	C _{rss}	_	_	5	pF	1111112
Switching Characteristics						
Turn-On Delay Time	t _{d(on)}			20	ns	_
Turn-Off Delay Time	t _{d(off)}			40	ns	_
Drain-Source (Body) Diode Characteristics and Maximum I	Ratings					
Drain-Source Diode Forward On-Voltage	V _{SD}	_	0.88	1.5	V	$V_{GS} = 0V$, $I_S = 300 \text{ mA}$

Electrical Characteristics @ T_A = 25°C unless otherwise specified

Sub-Component Device: Schottky Barrier Diode (D1)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
Reverse Breakdown Voltage (Note 6)	$V_{(BR)R}$	40	_	_	V	I _R = 10μA
Forward Voltage Drop (Note 6)	V_{FM}	_	_	0.37	V	I _F =20mA
Forward Voltage Drop (Note 6)		_	_	0.6	V	I _F =200mA
Peak Reverse Current (Note 6)	I _{RM}	_	_	5	μA	V _R = 30V
Total Capacitance	C _T		39	_	pF	V _R = 0V, f = 1.0 MHz
Reverse Recovery Time	t _{rr}		10	_	ns	$I_F=I_R=30$ mA, $I_{rr}=0.1$ x I_R , $R_L=100$ Ω

Note: 6. Short duration pulse test used to minimize self-heating effect.



Typical Characteristics

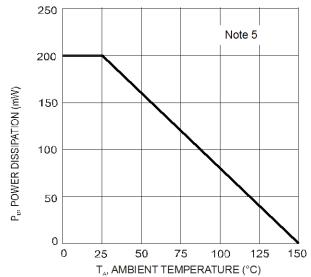
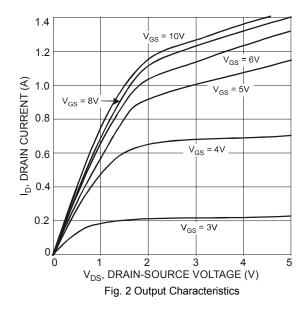
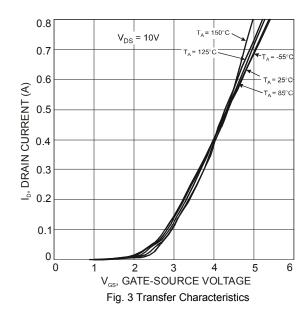


Fig. 1 Max Power Dissipation vs. Ambient Temperature

Typical N-Channel Characteristics

Sub-Component Device: MOSFET-Q1 (ESD Protected)







Typical N-Channel Characteristics (cont.)

Sub-Component Device: MOSFET-Q1 (ESD Protected)

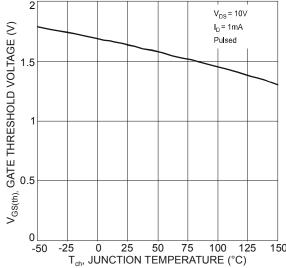


Fig. 4 Gate Threshold Voltage vs. Junction Temperature

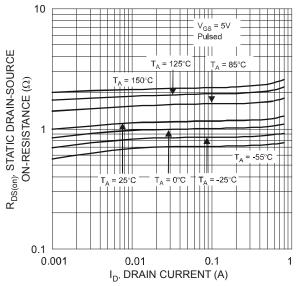


Fig. 6 Static Drain-Source On-Resistance vs. Drain Current

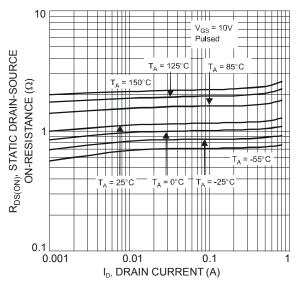


Fig. 5 Static Drain-Source On-Resistance vs. Drain Current

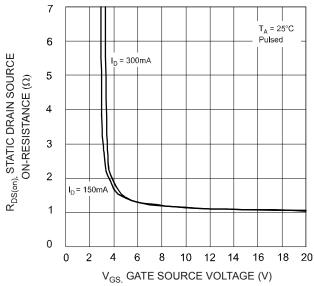


Fig. 7 Static Drain-Source On-Resistance vs. Gate-Source Voltage



Typical N-Channel Characteristics (cont.)

Sub-Component Device: MOSFET-Q1 (ESD Protected)

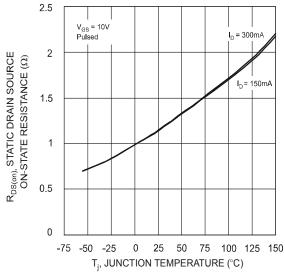


Fig. 8 Static Drain-Source On-State Resistance vs. Junction Temperature

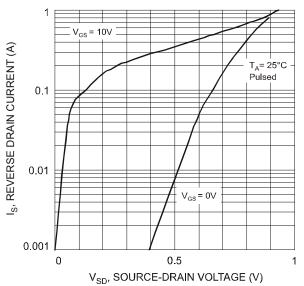


Fig. 10 Reverse Drain Current vs. Body Diode Forward Voltage

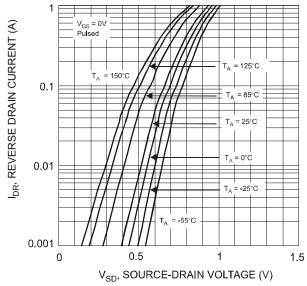


Fig. 9 Reverse Drain Current vs. Source-Drain Voltage

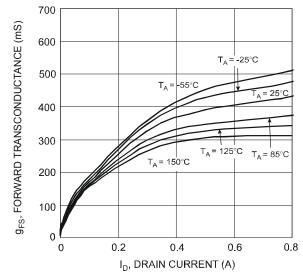
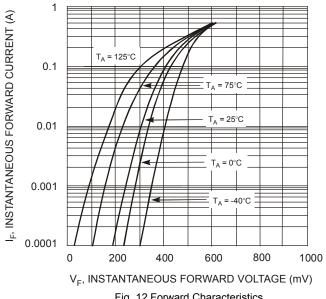


Fig. 11 Forward Transconductance vs. Drain Current (V_{DS}>I_D *R_{DS(ON)})



Typical Characteristics

Sub-Component Device: Schottky Barrier Diode (D1)



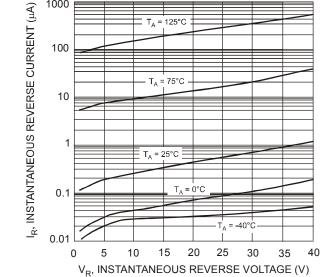


Fig. 13 Reverse Characteristics

T_A = 125°C

1000

100

Fig. 12 Forward Characteristics

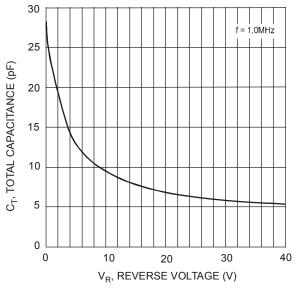
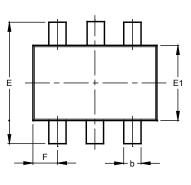


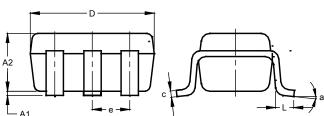
Fig. 14 Total Capacitance vs. Reverse Voltage



Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.





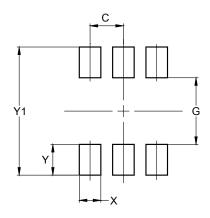
SOT363						
Dim	Min	Max	Тур			
A1	0.00	0.10	0.05			
A2	0.90	1.00	0.95			
b	0.10	0.30	0.25			
С	0.10	0.22	0.11			
D	1.80	2.20	2.15			
Е	2.00	2.20	2.10			
E1	1.15	1.35	1.30			
е	e 0.650 BSC					
F	0.40	0.45	0.425			
L	0.25	0.40	0.30			
а	0°	8°				
All I	Dimen	sions	in mm			

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT363

SOT363



Dimensions	Value
Dillielisiolis	(in mm)
С	0.650
G	1.300
Х	0.420
Y	0.600
Y1	2.500



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