



SBR10U200P5Q

10A SBR SUPER BARRIER RECTIFIER PowerDI5

Product Summary (@ TA = +25°C)

| V _{RRM} (V) | lo (A) | V _F Max (V) @ +25°C | I _R Max (mA) @ +25°C |
|----------------------|--------|-----------------------------------|------------------------------------|
| 200 | 10 | 0.88 | 0.1 |

Description & Applications

Packaged in the compact thermally efficient PowerDl 8 5 package, provides low V_F and low reverse leakage at high temperatures.

It is ideal for use in the following applications:

- Bridge diodes
- Freewheeling diodes
- Blocking diodes
- · Reverse protection diodes

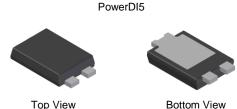
Features and Benefits

- Ultra Low-Forward Voltage Drop
- Excellent High Temperature Stability
- Patented Super Barrier Rectifier (SBR[®]) Technology
- Soft, Fast Switching Capability
- +175°C Operating Junction Temperature
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- The SBR10U200P5Q is suitable for automotive applications requiring specific change control; this part is AEC-Q101 qualified, PPAP capable, and manufactured in IATF 16949 certified facilities.

https://www.diodes.com/quality/product-definitions/

Mechanical Data

- Package: PowerDI5
- Package Material: Molded Plastic, "Green" Molding Compound;
 UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Matte Tin Finish Annealed over Copper Leadframe;
 Solderable per MIL-STD-202, Method 208 ®3
- Polarity: See Diagram
- Weight: 0.093 grams (Approximate)





Note: Pins Left & Right must be electrically connected at the printed circuit board.

Ordering Information (Note 4)

| Part Number | Paakaga | Packing | |
|------------------|----------|---------|-------------|
| Fait Number | Package | Qty. | Carrier |
| SBR10U200P5Q-13 | PowerDI5 | 5,000 | Tape & Reel |
| SBR10U200P5Q-13D | PowerDI5 | 5 000 | Tape & Reel |

Notes:

- 1. EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. All applicable RoHS exemptions applied.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information



S10U200 = Product Type Marking Code

Oli = Manufacturer's Code Marking

YYWW = Date Code Marking

YY = Last Two Digits of Year (ex: 23 for 2023)

WW = Week Code (01 to 53)

K = Factory Designator



Maximum Ratings (@ $T_A = +25^{\circ}C$, unless otherwise specified.)

Single phase, half wave, 60Hz, resistive or inductive load. For capacitive load, derate current by 20%.

| Characteristic | Symbol | Value | Unit |
|---|---------------------|-------|----------|
| Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage | VRRM VRWM VRM | 200 | V |
| Average Rectified Output Current | lo | 10 | Α |
| Non-Repetitive Peak Forward Surge Current 8.3ms Single Half Sine-Wave Superimposed on Rated Load | IFSM | 180 | А |
| Repetitive Peak Avalanche Power (1µs, +25°C) | PARM | 3,000 | W |

Thermal Characteristics (Note 9)

| Characteristic | | Symbol | Value | Unit |
|---|--|-----------------|---------------------|------|
| Typical Thermal Resistance Junction to Ambient (Note 5) | | $R_{\theta JA}$ | 70 | °C/W |
| Typical Thermal Resistance Junction to Case (Note 5) | | Rejc | 14 | °C/W |
| Typical Thermal Resistance Junction to Ambient (Note 6) | | RθJA | 20 | °C/W |
| Typical Thermal Resistance Junction to Case (Note 6) | | $R_{\theta JC}$ | 3 | °C/W |
| Operating Temperature Range Reverse Mode DC Forward Mode (Note 7) | | TJ | -65 to +175 ≤200 | °C |
| Storage Temperature Range | | Tstg | -65 to +175 | °C |

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

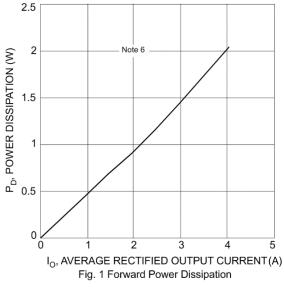
| Characteristic | Symbol | Min | Тур | Max | Unit | Test Condition |
|--------------------------|-----------------|-----|------|------|------|--|
| | | _ | 0.75 | 0.82 | | IF = 5A, T _J = +25°C |
| Forward Voltage Drop | VF | _ | 0.62 | 0.67 | V | IF = 5A, T _J = +125°C |
| | | _ | 0.83 | 0.88 | | I _F = 10A, T _J = +25°C |
| | | _ | _ | 0.8 | mA | V _R = 100V, T _J = +125°C |
| | | _ | _ | 10 | μA | V _R = 150V, T _J = +25°C |
| Leakage Current (Note 8) | IR | _ | _ | 4.5 | mA | V _R = 150V. T _J = +125°C |
| , , | | _ | | 20 | μA | V _R = 200V, T _J = +25°C |
| | | _ | 0.18 | 10 | mA | V _R = 200V, T _J = +125°C |
| Switching Speed | t _{RR} | | 19 | | ns | $I_F = 0.5A, I_R = 1A,$ |
| Ownerming Opeca | IKK | | 19 | | 113 | I _{RR} = 0.25A (RG1) |

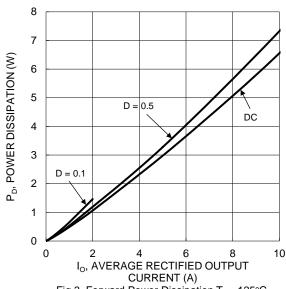
Notes:

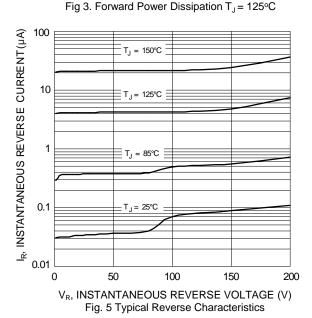
- 5. Device mounted on FR-4 PCB with minimum recommended pad layout per http://www.diodes.com/package-outlines.html.
- Device mounted on FR-4 PCB with 1-inch pad layout and additional HK2 (45mm x 20mm x12mm).
 Max junction temperature guaranteed for 2 hours.
 Short duration pulse test used to minimize self-heating effect.

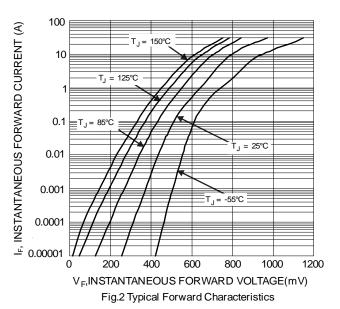
- 9. The heat generated must be less than thermal conductivity from junction-to-ambient: $dP_D/dT_J < 1/R_{\theta JA}$.

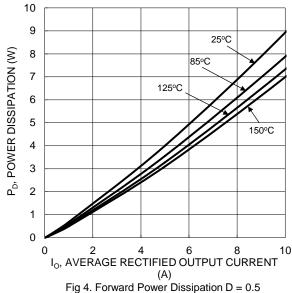


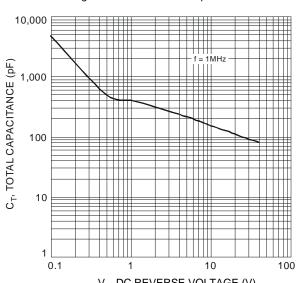






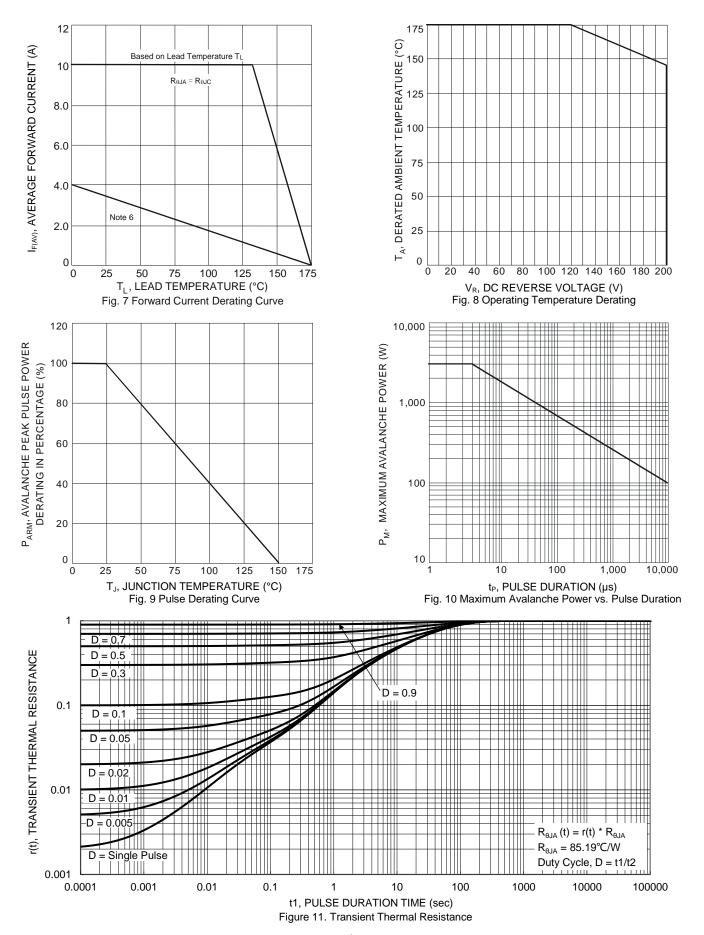






 V_R , DC REVERSE VOLTAGE (V) Fig. 6 Total Capacitance vs. Reverse Voltage



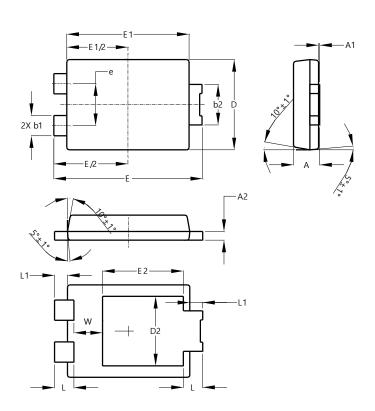




Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

PowerDI5

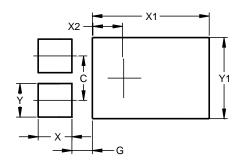


| PowerDI5 | | | | | |
|----------------------|------|------|-------|--|--|
| Dim | Min | Max | Тур | | |
| Α | 1.05 | 1.15 | 1.10 | | |
| A1 | 0.00 | 0.05 | | | |
| A2 | 0.33 | 0.43 | 0.381 | | |
| b1 | 0.80 | 0.99 | 0.89 | | |
| b2 | 1.70 | 1.88 | 1.78 | | |
| D | 3.90 | 4.05 | 3.966 | | |
| D2 | | | 3.054 | | |
| Е | 6.40 | 6.60 | 6.51 | | |
| е | | | 1.84 | | |
| E1 | 5.30 | 5.45 | 5.37 | | |
| E2 | | | 3.549 | | |
| L | 0.75 | 0.95 | 0.85 | | |
| L1 | 0.50 | 0.65 | 0.57 | | |
| W | 1.10 | 1.41 | 1.255 | | |
| All Dimensions in mm | | | | | |

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

PowerDI5



| Dimensions | Value (in mm) | | |
|------------|---------------|--|--|
| С | 1.840 | | |
| G | 0.852 | | |
| Х | 1.400 | | |
| X1 | 4.860 | | |
| X2 | 1.310 | | |
| Y | 1.390 | | |
| V1 | 2 260 | | |



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