



60V +175°C P-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

BV _{DSS}	R _{DS(ON)} Max	I _D Max T _A = +25°C
	155mΩ @ V _{GS} = -10V	-2.4A
-60V	240mΩ @ V _{GS} = -4.5V	-1.9A

Features and Benefits

- Rated to +175°C Ideal for High Ambient Temperature Environments
- Low On-Resistance
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- This part is qualified to JEDEC standards (as references in AEC-Q) for High Reliability.
 - https://www.diodes.com/quality/product-definitions/
- An automotive-compliant part is available under separate datasheet (DMPH6250SQ)

Description and Applications

This MOSFET is designed to minimize the on-state resistance (R_{DS(ON)}) yet maintain superior switching performance, making it ideal for high-efficiency power-management applications.

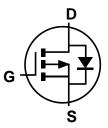
- · Battery charging
- Power-management functions
- DC-DC converters
- Load switches

Mechanical Data

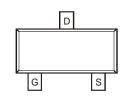
- Package: SOT23
- Package Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Annealed over Copper Leadframe.
 Solderable per MIL-STD-202, Method 208 (3)
- Terminals Connections: See Diagram Below
- Weight: 0.008 grams (Approximate)







Internal Schematic



Top View

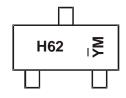
Ordering Information (Note 4)

Part Number	art Number Package		Packing			
Part Number	Package	Qty.	Carrier			
DMPH6250S-7	SOT23	3000	Tape & Reel			
DMPH6250S-13	SOT23	10000	Tape & Reel			

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- $4. For packaging details, go to our website at \ https://www.diodes.com/design/support/packaging/diodes-packaging/.$

Marking Information



H62 = Product Type Marking Code $\overline{Y}M$ = Date Code Marking \overline{Y} = Year (ex: L = 2024) M = Month (ex: 9 = September)

Date Code Key

2410 0040 110)												
Year	2018	-	2024	2025	2026	2027	2028	2029	2030	2031	2032	2033
Code	F	-	L	М	Ν	Р	R	S	Т	U	V	W
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code											N	



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit		
Drain-Source Voltage			VDSS	-60	V
Gate-Source Voltage			V_{GSS}	±20	V
Continuous Drain Current (Note 6) Vcs = -10V 1 - 1		T _A = +25°C T _A = +100°C	lσ	-2.4 -1.5	А
Pulsed Drain Current (380µs Pulse, Duty Cycle = 1%)			I _{DM}	-13	A
Maximum Continuous Body Diode Forward Current (Note 6)			ls	-1.6	A
Pulsed Body Diode Forward Current (380µs Pulse, Duty Cycle = 1%)			Ism	-13	A
Avalanche Current, L = 0.1mH			I _{AS}	-12	A
Avalanche Energy, L = 0.1mH			Eas	8	mJ

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 5)	PD	0.92	W
Thermal Resistance, Junction to Ambient @T _A = +25°C (Note 5)	$R_{ heta JA}$	165	°C/W
Power Dissipation (Note 6)	PD	1.62	W
Thermal Resistance, Junction to Ambient @T _A = +25°C (Note 6)	$R_{\theta JA}$	93.1	°C/W
Operating and Storage Temperature Range	TJ, TSTG	-55 to +175	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

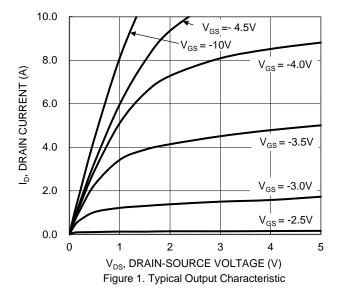
Characteristic	Symbol	Min	Tvn	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)	Syllibol	IVIIII	Тур	IVIAA	Uiiit	Test Condition	
Drain-Source Breakdown Voltage	BVpss	-60	_	_	V	Vgs = 0V, ID = -250µA	
Zero Gate Voltage Drain Current T _J = +25°C	Ipss			-1.0	μA	$V_{DS} = -60V, V_{GS} = 0V$	
Gate-Source Leakage	Igss	_	_	±100	nA	Vgs = ±20V, Vps = 0V	
ON CHARACTERISTICS (Note 7)			J			, 20	
Gate Threshold Voltage	Vgs(TH)	-1.0	-1.9	-3.0	V	$V_{DS} = V_{GS}$, $I_D = -250\mu A$	
Static Drain-Source On-Resistance	_	_	112	155	0	Vgs = -10V, ID = -2A	
Static Drain-Source On-Resistance	R _{DS(ON)}	_	149	240	mΩ	V _G S = -4.5V, I _D = -2A	
Diode Forward Voltage	VsD	_	-0.8	-1.2	V	V _G S = 0V, I _S = -1A	
DYNAMIC CHARACTERISTICS (Note 8)							
Input Capacitance	Ciss	_	512		рF), 20),), (2), (3), (3), (4)	
Output Capacitance	Coss	_	31.3		рF	V _{DS} = -30V, V _{GS} = 0V, - f = 1.0MHz	
Reverse Transfer Capacitance	Crss	_	23.2	_	рF	1 – 1.0101112	
Gate Resistance	Rg	_	11.9	_	Ω	$V_{DS} = 0V$, $V_{GS} = 0V$, $f = 1MHz$	
Total Gate Charge (V _{GS} = -4.5V)	Q_g	_	4.0		nC		
Total Gate Charge (Vgs = -10V)	Qg	_	8.3	_	nC	Vps = -30V. lp = -2A	
Gate-Source Charge	Qgs	_	1.2		nC	VDS = -30V, ID = -2A	
Gate-Drain Charge	Qgd	_	1.7		nC		
Turn-On Delay Time	tD(ON)	_	12.5	_	ns		
Turn-On Rise Time	t _R	_	13.4	_	ns	$V_{DD} = -30V, V_{GS} = -10V,$	
Turn-Off Delay Time	tD(OFF)	_	96.0	_	ns	$I_D = -1.0A, R_G = 50\Omega$	
Turn-Off Fall Time	t _F	_	39.1	_	ns		
Body Diode Reverse Recovery Time	trr	_	9.6	_	ns	I _F = -1A, di/dt = 100A/μs	
Body Diode Reverse Recovery Charge	Q _{RR}	_	3.1	_	nC	I _F = -1A, di/dt = 100A/μs	

5. Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.

^{6.} Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.
7. Short duration pulse test used to minimize self-heating effect.

^{8.} Guaranteed by design. Not subject to product testing.





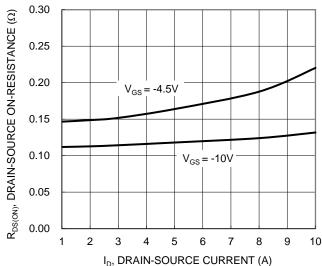


Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage

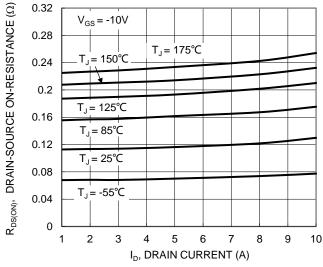
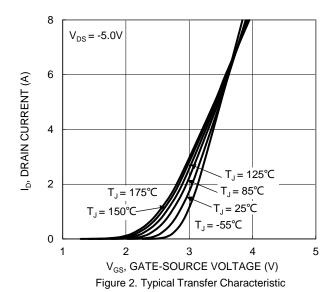


Figure 5. Typical On-Resistance vs. Drain Current and Temperature



1.6 1.4 1.2

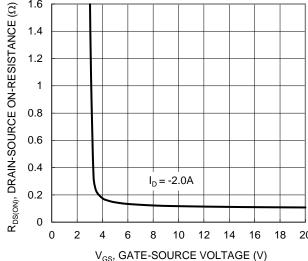


Figure 4. Typical Transfer Characteristic

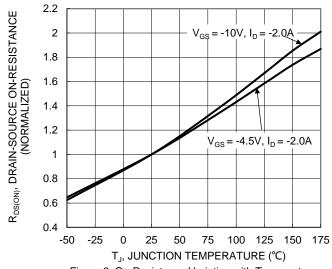


Figure 6. On-Resistance Variation with Temperature



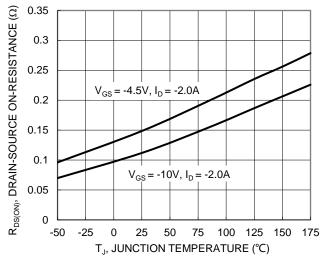
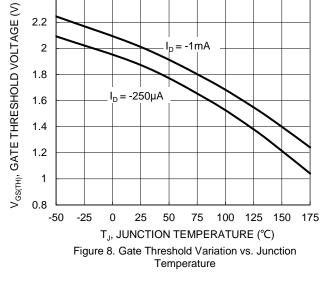


Figure 7. On-Resistance Variation with Temperature



2.4

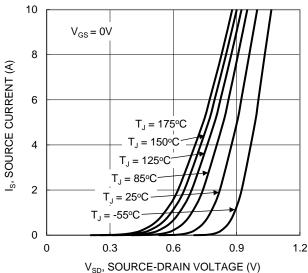
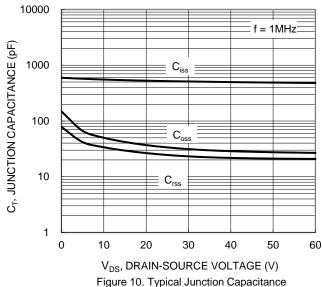


Figure 9. Diode Forward Voltage vs. Current



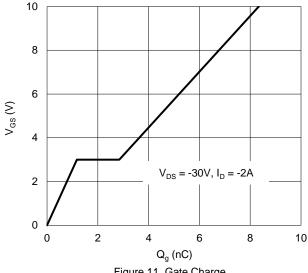


Figure 11. Gate Charge

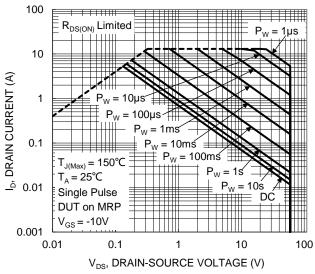


Figure 12. SOA, Safe Operation Area



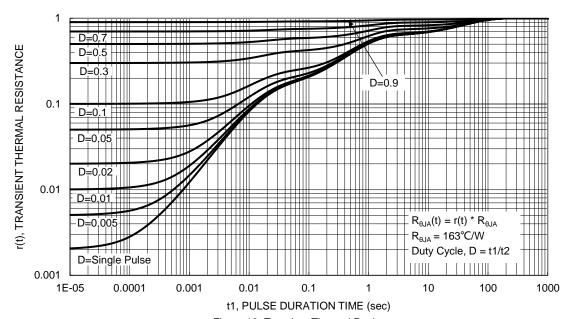


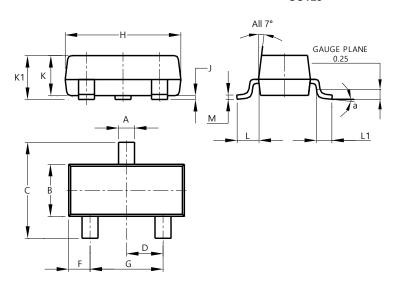
Figure 13. Transient Thermal Resistance



Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT23

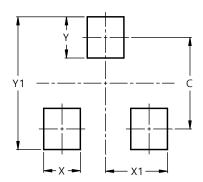


SOT23							
Dim	Min	Max	Тур				
Α	0.37	0.51	0.40				
В	1.20	1.40	1.30				
С	2.30	2.50	2.40				
D	0.89	1.03	0.915				
F	0.45	0.60	0.535				
G	1.78	2.05	1.83				
Н	2.80	3.00	2.90				
J	0.013	0.10	0.05				
K	0.890	1.00	0.975				
K1	0.903	1.10	1.025				
L	0.45	0.61	0.55				
L1	0.25	0.55	0.40				
М	0.085	0.150	0.110				
а	0°	8°					
All Dimensions in mm							

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT23



Dimensions	Value (in mm)			
С	2.0			
X	0.8			
X1	1.35			
Y	0.9			
Y1	2.9			

March 2024



IMPORTANT NOTICE

- DIODES INCORPORATED (Diodes) AND ITS SUBSIDIARIES MAKE NO WARRANTY OF ANY KIND, EXPRESS OR IMPLIED, WITH REGARDS TO ANY INFORMATION CONTAINED IN THIS DOCUMENT, INCLUDING, BUT NOT LIMITED TO, THE IMPLIED WARRANTIES OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE OR NON-INFRINGEMENT OF THIRD PARTY INTELLECTUAL PROPERTY RIGHTS (AND THEIR EQUIVALENTS UNDER THE LAWS OF ANY JURISDICTION).
- The Information contained herein is for informational purpose only and is provided only to illustrate the operation of Diodes' products described herein and application examples. Diodes does not assume any liability arising out of the application or use of this document or any product described herein. This document is intended for skilled and technically trained engineering customers and users who design with Diodes' products. Diodes' products may be used to facilitate safety-related applications; however, in all instances customers and users are responsible for (a) selecting the appropriate Diodes products for their applications, (b) evaluating the suitability of Diodes' products for their intended applications, (c) ensuring their applications, which incorporate Diodes' products, comply the applicable legal and regulatory requirements as well as safety and functional-safety related standards, and (d) ensuring they design with appropriate safeguards (including testing, validation, quality control techniques, redundancy, malfunction prevention, and appropriate treatment for aging degradation) to minimize the risks associated with their applications.
- Diodes assumes no liability for any application-related information, support, assistance or feedback that may be provided by Diodes from time to time. Any customer or user of this document or products described herein will assume all risks and liabilities associated with such use, and will hold Diodes and all companies whose products are represented herein or on Diodes' websites, harmless against all damages and
- Products described herein may be covered by one or more United States, international or foreign patents and pending patent applications. Product names and markings noted herein may also be covered by one or more United States, international or foreign trademarks and trademark applications. Diodes does not convey any license under any of its intellectual property rights or the rights of any third parties (including third parties whose products and services may be described in this document or on Diodes' website) under this document.
- Diodes' provided to Diodes' Standard Terms and Conditions of Sale products subject (https://www.diodes.com/about/company/terms-and-conditions/terms-and-conditions-of-sales/) or other applicable terms. This document does not alter or expand the applicable warranties provided by Diodes. Diodes does not warrant or accept any liability whatsoever in respect of any products purchased through unauthorized sales channel.
- Diodes' products and technology may not be used for or incorporated into any products or systems whose manufacture, use or sale is prohibited under any applicable laws and regulations. Should customers or users use Diodes' products in contravention of any applicable laws or regulations, or for any unintended or unauthorized application, customers and users will (a) be solely responsible for any damages, losses or penalties arising in connection therewith or as a result thereof, and (b) indemnify and hold Diodes and its representatives and agents harmless against any and all claims, damages, expenses, and attorney fees arising out of, directly or indirectly, any claim relating to any noncompliance with the applicable laws and regulations, as well as any unintended or unauthorized application.
- While efforts have been made to ensure the information contained in this document is accurate, complete and current, it may contain technical inaccuracies, omissions and typographical errors. Diodes does not warrant that information contained in this document is error-free and Diodes is under no obligation to update or otherwise correct this information. Notwithstanding the foregoing. Diodes reserves the right to make modifications, enhancements, improvements, corrections or other changes without further notice to this document and any product described herein. This document is written in English but may be translated into multiple languages for reference. Only the English version of this document is the final and determinative format released by Diodes.
- Any unauthorized copying, modification, distribution, transmission, display or other use of this document (or any portion hereof) is prohibited. Diodes assumes no responsibility for any losses incurred by the customers or users or any third parties arising from any such unauthorized use.
- 9. This Notice may be periodically updated with the most recent version available at https://www.diodes.com/about/company/terms-andconditions/important-notice

The Diodes logo is a registered trademark of Diodes Incorporated in the United States and other countries. All other trademarks are the property of their respective owners. © 2024 Diodes Incorporated. All Rights Reserved.

www.diodes.com