



### 40V P-CHANNEL ENHANCEMENT MODE MOSFET PowerDI5060-8

### **Product Summary**

BV <sub>DSS</sub>	RDS(ON) Max	I <sub>D</sub> T <sub>A</sub> = +25°C
-40V	11mΩ @ V <sub>GS</sub> = -10V	-11A
-407	$15m\Omega @ V_{GS} = -4.5V$	-10A

# **Description and Applications**

This new generation MOSFET is designed to minimize the on-state resistance (R<sub>DS(ON)</sub>) yet maintain superior switching performance, making it ideal for high-efficiency power management applications.

- DC-DC converters
- Power management functions
- Analog switches

## **Features and Benefits**

- 100% Unclamped Inductive Switch (UIS) Test In Production
- Low On-Resistance
- · Fast Switching Speed
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e.: parts qualified to AEC-Q100/101/104/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please refer to the related automotive grade (Q-suffix) part. A listing can be found at

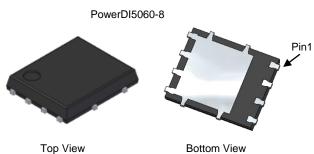
https://www.diodes.com/products/automotive/automotive-products/.

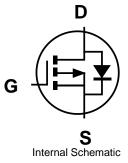
- This part is qualified to JEDEC standards (as references in AEC-Q) for High Reliability.
  - https://www.diodes.com/quality/product-definitions/
- An Automotive-Compliant Part is Available Under Separate Datasheet (DMP4015SPSQ)

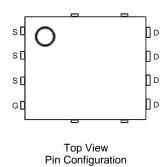
### **Mechanical Data**

- Package: PowerDI<sup>®</sup>5060-8
- Package Material: Molded Plastic, "Green" Molding Compound.
   UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Diagram
- Terminals: Finish 100% Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 (§3)
- Weight: 0.097 grams (Approximate)

Site1:





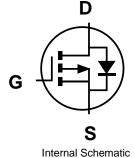


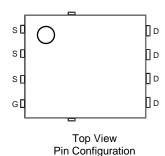
Site2:

PowerDI5060-8 (SWP) (Type UX)









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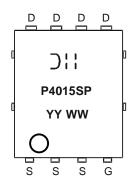
## Ordering Information (Note 4)

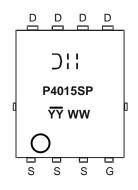
Part Number	Package	Packing		
Fait Nullibei	Fackage	Qty.	Carrier	
DMP4015SPS-13	PowerDI5060-8	2,500	Reel	
DMP4015SPS-13	PowerDI5060-8 (SWP) (Type UX)	2,500	Reel	

Notes:

- 1. EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. All applicable RoHS exemptions applied.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/

## **Marking Information**





☐ !! = Manufacturer's Marking P4015SP = Product Type Marking Code YYWW or YYWW = Date Code Marking YY or  $\overline{YY}$ = Year (ex: 22 = 2022) WW = Week (01 to 53)

## **Maximum Ratings** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Units		
Drain-Source Voltage	VDSS	-40	V		
Gate-Source Voltage	V <sub>GSS</sub>	±25	V		
Steady State		T <sub>A</sub> = +25°C T <sub>A</sub> = +70°C	lD	-8.5 -6.8	А
Continuous Drain Current (Note 5) V <sub>GS</sub> = -10V	t<10s	T <sub>A</sub> = +25°C T <sub>A</sub> = +70°C	ID	-13 -10.5	А
Continuous Dusin Courset (Nata C) V 40V	Steady State	T <sub>A</sub> = +25°C T <sub>A</sub> = +70°C	ID	-11 -8.7	А
Continuous Drain Current (Note 6) V <sub>GS</sub> = -10V	t<10s	$T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$	I <sub>D</sub>	-17 -13.5	А
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)	IDM	-100	Α		
Maximum Body Diode Continuous Current (Note 6)			Is	-11	A
Avalanche Current L = 1mH			las	-22	Α
Avalanche Energy L = 1mH			Eas	242	mJ

### Thermal Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Units	
Total Dawer Dissination (Note 5)	T <sub>A</sub> = +25°C	6	1.3	W
Total Power Dissipation (Note 5)	T <sub>A</sub> = +70°C	PD	0.8	
Thermal Desistance Investigate Ambient (Nets 5)	Steady state	D	96.4	°C/W
Thermal Resistance, Junction to Ambient (Note 5)	t<10s	Reja	40.6	°C/W
Total Power Dissipation (Note 6)	T <sub>A</sub> = +25°C	- P <sub>D</sub>	2.1	W
Total Fower Dissipation (Note 6)	T <sub>A</sub> = +70°C		1.4	
Thormal Basistanas, Junation to Ambient (Note 6)	Steady state	Davi	49	°C/W
Thermal Resistance, Junction to Ambient (Note 6)	t<10s	Reja	24	°C/W
Thermal Resistance, Junction to Case (Note 7)		R <sub>0</sub> JC	1.6	°C/W
Operating and Storage Temperature Range		TJ, TSTG	-55 to +150	°C

Notes:

- 5. Device mounted on FR-4 PC board, with minimum recommended pad layout, single sided.
- 6. Device mounted on FR-4 substrate PC board, 2oz copper, with thermal bias to bottom layer 1inch square copper plate.

7. Thermal resistance from junction to soldering point (on the exposed drain pad).



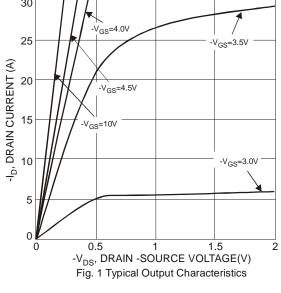
## **Electrical Characteristics** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

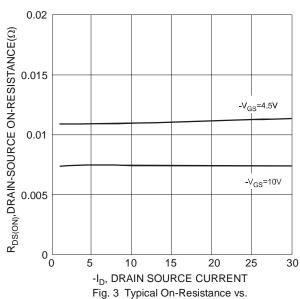
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 8)				•	•	•	
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	-40	_	_	V	$V_{GS} = 0V, I_{D} = -250\mu A$	
Zero Gate Voltage Drain Current	IDSS		_	-1	μΑ	$V_{DS} = -40V$ , $V_{GS} = 0V$	
Gate-Source Leakage	I <sub>GSS</sub>	_	_	±100	nA	$V_{GS} = \pm 25V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 8)	ON CHARACTERISTICS (Note 8)						
Gate Threshold Voltage	V <sub>GS(th)</sub>	-1.5	-2	-2.5	V	$V_{DS} = V_{GS}$ , $I_D = -250\mu A$	
Static Drain-Source On-Resistance	Dagger		7	11	mΩ	$V_{GS} = -10V, I_{D} = -9.8A$	
Static Dialii-Source Oil-Resistance	RDS(ON)		9	15	11122	$V_{GS} = -4.5V, I_{D} = -9.8A$	
Forward Transfer Admittance	Y <sub>fs</sub>	_	26	_	S	$V_{DS} = -20V, I_{D} = -9.8A$	
Diode Forward Voltage	VsD	_	-0.7	-1	V	Vgs = 0V, Is = -1A	
DYNAMIC CHARACTERISTICS (Note 9)							
Input Capacitance	Ciss		4,234	_		$V_{DS} = -20V$ , $V_{GS} = 0V$ f = 1MHz	
Output Capacitance	Coss		1,036	_	pF		
Reverse Transfer Capacitance	Crss	_	526	_			
Gate Resistance	Rg	_	7.77	_	Ω	$V_{DS} = 0V$ , $V_{GS} = 0V$ , $f = 1MHz$	
Total Gate Charge	Qg	_	47.5	_		V 00V/V 5V	
Gate-Source Charge	Qgs	_	14.2	_	nC	$V_{DS} = -20V, V_{GS} = -5V$ $I_{D} = -9.8A$	
Gate-Drain Charge	Qgd	_	13.5	_			
Turn-On Delay Time	t <sub>D(on)</sub>	_	13.2	_		$V_{GS} = -10V$ , $V_{DD} = -20V$ , $R_{G} = 6\Omega$ ,	
Turn-On Rise Time	tr	_	10	_			
Turn-Off Delay Time	t <sub>D(off)</sub>		302.7	_	ns	$I_D = -1A$ , $R_L = 20\Omega$	
Turn-Off Fall Time	tf	_	137.9	_			

Notes:

<sup>8.</sup> Short duration pulse test used to minimize self-heating effect.
9. Guaranteed by design. Not subject to production testing.







Drain Current and Gate Voltage

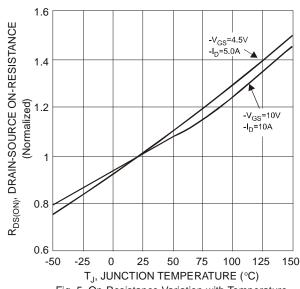
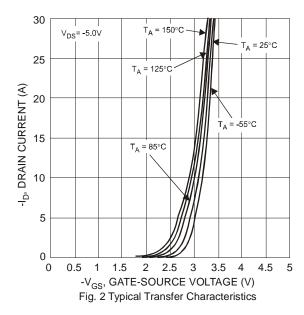
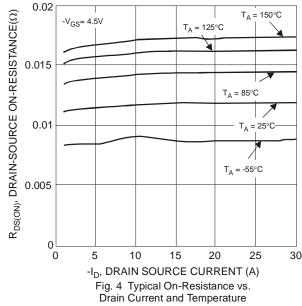
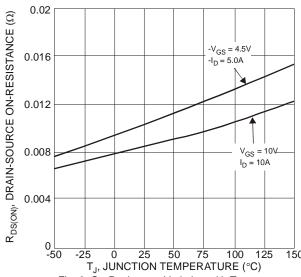


Fig. 5 On-Resistance Variation with Temperature









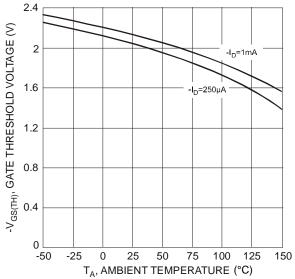
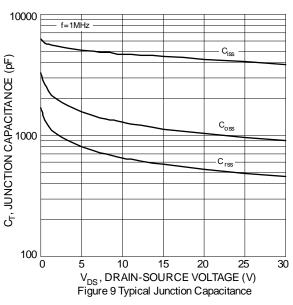
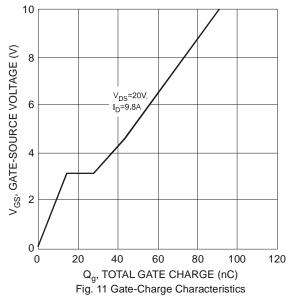
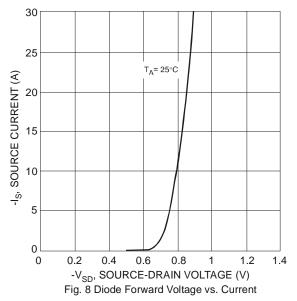


Fig. 7 Gate Threshold Variation vs. Ambient Temperature







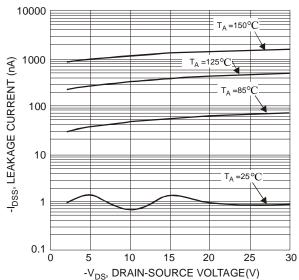


Fig. 10 Typical Drain-Source Leakage Current vs. Voltage

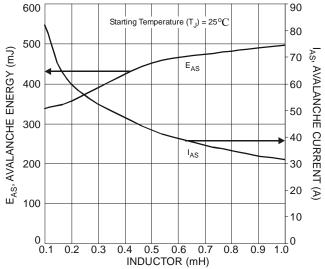
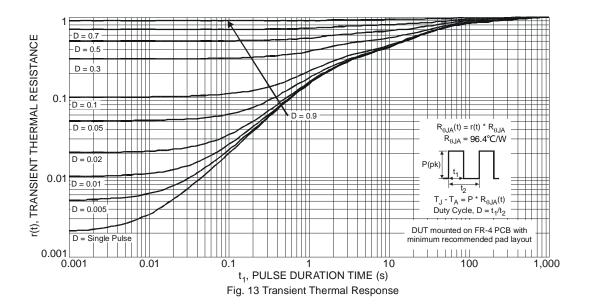


Fig. 12 Single-Pulse Avalanche Tested



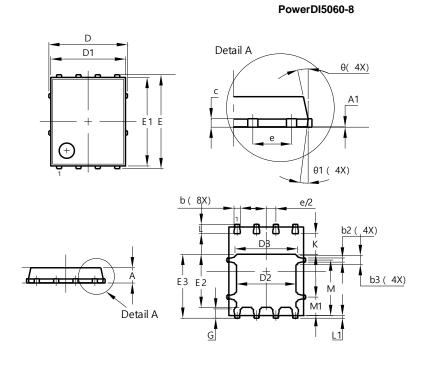




## **Package Outline Dimensions**

Please see http://www.diodes.com/package-outlines.html for the latest version.

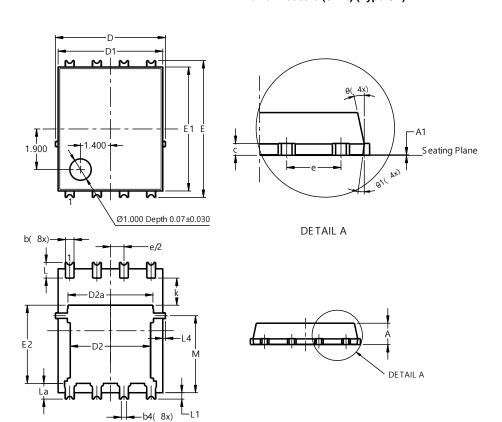
### Site1:



PowerDI5060-8				
Dim	Min	Max	Тур	
Α	0.90	1.10	1.00	
A1	0.00	0.05	-	
b	0.33	0.51	0.41	
b2	0.200	0.350	0.273	
b3	0.40	0.80	0.60	
С	0.230	0.330	0.277	
D	ļ	5.15 BSC		
D1	4.70	5.10	4.90	
D2	3.70	4.10	3.90	
D3	3.90	4.30	4.10	
Е	•	6.15 BSC	•	
E1	5.60	6.00	5.80	
E2	3.28	3.68	3.48	
E3	3.99	4.39	4.19	
е	1.27 BSC			
G	0.51	0.71	0.61	
K	0.51	-	-	
L	0.51	0.71	0.61	
L1	0.100	0.200	0.175	
М	3.235	4.035	3.635	
M1	1.00	1.40	1.21	
Θ	10°	12°	11°	
Θ1	6°	8°	7°	
All Dimensions in mm				

Site2:

PowerDI5060-8 (SWP) (Type UX)



Dim A A1 b	0.230	Max 1.10 0.05 0.50 0.35 0.25REF 0.330	Typ 1.00 0.41 0.25
A1 b	0 0.30 0.20 0.230	0.05 0.50 0.35 0.25REF 0.330	0.41 0.25
b	0.30 0.20 0.230	0.50 0.35 0.25REF 0.330	0.25
	0.20 0.230	0.35 0.25REF 0.330	0.25
b2	0.230	0.25REF 0.330	
	0.230	0.330	
b4		0.000	0.277
С	5	1	
D		.15 BS0	)
D1	4.70	5.10	4.90
D2	3.56	3.96	3.76
D2a	3.78	4.18	3.98
E	6.40 BSC		
E1	5.60	6.00	5.80
E2	3.46	3.86	3.66
E2a	4.195	4.595	4.395
е	1	.27BSC	;
k	1.05		
L	0.635	0.835	0.735
La	0.635	0.835	0.735
L1	0.200	0.400	0.300
L1a	0.050REF		
L4	0.025	0.225	0.125
M	3.205	4.005	3.605
θ	10°	12°	11°
θ1	6°	8°	7°
All Dimensions in mm			

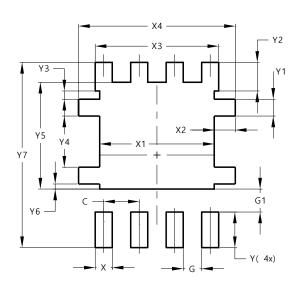


## **Suggested Pad Layout**

Please see http://www.diodes.com/package-outlines.html for the latest version.

### Site1:

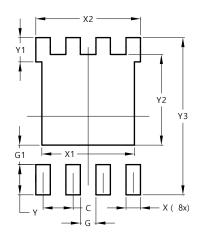
### PowerDI5060-8



Dimensions	Value (in mm)
С	1.270
G	0.660
G1	0.820
X	0.610
X1	4.100
X2	0.755
Х3	4.420
X4	5.610
Y	1.270
Y1	0.600
Y2	1.020
Y3	0.295
Y4	1.825
Y5	3.810
Y6	0.180
Y7	6.610

### Site2:

### PowerDI5060-8 (SWP) (Type UX)



Dimensions	Value (in mm)
С	1.270
G	0.660
G1	0.820
X	0.610
X1	4.100
X2	4.420
Y	1.270
Y1	1.020
Y2	3.810
Y3	6.610

April 2022



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