



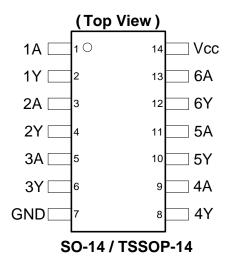
#### **Description**

The 74HCU04 provides provides six independent unbuffered inverters with standard push-pull outputs. The device is designed for operation with a power supply range of 2.0V to 6.0V.

The gates perform the Boolean function:

$$Y = \overline{A}$$

#### **Pin Assignments**



#### **Features**

- Wide Supply Voltage Range from 2.0V to 6.0V
- Sinks or Sources 4mA at V<sub>CC</sub> = 4.5V
- CMOS Low-Power Consumption
- Schmitt Trigger Action at All Inputs
- ESD Protection Exceeds JESD 22
  - 200-V Machine Model (A115-A)
  - 2000-V Human Body Model (A114-A)
  - Exceeds 1000-V Charged Device Model (C101C)
- Range of Package Options SO-14 and TSSOP-14
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

#### **Applications**

- Crystal Oscillators, Analog Inverters
- General Purpose Logic
- Wide array of products, such as:
  - PCs, Networking, Notebooks, Netbooks
  - Computer Peripherals, Hard Drives, CD/DVD ROM
  - TV, DVD, DVR, Set-Top Box

Notes:

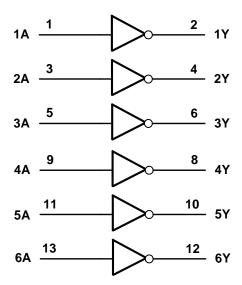
- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.



## **Pin Descriptions**

Pin Number	Pin Name	Function	
1	1A	Data Input	
2	1Y	Data Output	
3	2A	Data Input	
4	2Y	Data Output	
5	3A	Data Input	
6	3Y	Data Output	
7	GND	Ground	
8	4Y	Data Output	
9	4A	Data Input	
10	5Y	Data Output	
11	5A	Data Input	
12	6Y	Data Output	
13	6A	Data Input	
14	Vcc	Supply Voltage	

## **Logic Diagram**



## **Function Table**

Input	Output
A	Υ
Н	L
L	Н



# Absolute Maximum Ratings (Note 4) (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Symbol	Description	Rating	Unit
ESD HBM	Human Body Model ESD Protection	2	KV
ESD CDM	Charged Device Model ESD Protection	1	KV
ESD MM	Machine Model ESD Protection	200	V
Vcc	Supply Voltage Range	-0.5 to +7.0	V
VI	Input Voltage Range (Note 5)	-0.5 to +7.0	V
I <sub>IK</sub>	Input Clamp Current $V_1 < -0.5V$ or $V_1 > V_{CC} +0.5V$	±20	mA
I <sub>OK</sub>	Output Clamp Current $V_O < -0.5V$ or $V_O > V_{CC} +0.5V$	±20	mA
Io	Continuous Output Current -0.5V < V <sub>O</sub> V <sub>CC</sub> +0.5V	±25	mA
Icc	Continuous Current Through Vcc	50	mA
I <sub>GND</sub>	Continuous Current Through GND	-50	mA
TJ	Operating Junction Temperature	-40 to +150	°C
T <sub>STG</sub>	Storage Temperature	-65 to +150	°C
P <sub>TOT</sub>	Total Power Dissipation	500	mW

Notes:

#### Recommended Operating Conditions (Note 6) (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Symbol	Parameter	Conditions	Min	Max	Unit
Vcc	Supply Voltage	_	2.0	6.0	V
VI	Input Voltage	_	0	Vcc	V
Vo	Output Voltage	_	0	V <sub>CC</sub>	V
		$V_{CC} = 2.0V$	_	625	
Δt/ΔV	Input Transition Rise or Fall Rate	$V_{CC} = 4.5V$		140	ns/V
		$V_{CC} = 6.0V$		85	
T <sub>A</sub>	Operating Free-Air Temperature	_	-40	+125	°C

Note:

6. Unused inputs should be held at  $V_{\mbox{\footnotesize CC}}$  or Ground.

<sup>4.</sup> Stresses beyond the absolute maximum may result in immediate failure or reduced reliability. These are stress values and device operation should be within recommend values.

<sup>5.</sup> Input Voltage cannot exceed V<sub>CC</sub> to the extent the Maximum clamp current is exceeded.



## **Electrical Characteristics** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Symbol	Parameter	Test Conditions	V	T <sub>A</sub> = -40°	C to +85°C	$T_A = -40^{\circ}C \text{ to } +125^{\circ}C$		Unit
Cymbol I arameter		rest Conditions	V <sub>CC</sub>	Min	Max	Min	Max	Ollic
	Link Lavallanut	_	2.0V	1.7	_	1.7	_	
$V_{IH}$	High-Level Input Voltage	_	4.5V	3.6	_	3.6	_	V
	Voltago	_	6.0V	4.8	_	4.8	_	
	Low Lovel Input	_	2.0V	_	0.3	_	0.3	
VIL	Low-Level Input Voltage	_	4.5V	_	0.9	_	0.9	V
	Voltago	_	6.0V	_	1.2	_	1.2	
		I <sub>OH</sub> = -20μA	2.0V	1.8		1.9	_	
		$I_{OH} = -20\mu A$	4.5V	4.0	_	4.0	_	
V <sub>OH</sub>	High-Level Output Voltage	$I_{OH} = -20\mu A$	6.0V	5.5	_	5.5	_ \	V
	Vollago	$I_{OH} = -4.0$ mA	4.5V	3.84	_	3.7	_	
		$I_{OH} = -5.2 \text{mA}$	6.0V	5.34		5.2	_	
		I <sub>OL</sub> = 20μA	2.0V	_	0.2	_	0.2	
		$I_{OL} = 20\mu A$	4.5V	_	0.5	_	0.5	
V <sub>OL</sub>	V <sub>OL</sub> Low-Level Output Voltage	$I_{OL} = 20\mu A$	6.0V	_	0.5	_	0.5	V
Voltago	Vollago	$I_{OL} = 4mA$	4.5V	_	0.33	_	0.40	
		$I_{OL} = 5.2 \text{mA}$	6.0V	_	0.33	_	0.40	
l <sub>l</sub>	Input Current	V <sub>I</sub> = GND to 5.5V	6.0V	_	±1	_	±1	μΑ
Icc	Supply Current	$V_I = GND \text{ or } V_{CC}, I_O = 0$	6.0V		20		40	μA

## **Switching Characteristics**

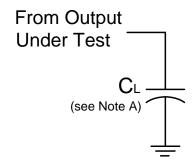
Symbol Barameter		Test	Test Conditions $V_{CC}$ $T_A = +25^{\circ}$ Min Typ		Γ <sub>A</sub> = +25°C	;	-40°C to +85°C	-40°C to +125°C	Unit
Symbol	Symbol Parameter Con				Тур	Max	Max	Max	Oilit
	Decreased as Delevi	Figure 1	2.0V	_	19	70	90	105	
t <sub>PD</sub>		Figure 1 $C_L = 50pF$	4.5V	_	7	14	18	21	ns
	ANTOTA	CL = 50pr	6.0V	_	5	12	15	18	
		Figure 1	2.0V	_	19	75	95	110	
t <sub>t</sub> Transition Time	Figure 1 $C_L = 50pF$	4.5V	_	7	15	19	22	ns	
		CL = 50pr	6.0V	_	6	13	16	19	

## Operating Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Parameter		Test Conditions	V <sub>CC</sub> = 6V Typ	Unit
C <sub>pd</sub>	Power Dissipation Capacitance per Gate	f = 1MHz	10	pF
Cı	Input Capacitance	$V_I = V_{CC} - \text{ or GND}$	4	pF



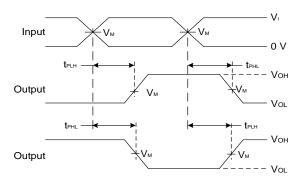
#### **Parameter Measurement Information**



V	Inputs		V	C	
V <sub>CC</sub>	VI	t <sub>r</sub> /t <sub>f</sub>	T V <sub>M</sub>	CL	
2.0V to 6.0V	Vcc	6ns	V <sub>CC</sub> /2	15pF, 50pF	



Voltage Waveform Pulse Duration



Voltage Waveform Propagation Delay Times
Inverting and Non Inverting Outputs

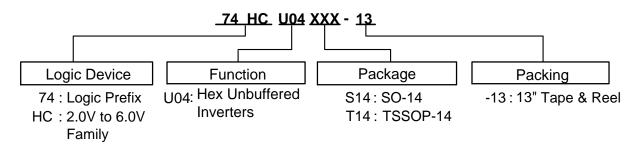
A. Includes test lead and test apparatus capacitance. Notes:

- B. All pulses are supplied at pulse repetition rate ≤ 1 MHz.
  C. Inputs are measured separately one transition per measurement.
- D.  $t_{PLH}$  and  $t_{PHL}$  are the same as  $t_{PD}$ .

Figure 1 Load Circuit and Voltage Waveforms



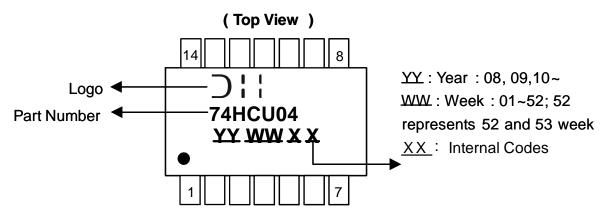
#### **Ordering Information**



Davisa	Doving Package Code		Device Package Code Packaging		7" Tape and Reel		
Device	Package Code	(Note 7)	Quantity	Part Number Suffix			
74HCU04S14-13	S14	SO-14	2500/Tape & Reel	-13			
74HCU04T14-13	T14	TSSOP-14	2500/Tape & Reel	-13			

#### **Marking Information**

#### (1) SO-14, TSSOP-14



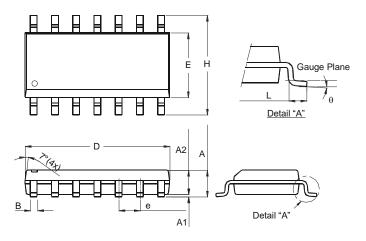
Part Number	Package
74HCU04S14	SO-14
74HCU04T14	TSSOP-14



## Package Outline Dimensions (All dimensions in mm.)

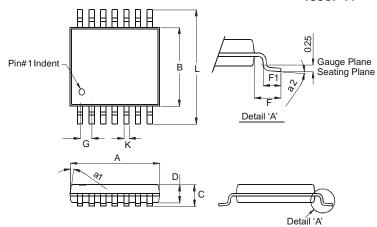
Please see http://www.diodes.com/package-outlines.html for the latest version.





·	00.44					
	SO-14					
Dim	Min	Max				
Α	1.47	1.73				
A1	0.10	0.25				
A2	1.45	Тур				
В	0.33	0.51				
D	8.53	8.74				
Е	3.80	3.99				
е	1.27	Тур				
Н	5.80	6.20				
L	0.38	1.27				
θ	0°	8°				
All Di	All Dimensions in mm					



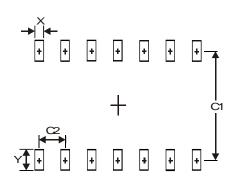


	TSSOP-14					
Dim	Min Max					
a1	7° (	4X)				
a2	0°	8°				
Α	4.9	5.10				
В	4.30	4.50				
С	-	1.2				
D	0.8	1.05				
F	1.00	Тур				
F1	0.45	0.75				
G	0.65	Тур				
K	0.19	0.30				
Ĺ	6.40	Тур				
All Dimensions in mm						



## **Suggested Pad Layout**

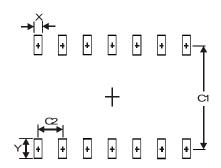
Please see http://www.diodes.com/package-outlines.html for the latest version.



**SO-14** 

Dimensions	Value (in mm)
Х	0.60
Y	1.50
C1	5.4
C2	1.27

TSSOP-14



Dimensions	Value (in mm)
Х	0.45
Y	1.45
C1	5.9
C2	0.65



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