



DUAL N-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

BV _{DSS}	Rds(ON) Max	I _D Max T _A = +25°C
60V	3.0Ω @ V _{GS} = 10V	261mA
607	4.0Ω @ V _{GS} = 4.5V	226mA

Description and Applications

This MOSFET is designed to minimize the on-state resistance (R_{DS(ON)}) yet maintain superior switching performance, making it ideal for high efficiency power management applications.

- Motor Control
- Power Management Functions

Features and Benefits

- Dual N-Channel MOSFET
- Low On-Resistance
- Low Gate Threshold Voltage
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- ESD Protected
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please <u>contact us</u> or your local Diodes representative. https://www.diodes.com/quality/product-definitions/

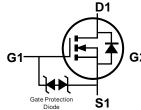
Mechanical Data

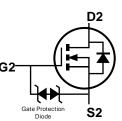
- Package: SOT363
- Package Material: Molded Plastic. "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Matte Tin Finish Annealed over Alloy 42 Leadframe (Lead-Free Plating). Solderable per MIL-STD-202, Method 208 (3)
- Terminal Connections: See Diagram
- Weight: 0.006 grams (Approximate)

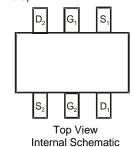




Top View







Ordering Information (Note 4)

Part Number	Pankaga	Packing		
Fait Nulliber	Package	Qty.	Carrier	
2N7002DWK-7	SOT363	3,000	Tape & Reel	
2N7002DWK-13	SOT363	10,000	Tape & Reel	

Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.

2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free

Equivalent Circuit

- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information

DWK = Product Type Marking Code

 $\overline{Y}M$ = Date Code Marking \overline{Y} = Year (ex: I = 2021)

M = Month (ex: 9 = September)

Date Code Key

Year	2020	2021	2022	2023	2024	2025	2026	2027	2028	2029	2030	2031
Code	Н	- 1	J	K	L	М	N	0	Р	R	S	T
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec



Maximum Ratings (@ $T_A = +25$ °C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit	
Drain-Source Voltage		VDSS	60	V	
Gate-Source Voltage	V_{GSS}	±20	V		
Continuous Drain Current (Note 6) V _{GS} = 10V	lo	261 208	mA		
Maximum Continuous Body Diode Forward Current	(Note 6)	Is	261	mA	
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%	6)	I _{DM}	1.1	Α	
Pulsed Source Current (10µs Pulse, Duty Cycle = 1	l%)		Ism	1.1	Α

Thermal Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 5)		P_{D}	0.33	W
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	Reja	379	°C/W
Total Power Dissipation (Note 6)		P_{D}	0.45	W
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	R _{0JA}	278	°C/W
Operating and Storage Temperature Range		TJ, TSTG	-55 to +150	°C

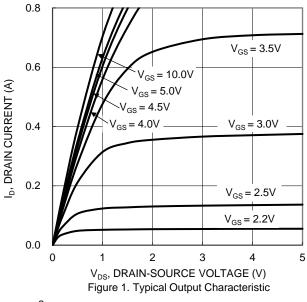
Electrical Characteristics (@TA = +25°C, unless otherwise specified.)

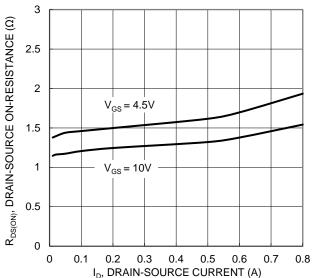
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition		
OFF CHARACTERISTICS (Note 7)								
Drain-Source Breakdown Voltage	BV _{DSS}	60		_	V	V _G S = 0V, I _D = 250μA		
Zero Gate Voltage Drain Current	I _{DSS}	_	_	1	μΑ	V _{DS} = 60V, V _{GS} = 0V		
Gate-Source Leakage	I _{GSS}	_	_	±10	μΑ	$V_{GS} = \pm 20V, V_{DS} = 0V$		
ON CHARACTERISTICS (Note 7)								
Gate Threshold Voltage	V _{GS(TH)}	1.0		2.0	٧	$V_{DS} = V_{GS}$, $I_D = 250\mu A$		
Static Drain-Source On-Resistance	R _{DS(ON)}		1.3 1.5	3.0 4.0	Ω	$V_{GS} = 10V, I_D = 200mA$ $V_{GS} = 4.5V, I_D = 150mA$		
Diode Forward Voltage	VsD	_	0.8	1.4	V	V _G S = 0V, I _S = 115mA		
DYNAMIC CHARACTERISTICS (Note 8)	•							
Input Capacitance	Ciss	1	41		pF			
Output Capacitance	Coss		4.5	_	pF	$V_{DS} = 30V, V_{GS} = 0V$ f = 1.0MHz		
Reverse Transfer Capacitance	Crss		2.7	_	pF	1 - 1.000112		
Gate Resistance	Rg		224	_	Ω	$f = 1MHz$, $V_{GS} = 0V$, $V_{DS} = 0V$		
Total Gate Charge (VGS = 4.5V)	Qg		0.51	_	nC			
Total Gate Charge (VGS = 10V)	Qg		1.04	_	nC	V _{DS} = 15V,		
Gate-Source Charge	Qgs		0.16	_	nC	$I_D = 200 \text{mA}$		
Gate-Drain Charge	Q_{gd}		0.18	_	nC			
Turn-On Delay Time	t _D (ON)	_	6.9	_	ns			
Turn-On Rise Time	t _R	_	5.8	_	ns	V _{DD} = 30V, V _{GS} = 10V,		
Turn-Off Delay Time	tD(OFF)		37.8	_	ns	$R_G = 150\Omega$, $I_D = 200mA$		
Turn-Off Fall Time	t _F	_	14.3	_	ns			
Reverse Recovery Time	t _{RR}	_	19	_	ns	I _F = 1A, di/dt = 100A/μs		
Reverse Recovery Charge	QRR	_	9	_	nC	I _F = 1A, di/dt = 100A/μs		

 Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.
Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.
Short duration pulse test used to minimize self-heating effect. Notes:

^{8.} Guaranteed by design. Not subject to product testing.







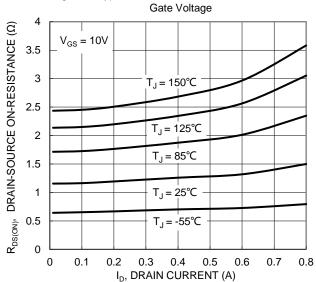
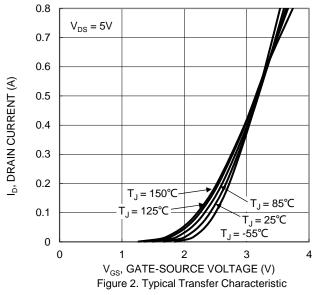
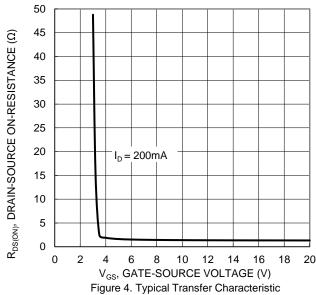


Figure 3. Typical On-Resistance vs. Drain Current and

Figure 5. Typical On-Resistance vs. Drain Current and Junction Temperature





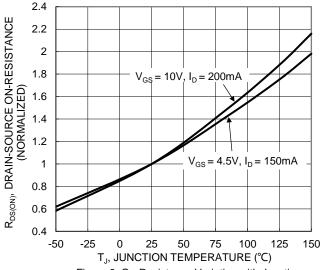


Figure 6. On-Resistance Variation with Junction Temperature



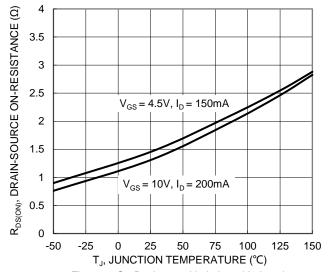
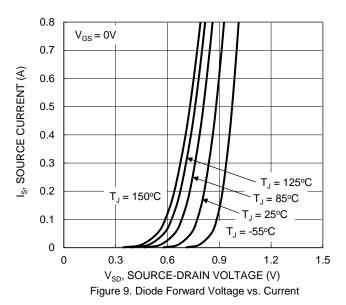
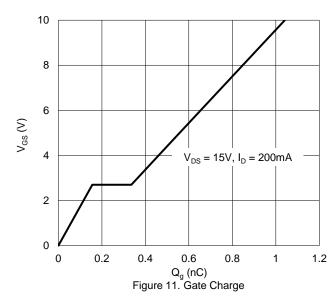
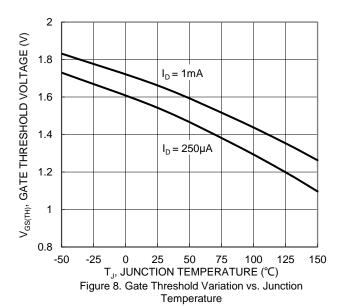
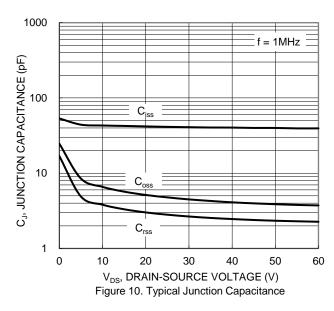


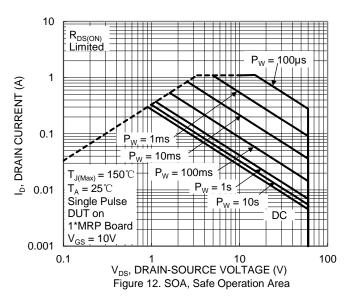
Figure 7. On-Resistance Variation with Junction Temperature













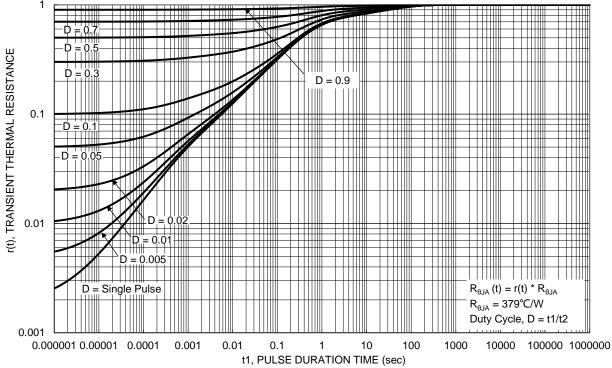


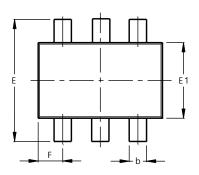
Figure 13. Transient Thermal Resistance

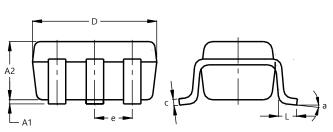


Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.





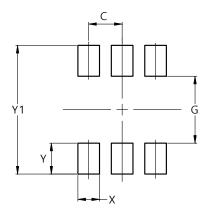


SOT363						
Dim	Min	Max	Тур			
A1	0.00	0.10	0.05			
A2	0.90	1.00	0.95			
b	0.10	0.30	0.25			
С	0.10	0.22	0.11			
D	1.80	2.20	2.15			
Е	2.00	2.20	2.10			
E1	1.15	1.35	1.30			
e	0.650 BSC					
F	0.40	0.45	0.425			
L	0.25	0.40	0.30			
а	0°	8°				
All Dimensions in mm						

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT363



Dimensions	Value (in mm)
C	0.650
G	1.300
X	0.420
Y	0.600
Y1	2.500



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