**Verification of PI6CG18801 IBIS model**

1. **Introduction: to verify the correlation between the ibis model and hspice model, we need to do some simulations:**

**The frequency of signal is 2.5MHz:**

vin sda\_in 0 pulse(0 clamp dly 1n 1n 199n 400n)

1. **Add 10k Ohm pull-up resistor to the SDATA\_IO:**

PI6CG18801

**VOUT**

**SCL\_C**

**SDA\_C**

**Input Signals**

**SCL\_C**

**SDA\_C**

**VIN**

**SCL\_C**

**SDA\_C**

SDA\_IO

**SCL\_C**

**SDA\_C**

SDA\_IN

**SCL\_C**

**SDA\_C**

**…..**

**SCL\_C**

**SDA\_C**

3.3V

**SCL\_C**

**SDA\_C**

R=10kOhm

**SCL\_C**

**SDA\_C**

1. Simulation **without** package data;
2. Simulation **with** package data.
3. **Conclusion:**

For the verification, the simulation results of IBIS model can match very well with the HSPICE model at different simulating conditions.

1. **Simulation Result:**
2. **Add 10k Ohm pull-up resistor to the SDATA\_IO:**
3. Simulation **without** package data;



1. Simulation **with** package data.

