



#### N-CHANNEL ENHANCEMENT MODE MOSFET

## **Product Summary**

V <sub>(BR)DSS</sub>	R <sub>DS(ON)</sub> Max	I <sub>D</sub> Max T <sub>A</sub> = +25°C
	$25m\Omega$ @ $V_{GS} = 4.5V$	6.8A
20V	$29m\Omega$ @ $V_{GS} = 2.5V$	5.5A

## **Description and Applications**

This MOSFET is designed to minimize the on-state resistance (R<sub>DS(ON)</sub>) yet maintain superior switching performance, which make it ideal for high-efficiency power management applications.

- Backlighting
- **Power-Management Functions**
- **DC-DC Converters**
- Motor Control

## **Features and Benefits**

- Low On-Resistance
- Low-Input Capacitance
- Fast Switching Speed
- **ESD Protected Gate**
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

### **Mechanical Data**

- Case: SOT23
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish—Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 (3)
- Terminals Connections: See Diagram Below
- Weight: 0.009 grams (Approximate)



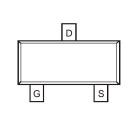


Top View



Internal Schematic

Gate Protection



Top View

# Ordering Information (Note 4)

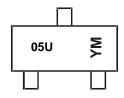
Part Number	Case	Packaging
DMN2024U-7	SOT23	3000/Tape & Reel
DMN2024U-13	SOT23	10,000/Tape & Reel

G

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

## **Marking Information**



05U = Product Type Marking Code YM = Date Code Marking Y or  $\overline{Y}$ = Year (ex: F = 2018) M = Month (ex: 9 = September)

Date Code Key

Year	2018		2019	2020		2021	2022		2023	2024		2025
Code	F		G	Н			J		K	L		M
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D



## **Maximum Ratings** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Units
Drain-Source Voltage		$V_{DSS}$	20	V
Gate-Source Voltage		$V_{GSS}$	±10	V
Continuous Drain Current (Note 6) V <sub>GS</sub> = 4.5V	Ι <sub>D</sub>	6.8 5.5	А	
Maximum Continuous Body Diode Forward Curre	ent (Note 6)	Is	2.2	Α
Pulsed Drain Current (10µs pulse, duty cycle = 1	%)	I <sub>DM</sub>	45	А

## **Thermal Characteristics**

Characteristic		Symbol	Value	Units
Total Power Dissipation (Note 5)		$P_{D}$	0.8	W
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	$R_{\Theta JA}$	159	°C/W
Total Power Dissipation (Note 6)		P <sub>D</sub>	1.4	W
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	Reja	92	°C/W
Operating and Storage Temperature Range		T <sub>J,</sub> T <sub>STG</sub>	-55 to +150	°C

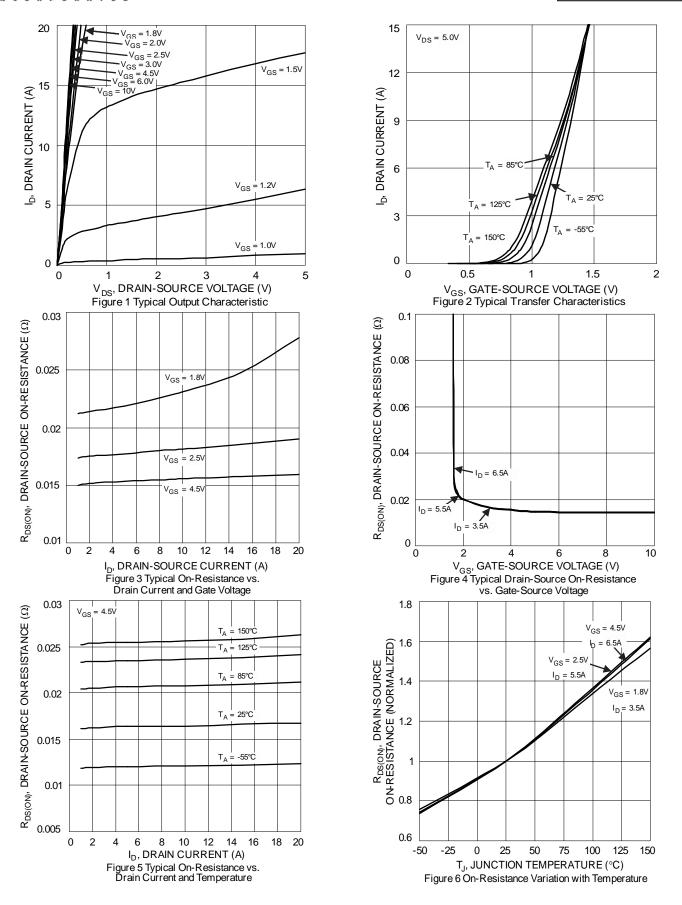
# Electrical Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition		
OFF CHARACTERISTICS (Note 7)								
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	20	1		V	$V_{GS} = 0V, I_D = 250\mu A$		
Zero Gate Voltage Drain Current T <sub>J</sub> = +25°C	I <sub>DSS</sub>	_	1	1.0	μA	$V_{DS} = 20V$ , $V_{GS} = 0V$		
Gate-Source Leakage	I <sub>GSS</sub>	_	1	±10	μΑ	$V_{GS} = \pm 8V$ , $V_{DS} = 0V$		
ON CHARACTERISTICS (Note 7)								
Gate Threshold Voltage	V <sub>GS(TH)</sub>	0.5	-	0.9	V	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$		
			16	25		$V_{GS} = 4.5V, I_D = 6.5A$		
Static Drain-Source On-Resistance	R <sub>DS(ON)</sub>	_	18.5	29	mΩ	$V_{GS} = 2.5V, I_D = 5.5A$		
			23	36		$V_{GS} = 1.8V, I_D = 3.5A$		
Diode Forward Voltage	$V_{SD}$	_	0.8	1.2	V	$V_{GS} = 0V$ , $I_D = 5A$		
DYNAMIC CHARACTERISTICS (Note 8)								
Input Capacitance	C <sub>iss</sub>	_	647		pF	1, , ,,,,		
Output Capacitance	Coss	_	78		pF	$V_{DS} = 10V, V_{GS} = 0V$ -f = 1.0MHz		
Reverse Transfer Capacitance	Crss	_	38		pF	1 – 1.600112		
Gate Resistance	$R_g$	_	628		Ω	$V_{DS} = 0V$ , $V_{GS} = 0V$ , $f = 1MHz$		
Total Gate Charge	$Q_g$	_	7.1		nC			
Gate-Source Charge	$Q_{gs}$	_	0.9		nC	$V_{GS} = 4.5V, V_{DS} = 10V, I_D = 6.5A$		
Gate-Drain Charge	$Q_{gd}$	_	0.7		nC			
Turn-On Delay Time	t <sub>D(ON)</sub>	_	98	ı	ns			
Turn-On Rise Time	t <sub>R</sub>	_	140	ı	ns	$V_{DS} = 10V, V_{GS} = 4.5V,$		
Turn-Off Delay Time	t <sub>D(OFF)</sub>	_	1024		ns	$R_L = 10\Omega$ , $R_G = 6\Omega$ , $I_D = 1A$		
Turn-Off Fall Time	t <sub>F</sub>	_	434	_	ns			
Reverse Recovery Time	t <sub>RR</sub>	_	245	_	ns	$I_F = 1.0A$ , $di/dt = 100A/\mu s$		
Reverse Recovery Charge	$Q_{RR}$		149		nC	$I_F = 1.0A$ , $di/dt = 100A/\mu s$		

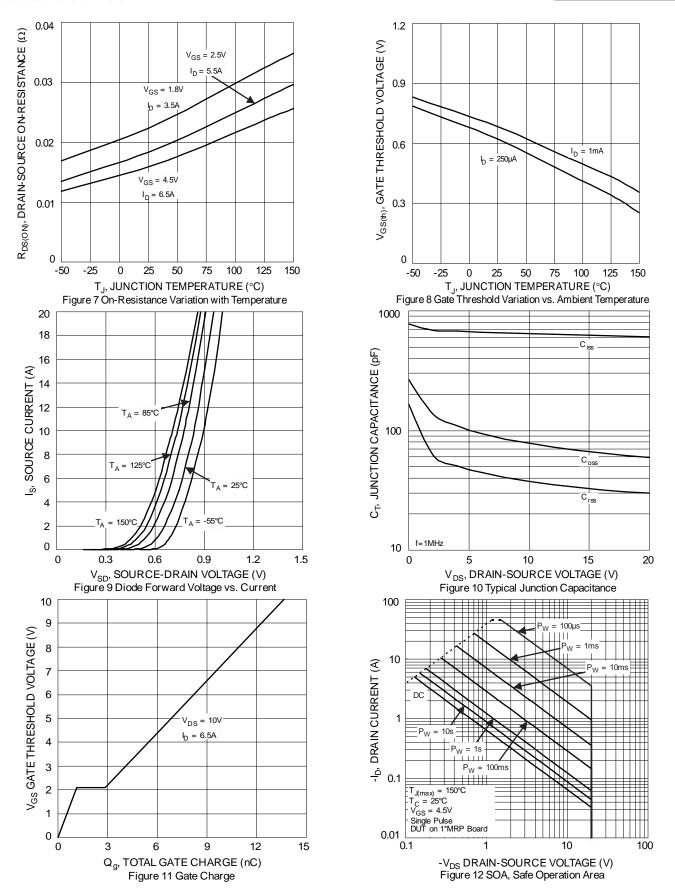
Notes:

- 5. Device mounted on FR-4 PCB with minimum recommended pad layout.
  6. Device mounted on 1" x 1" FR-4 PCB with high-coverage 2oz copper, single sided.
- 7. Short duration pulse test used to minimize self-heating effect.
- 8. Guaranteed by design. Not subject to product testing.

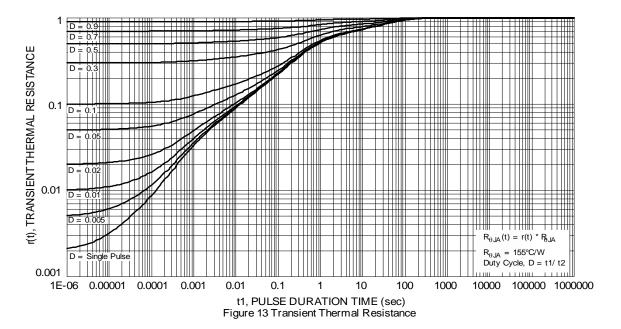








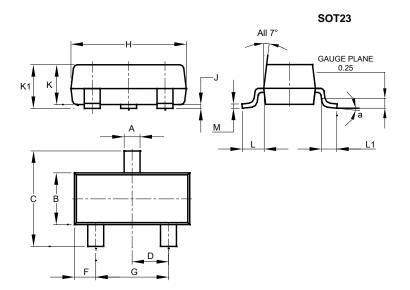






# **Package Outline Dimensions**

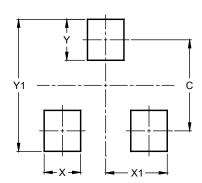
Please see http://www.diodes.com/package-outlines.html for the latest version.



	SOT23								
Dim	Min	Max	Тур						
Α	0.37	0.51	0.40						
В	1.20	1.40	1.30						
С	2.30	2.50	2.40						
D	0.89	1.03	0.915						
F	0.45	0.60	0.535						
G	1.78	2.05	1.83						
Н	2.80	3.00	2.90						
J	0.013	0.10	0.05						
K	0.890	1.00	0.975						
K1	0.903	1.10	1.025						
L	0.45	0.61	0.55						
L1	0.25	0.55	0.40						
M	0.085	0.150	0.110						
а	0°	8°							
All	All Dimensions in mm								

# **Suggested Pad Layout**

Please see http://www.diodes.com/package-outlines.html for the latest version.



### SOT23

Dimensions	Value (in mm)
С	2.0
Х	0.8
X1	1.35
Y	0.9
Y1	2.9



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